

1122-1124 - US-59 (Eastex Frwy) @ Northpark

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Control Table Info

Description	US 59 @ Northpark
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	jclark
Modified On	11/02/10 14:46:44

NextPhase 1.7.1 Control Params

Control Mode	scheduler
Manual Plan	3: PM Peak
Free Plan	1: Off Peak
Default Schedule	1: Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Schedule	guest	04/15/2002 12:54:24	unknown	05/18/10 14:09:47

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	1
2	X	5	30		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	Holiday
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	Turner
Modified On	06/12/06 14:39:10

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Schedule	
2		1	1								1: Schedule	
3		1	1								1: Schedule	
4		1	1								1: Schedule	
5		1	1								1: Schedule	
6		1	1								1: Schedule	
7		1	1								1: Schedule	
8		1	1								1: Schedule	
9		1	1								1: Schedule	
10		1	1								1: Schedule	
11		1	1								1: Schedule	
12		1	1								1: Schedule	
13		1	1								1: Schedule	
14		1	1								1: Schedule	
15		1	1								1: Schedule	
16		1	1								1: Schedule	
17		1	1								1: Schedule	
18		1	1								1: Schedule	
19		1	1								1: Schedule	
20		1	1								1: Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Schedule	
2		1	1								1: Schedule	
3		1	1								1: Schedule	
4		1	1								1: Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Schedule	
6		1	1								1: Schedule	
7		1	1								1: Schedule	
8		1	1								1: Schedule	
9		1	1								1: Schedule	
10		1	1								1: Schedule	
11		1	1								1: Schedule	
12		1	1								1: Schedule	
13		1	1								1: Schedule	
14		1	1								1: Schedule	
15		1	1								1: Schedule	
16		1	1								1: Schedule	
17		1	1								1: Schedule	
18		1	1								1: Schedule	
19		1	1								1: Schedule	
20		1	1								1: Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 20:39:24	aescobar	09/12/07 17:33:50
2	AM Peak	Unknown	10/20/2000 11:18:22	unknown	05/20/10 07:58:02
3	PM Peak	Unknown	06/21/2001 15:17:00	unknown	05/19/10 16:38:31
4	Midday	unknown	08/19/04 09:28:03	unknown	05/05/09 09:54:39

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	10																X										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	15	7	20	10	15	7	0	0	0	10	0	0	0	20
CordNom	27	20	7	29	13	27	7	0	0	0	10	0	0	0	29
CordMax	30	30	7	40	20	30	7	0	0	0	20	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	15	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	7	0	17	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	20	0	30	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	65	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	10																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X					X										X				
Hold	X					X										X				

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	40	10	7	25	5	20	7	5	0	0	10	0	0	0	25
CordNom	74	15	7	30	8	27	7	8	0	0	11	0	0	0	30
CordMax	80	20	7	50	10	40	7	10	0	0	20	0	0	0	50
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	30	0	7	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	54	0	10	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	60	0	15	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	140	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	10																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	15	7	15	10	15	7	15	0	0	35	0	0	0	15
CordNom	37	22	7	20	15	25	7	22	0	0	57	0	0	0	20
CordMax	40	30	7	30	20	30	7	30	0	0	70	0	0	0	30
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	15	0	25	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	19	0	42	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	30	0	50	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	10																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	15	7	20	10	15	7	10	0	0	10	0	0	0	20
CordNom	29	24	7	30	17	27	7	17	0	0	16	0	0	0	30
CordMax	40	30	7	40	20	30	7	20	0	0	20	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	9	0	10	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	9	0	13	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	18	0	20	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Diamond	GTSI	08/20/99 09:36:34	Turner	06/12/06 14:40:58

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	OVERLAP	GTSI	01/17/00 18:35:32	aescobar	09/12/07 15:18:32

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

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Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	1	1	5	5	5	5	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.6	3.6	4.7	4.7	3.6	3.6	3.6	3.6	4.7	0.0
RedClear	1.7	1.7	2.0	2.0	1.7	1.7	1.7	1.7	2.0	0.0
Walk	0	0	5	0	5	0	0	0	0	0
PedClear	0	0	11	0	14	0	0	0	0	0
Reservice	5	5	5	5	5	5	5	5	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Diamond Phase Data	GTSI	01/17/00 20:39:24	unknown	07/26/10 19:27:39

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl	X															X				
MaxRcl									X	X		X	X	X			X		X	X
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt					X						X				X			X		
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X		X		X		X												
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	7	1	5	3	5	1	1	0	0	3	0	0	0	3
Passage	1.5	2.5	0.5	2.5	1.5	2.0	0.5	2.0	0.0	0.0	1.5	0.0	0.0	0.0	0.5
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	50	40	1	35	30	11	1	22	0	0	40	0	0	0	35
Max2	45	30	1	25	20	25	1	22	0	0	55	0	0	0	25
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.6	3.6	4.7	4.7	3.6	3.6	4.7	4.7	0.0	0.0	3.6	0.0	0.0	0.0	3.6
RedClr	1.7	1.7	2.0	2.0	1.7	1.7	2.0	2.0	0.0	0.0	1.7	0.0	0.0	0.0	1.7
Walk	0	0	0	0	0	5	0	7	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	16	0	8	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	4	0	3	0	0	1	0	0	0	0	0	0	0	0	0
Passage	2.5	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	45	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Max2	55	0	40	0	0	1	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.6	0.0	4.7	0.0	0.0	3.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	1.7	0.0	2.0	0.0	0.0	1.7	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	External Plans
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	Turner
Modified On	06/27/06 13:58:00

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Revert to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
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99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
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131										ExtPlan	0	
132										ExtPlan	0	
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141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
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191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
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229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Prioritor	trf_ctrl	02/16/05 08:34:45	unknown	09/27/06 10:43:51

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Configuration	GTSI	01/17/00 20:39:23	Turner	06/13/06 13:05:27

Active Config Table 1 Params

Phase	1: Phase Configuration
Overlap	1: 4 Phase Diamond Overlaps
Preempt	1: PREEMPTION [FIRE]
Cabinet	1: input settings/output settings
Detector	1: DETECTOR
Coordination	1: Coord Parameters
Peer	1: Table 01

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Configuration	GTSI	01/17/00 18:35:33	aescobar	09/05/07 09:24:14

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	sbt	sbt	ebf	wbt	nbt	nbt	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup	X					X														
YelStart	X					X														
RedStart																				
SecStart																				
Ped						X		X												
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,4,b,3,1,c,21,11,d
2	7,5,a,15,b,6,16,c,8,18,d
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	DETECTOR	GTSI	08/24/99 16:13:16	aescobar	09/12/07 17:31:28

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X		X		X		X		X	X	X	X											
Call	X		X		X		X		X	X	X	X											
Extend	X		X		X		X		X	X	X	X											
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable									X	X	X	X	X	X	X	X	X	X
Call									X	X	X	X	X	X	X	X	X	X
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	0	4	0	16	0	11	0	5	5	1	1	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	3	7	18	5	6	21
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	1	5	6	21
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	5	3	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

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Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	unknown	09/12/07 18:09:23

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

NextEdit

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Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	Preempt	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	NAnd	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	None	None	None	None	None	None
Pin	52	10	11	12	13	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None
FctnIndx	0	0	0	0	2	3	4	5	1	0	0
Device	None	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None
Pin	None	None	None	None	71	73	72	74	54	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	Veh	Veh	Veh	Veh	Veh
FctnIndx	0	0	0	0	0	0	55	56	57	58	59
Device	None	None	None	None	None	None	Virt	Virt	None	None	Virt
Pin	None	None	None	None	None	None	4	11	None	None	4
Chan1	0	0	0	0	0	0	0	0	7	1	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	Veh	Veh	Veh	None	Omit	Veh	Veh
FctnIndx	60	61	62	0	18	63	64
Device	None	Virt	Virt	Virt	Virt	Virt	Virt
Pin	None	3	7	28	18	4	8
Chan1	128	0	0	0	125	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

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Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovlped	ovlped	ped
FctnIndx	7	5	1	3	8	6	2	4	5	3	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	detrst	fl/wd	none	none	none	none	none	none	none
FctnIndx	8	9	1	1	0	0	0	0	0	0	0
Device1	C1S	None	C1S	C1S	None	None	None	None	None	None	None
Pin1	19	None	102	103	None	None	None	None	None	None	None
Device2	None	C1S	None	None	None	None	None	None	None	None	None
Pin2	None	38	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	None	None	None	None	None	None	None	None	None
Pin3	20	36	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	phson	phson	phson	aux	phson	phson	check
FctnIndx	11	3	7	8	8	4	8
Device1	Virt	Virt	Virt	Virt	Virt	Virt	Virt
Pin1	11	3	7	28	18	4	8
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

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Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	PREEMPTION [FIRE]	GTSI	01/10/00 07:53:31	aescobar	09/05/07 09:17:28

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell											X									
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X											X					
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	5	5	5	5	0	0	0	0	0
DwellExt	0	2	2	8	8	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	85	85	85	85	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	4 Phase Diamond Overlaps	GTSI	01/17/00 18:35:32	aescobar	09/12/07 15:08:43

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X									X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X									X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X	X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X				X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X																		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X										X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X									X		X		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X	X	X	X											
Ped			X		X															
YelFlash																				
AltFlsHz	X	X			X	X	X	X												
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coord Parameters	guest	04/15/2002 12:54:44	aescobar	09/05/07 09:15:48

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	0	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	guest	06/20/2003 11:52:34	guest	06/20/2003 11:52:34

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Northpark @ Woodland Hills
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	06/25/10 10:24:05

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	2: 144" AM Peak
Free Plan	1: 100" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 13:19:01

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTS	09/08/04 20:13:20	aescoba	06/21/10 08:15:09
2	144" AM Peak	awrigley	12/06/05 15:40:08	unknown	12/02/10 11:07:13
3	150" PM Peak	awrigley	12/06/05 15:40:11	unknown	04/07/11 16:52:16
4	120" Midday	awrigley	12/06/05 15:40:13	unknown	04/07/11 17:04:31

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max1	seconds	1:	1: Off Peak (Lag 1)	1:	1:	1:	100

Plan Table 1 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
30																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	10	7	15	25	10	0	0	0	0	0	0	0	0
CordNom	20	40	15	10	20	40	15	0	0	0	0	0	0	0	0
CordMax	30	50	20	15	30	50	20	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	25	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	50	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	30	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1:	2: AM Peak(Lag 3 & 5)	1:	1:	1:	144

Plan Table 2 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
30																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	25	25	20	10	15	35	15	0	0	0	0	0	0	0	0
CordNom	45	35	26	14	20	60	24	0	0	0	0	0	0	0	0
CordMax	50	50	40	20	30	70	30	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	64	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	80	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1:	3: PM (Lag 1)	1:	1:	1:	150

Plan Table 3 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
30																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	45	20	7	20	40	15	0	0	0	0	0	0	0	0
CordNom	20	80	20	10	30	70	20	0	0	0	0	0	0	0	0
CordMax	30	90	40	15	40	80	30	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	30	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	50	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	120	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1:	4: Midday Lag 5	1:	1:	1:	120

Plan Table 4 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
30																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	15	7	15	25	15	0	0	0	0	0	0	0	0
CordNom	25	40	25	10	20	45	20	0	0	0	0	0	0	0	0
CordMax	30	50	30	15	30	50	30	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	30	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	55	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak (Lag 1)	GTSI	08/20/99 09:36:34	aescoba	04/05/10 11:55:32
2	AM Peak(Lag 3 & 5)	awrigley	12/06/05 15:42:38	aescoba	04/05/10 11:56:33
3	PM (Lag 1)	awrigley	12/06/05 15:42:41	unknown	04/07/11 17:03:53
4	Midday Lag 5	unknown	04/07/11 17:02:48	unknown	04/07/11 17:04:07

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	2	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	unknown	11/18/10 07:11:44

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	4	0	0	0	4	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
RedClear	0.0	0.0	0.0	2.0	0.0	0.0	0.0	2.0	0.0	0.0
Walk	0	0	0	4	0	0	0	4	0	0
PedClear	0	0	0	10	0	0	0	10	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

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Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:24	unknown	04/07/11 16:10:58

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl								X	X	X	X	X	X	X	X	X	X		X	X
MaxRcl		X				X		X	X	X										
SftRcl																				
ConSvc																				
PedRcl	X		X	X	X		X	X	X	X										
DualEnt		X		X		X														
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X	X	X	X	X	X	X	X												

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	10	5	4	5	10	4	0	0	0	0	0	0	0	0
Passage	2.0	3.2	2.0	2.0	2.0	3.2	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	50	15	30	25	50	20	0	0	0	0	0	0	0	0
Max2	45	90	45	35	40	80	30	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	4	0	4	0	4	0	0	0	0	0	0	0	0	0
PedClr	0	16	0	10	0	16	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	8	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	35	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	65	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTS	09/10/04 14:08:44	GTS	09/10/04 14:08:45

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:23	unknown	01/17/00 21:10:34

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Active Config Table 1 Params

Phase	1:
Overlap	1:
Preempt	1:
Cabinet	1: COH ITS CABINET [SERIAL]
Detector	1:
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:33	unknown	06/24/10 08:49:32

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X				X														
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

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Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,7,4,3,b
2	5,6,a,18,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTS	09/08/04 20:13:18	unknown	08/25/10 09:28:26

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Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X			X	X	X	X	X	X			X	X	X	X	X	X	
Call	X	X	X	X	X	X			X	X	X	X	X	X			X	X	X	X	X	X	
Extend	X	X	X	X	X	X			X	X	X	X	X	X			X	X	X	X	X	X	
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable		X	X	X	X	X	X																
Call		X	X	X	X	X	X																
Extend		X	X	X	X	X	X																
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																	X	
Call																	X	
Extend																	X	
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	2	2	2	2	2	2	3	3	4	4	4	4	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	4	5	5	6	6	6	6	6	6	7	7	4	4	4	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	4	4	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	18
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X		X		X		X															

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Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	3	0	5	6	7	0	0	0	0	0	0	0	0
Overlap	0	0	0	4	0	0	0	8	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH ITS CABINET [SERIAL]	GTS	09/08/04 20:13:17	unknown	12/19/10 08:25:48

Cabinet Table 1 Options

Cabinet Type	ITS_CBD
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1
Pin	6	7	8	9	10	11	12	13	14	15	16
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2	IN2	IN2
Pin	17	18	19	20	21	6	7	8	9	10	11
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped
FctnIndx	23	24	25	26	27	28	29	30	31	32	2
Device	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN1
Pin	12	13	14	15	16	17	18	19	20	21	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel	TimSet	Prempt	Prempt	Prempt
FctnIndx	4	6	8	1	2	3	4	1	2	3	4
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2
Pin	27	28	29	22	23	24	25	28	24	25	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Prempt	Prempt	MCE	Advanc	StopTm	Flash	Start	None	None	None	None
FctnIndx	5	6	1	1	1	1	1	0	0	0	0
Device	IN2	IN2	OUT1	OUT1	OUT1	OUT1	OUT1	None	None	None	None
Pin	27	29	Opto1	Opto2	Opto3	Opto4	Opto4	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	NAnd	And	And	Or	NAnd	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	None	None	None	None
FctnIndx	0	0	0	2	3	4	5	0	0	0	0
Device	None	None	None	IN2	IN2	IN2	IN2	None	None	None	None
Pin	None	None	None	24	25	26	27	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	Veh
FctnIndx	0	0	0	0	0	0	64
Device	None	None	None	None	None	None	Virt
Pin	None	None	None	None	None	None	18
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	ped	phase	ovl	ovlped	phase	phase	ped	phase	ovl
FctnIndx	1	2	2	3	4	4	5	6	6	7	8
Device1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin1	0	3	6	9	12	15	18	21	24	27	30
Device2	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin2	1	4	7	10	13	16	19	22	25	28	31
Device3	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin3	2	5	8	11	14	17	20	23	26	29	32

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovlped	none	none	ovl	ovl	ovl	ovl	none	none	none	none
FctnIndx	8	0	0	1	2	3	4	0	0	0	0
Device1	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin1	33	36	39	0	3	6	9	12	15	None	None
Device2	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin2	34	37	40	1	4	7	10	13	16	None	None
Device3	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin3	35	38	41	2	5	8	11	14	17	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	phsnxt	phsnxt	phsnxt
FctnIndx	0	0	0	0	3	4	7
Device1	None	None	None	None	Virt	Virt	Virt
Pin1	None	None	None	None	18	18	18
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	14Pak	Off	6Pak	Off	unused
Det BIU/In SIU	On	On	Off	Off	Off

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1								X	X						X	
2			X				X	X	X							
3							X	X	X							
4											X	X				
5						X				X	X	X			X	
6										X	X	X			X	

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
7																
8									X							
9																
10															X	
11												X				
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/10/00 07:53:31	aescoba	06/21/10 10:12:01

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X																	
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell							X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	unknown	06/24/10 10:27:46

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X			X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X	X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X			X				X												
Ped				X				X												
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	3	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	unknown	02/14/06 09:24:52

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	0	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								2

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Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:50:46
Modified By	rreevey
Modified On	03/19/14 15:50:46

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:50:46
Modified By	rreevey
Modified On	03/19/14 15:50:46

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:50:46
Modified By	rreevey
Modified On	03/19/14 15:50:46

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:50:46
Modified By	rreevey
Modified On	03/19/14 15:50:46

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	US 59 at Kingwood Dr
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	05/19/08 17:36:06

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: Off Peak
Free Plan	1: Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	guest	04/15/2002 12:54:24	unknown	04/06/11 09:45:34

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	Holiday
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	Turner
Modified On	06/12/06 14:39:10

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 20:39:24	aescobar	09/12/07 15:36:21
2	AM Peak	Unknown	10/20/2000 11:18:22	unknown	03/31/11 18:00:50
3	PM Peak	Unknown	06/21/2001 15:17:00	unknown	03/30/11 18:29:45
4	Midday	unknown	08/19/04 09:28:03	unknown	10/01/08 14:39:57

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	10																X										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	15	7	20	10	15	7	0	0	0	10	0	0	0	20
CordNom	27	20	7	29	13	27	7	0	0	0	10	0	0	0	29
CordMax	30	30	7	40	20	30	7	0	0	0	20	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	15	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	7	0	17	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	20	0	30	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	10																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	25	15	7	25	10	15	7	15	0	0	15	0	0	0	25
CordNom	43	20	7	44	13	27	7	17	0	0	23	0	0	0	44
CordMax	50	30	7	50	20	30	7	30	0	0	30	0	0	0	50
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	15	0	10	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	23	0	13	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	30	0	20	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	95	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	10																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	15	7	20	10	15	7	10	0	0	25	0	0	0	10
CordNom	32	22	7	35	15	27	7	17	0	0	47	0	0	0	35
CordMax	40	30	7	40	20	30	7	20	0	0	50	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	10	0	20	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	12	0	37	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	20	0	40	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	105	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Diamond Phase Data	1: Diamond	1: OVERLAP	1: Prioritor	1: Configuration

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	10																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord	X															X				
Hold	X															X				

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	15	7	20	10	15	7	10	0	0	10	0	0	0	20
CordNom	29	24	7	30	17	27	7	17	0	0	16	0	0	0	30
CordMax	40	30	7	40	20	30	7	20	0	0	20	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	9	0	10	0	0	7	0	0	0	0	0	0	0	0	0
CordNom	9	0	13	0	0	7	0	0	0	0	0	0	0	0	0
CordMax	18	0	20	0	0	7	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	c	None	None	None	None	None	None	None	None	None	None
Value	25	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Diamond	GTSI	08/20/99 09:36:34	Turner	06/12/06 14:40:58

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	OVERLAP	GTSI	01/17/00 18:35:32	aescoba	05/24/10 16:16:37

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

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Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	1	1	5	5	5	5	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.6	3.6	4.7	4.7	3.6	3.6	3.6	3.6	4.7	0.0
RedClear	1.7	1.7	2.0	2.0	1.7	1.7	1.7	1.7	2.0	0.0
Walk	0	0	5	0	5	0	0	0	0	0
PedClear	0	0	14	0	15	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Diamond Phase Data	GTSI	01/17/00 20:39:24	unknown	03/08/11 09:12:03

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl	X															X				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt					X					X					X			X		
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X		X		X		X												
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	7	1	5	3	8	1	1	0	0	3	0	0	0	3
Passage	1.5	2.5	0.5	1.5	1.5	2.0	0.5	2.0	0.0	0.0	1.0	0.0	0.0	0.0	0.5
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	50	40	1	35	30	11	1	22	0	0	40	0	0	0	35
Max2	45	30	1	45	20	25	1	22	0	0	50	0	0	0	45
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.6	3.6	4.7	4.7	3.6	3.6	4.7	4.7	0.0	0.0	3.6	0.0	0.0	0.0	3.6
RedClr	1.7	1.7	2.0	2.0	1.7	1.7	2.0	2.0	0.0	0.0	1.7	0.0	0.0	0.0	1.7
Walk	0	0	0	0	0	5	0	6	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	15	0	14	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	4	0	3	0	0	1	0	0	0	0	0	0	0	0	0
Passage	2.5	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	45	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Max2	55	0	40	0	0	1	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.6	0.0	4.7	0.0	0.0	3.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	1.7	0.0	2.0	0.0	0.0	1.7	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	External Plans
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	Turner
Modified On	06/27/06 13:58:00

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Revert to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Prioritor	trf_ctrl	02/16/05 08:34:45	unknown	09/27/06 10:43:51

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Configuration	GTSI	01/17/00 20:39:23	Turner	06/13/06 13:05:27

Active Config Table 1 Params

Phase	1: Phase Configuration
Overlap	1: 4 Phase Diamond Overlaps
Preempt	1: PREEMPTION [FIRE]
Cabinet	1: input settings/output settings
Detector	1: DETECTOR
Coordination	1: Coord Parameters
Peer	1: Table 01

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Configuration	GTSI	01/17/00 18:35:33	aescobar	09/05/07 09:24:14

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	sbt	sbt	ebf	wbt	nbt	nbt	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup	X					X														
YelStart	X					X														
RedStart																				
SecStart																				
Ped						X		X												
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,4,b,3,1,c,21,11,d
2	7,5,a,15,b,6,16,c,8,18,d
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	DETECTOR	GTSI	08/24/99 16:13:16	aescobar	09/12/07 17:31:28

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X		X		X		X		X	X	X	X											
Call	X		X		X		X		X	X	X	X											
Extend	X		X		X		X		X	X	X	X											
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable									X	X	X	X	X	X	X	X	X	X
Call									X	X	X	X	X	X	X	X	X	X
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	0	4	0	16	0	11	0	5	5	1	1	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	3	7	18	5	6	21
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	1	5	6	21
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	5	3	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	unknown	09/12/07 18:09:23

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	Prempt	Prempt	Prempt	Prempt	Prempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	NAnd	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	None	None	None	None	None	None
Pin	52	10	11	12	13	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None
FctnIndx	0	0	0	0	2	3	4	5	1	0	0
Device	None	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None
Pin	None	None	None	None	71	73	72	74	54	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	Veh	Veh	Veh	Veh	Veh
FctnIndx	0	0	0	0	0	0	55	56	57	58	59
Device	None	None	None	None	None	None	Virt	Virt	None	None	Virt
Pin	None	None	None	None	None	None	4	11	None	None	4
Chan1	0	0	0	0	0	0	0	0	7	1	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	Veh	Veh	Veh	None	Omit	Veh	Veh
FctnIndx	60	61	62	0	18	63	64
Device	None	Virt	Virt	Virt	Virt	Virt	Virt
Pin	None	3	7	28	18	4	8
Chan1	128	0	0	0	125	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovlped	ovlped	ped
FctnIndx	7	5	1	3	8	6	2	4	5	3	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	detrst	fl/wd	none	none	none	none	none	none	none
FctnIndx	8	9	1	1	0	0	0	0	0	0	0
Device1	C1S	None	C1S	C1S	None	None	None	None	None	None	None
Pin1	19	None	102	103	None	None	None	None	None	None	None
Device2	None	C1S	None	None	None	None	None	None	None	None	None
Pin2	None	38	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	None	None	None	None	None	None	None	None	None
Pin3	20	36	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	phson	phson	phson	aux	phson	phson	check
FctnIndx	11	3	7	8	8	4	8
Device1	Virt	Virt	Virt	Virt	Virt	Virt	Virt
Pin1	11	3	7	28	18	4	8
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	PREEMPTION [FIRE]	GTSI	01/10/00 07:53:31	aescobar	09/05/07 09:17:28

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell											X									
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X											X					
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	5	5	5	5	0	0	0	0	0
DwellExt	0	2	2	8	8	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	85	85	85	85	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	4 Phase Diamond Overlaps	GTSI	01/17/00 18:35:32	aescobar	09/12/07 15:08:43

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X									X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X									X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X	X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X				X									
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X																		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X										X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X									X		X		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X	X	X	X											
Ped			X	X																
YelFlash																				
AltFlsHz	X	X		X	X	X	X													
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coord Parameters	guest	04/15/2002 12:54:44	aescobar	09/05/07 09:15:48

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	0	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	guest	06/20/2003 11:52:34	guest	06/20/2003 11:52:34

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:37:04
Modified By	rreevey
Modified On	03/19/14 15:37:04

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

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NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:37:04
Modified By	rreevey
Modified On	03/19/14 15:37:04

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:37:04
Modified By	rreevey
Modified On	03/19/14 15:37:04

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:37:04
Modified By	rreevey
Modified On	03/19/14 15:37:04

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	COH16997 - Brookdale @ Northpark
Created By	awrigley
Created On	03/31/08 14:46:39
Modified By	aescobar
Modified On	10/16/08 15:26:08

NextPhase 1.7.2 Control Params

Control Mode	scheduler
Manual Plan	1: OFF PEAK 100
Free Plan	1: OFF PEAK 100
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	awrigley	03/31/08 14:46:39	unknown	04/06/11 13:10:43

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	Holiday
Created By	awrigley
Created On	03/31/08 14:46:39
Modified By	awrigley
Modified On	03/31/08 14:47:00

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	OFF PEAK 100	awrigley	03/31/08 14:46:39	unknown	08/11/09 09:03:38
2	AM PEAK 144	awrigley	03/31/08 14:46:59	unknown	08/11/09 09:03:47
3	PM PEAK 150	awrigley	03/31/08 14:46:59	unknown	08/11/09 09:03:56
4	MIDDAY 120	awrigley	06/27/08 15:22:15	unknown	08/11/09 09:04:10

Plan Table 1 Params

Plan Mode	Max Mode	Split/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1:	2: Lag 5	1: Overlap Data	1: Prioritor Data	1: RING STRUCTURE

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn										AuxFcn										Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10				
100	40																														

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	25	15	15	10	25	15	15	0	0	0	0	0	0	0
CordNom	15	45	20	20	15	45	20	20	0	0	0	0	0	0	0
CordMax	20	50	30	30	20	50	30	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	55	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1:	2: Lag 5	1: Overlap Data	1: Prioritor Data	1: RING STRUCTURE

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	40																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	20	20	15	35	20	20	0	0	0	0	0	0	0
CordNom	24	60	30	30	24	60	30	30	0	0	0	0	0	0	0
CordMax	30	70	40	40	30	70	40	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	72	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1:	2: Lag 5	1: Overlap Data	1: Prioritor Data	1: RING STRUCTURE

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	40																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	40	20	20	20	40	20	20	0	0	0	0	0	0	0
CordNom	20	70	30	30	30	60	30	30	0	0	0	0	0	0	0
CordMax	30	80	40	40	40	80	40	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1:	2: Lag 5	1: Overlap Data	1: Prioritor Data	1: RING STRUCTURE

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	40																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	35	15	15	10	35	15	15	0	0	0	0	0	0	0
CordNom	15	65	20	20	18	62	20	20	0	0	0	0	0	0	0
CordMax	20	70	30	30	20	70	30	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Leading	awrigley	03/31/08 14:46:39	aescobar	10/16/08 15:59:03
2	Lag 5	aescobar	10/16/08 15:58:41	aescobar	10/16/08 15:59:07

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	awrigley	03/31/08 14:46:39	aescobar	10/16/08 15:59:49

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Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	5	5	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		awrigley	03/31/08 14:46:39	unknown	05/20/11 13:23:08

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X	X	X	X	X	X	X	X												

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	6	6	6	6	6	6	6	6	0	0	0	0	0	0	0
Passage	1.5	3.2	2.0	2.5	1.5	3.2	2.0	2.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	35	50	25	25	35	50	25	25	0	0	0	0	0	0	0
Max2	35	75	35	35	35	75	35	35	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	4	0	4	0	4	0	4	0	0	0	0	0	0	0
PedClr	0	9	0	11	0	9	0	11	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	External
Created By	awrigley
Created On	03/31/08 14:46:39
Modified By	aescobar
Modified On	10/16/08 16:06:44

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	1	Dial 1
2										ExtPlan	2	Dial 2
3										ExtPlan	3	Dial 3
4										ExtPlan	4	Dial 4
5										Schedule	0	Return to Schedule
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
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185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Prioritor Data	awrigley	03/31/08 14:46:39	unknown	07/02/08 07:04:27

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	4	8	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	RING STRUCTURE	awrigley	03/31/08 14:46:39	jclark	08/29/08 08:10:58

Active Config Table 1 Params

Phase	1: Ring Structure/IDs
Overlap	1: Overlaps
Preempt	1: Preemption (Fire)
Cabinet	1: Basic 170 332
Detector	1: COH 332 Detectors
Coordination	1: Transition Settings
Peer	1: Peer Settings

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Ring Structure/IDs	awrigley	03/31/08 14:46:39	aescobar	10/16/08 15:22:55

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	sbl	sbt	ebf	wbt	nbl	nbt	none	none	none	ebt	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	wbl	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,b,4,c
2	5,6,a,8,b,7,c
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH 332 Detectors	awrigley	03/31/08 14:46:39	unknown	05/20/11 13:26:05

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X										X	X							X				
Call	X	X	X	X	X	X	X	X			X	X							X				X
Extend	X	X	X	X	X	X	X	X			X	X							X				X
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable						X	X																
Call	X	X	X			X	X																
Extend	X	X	X			X	X																
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	0	0	0	7	0	2	5	0	0	4	4	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	5.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	5	0	0	0	6	1	8	0	0	0	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	5.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

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Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

NextEdit

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	awrigley	03/31/08 14:46:39	unknown	08/11/09 08:47:27

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

NextEdit

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	1	1	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	None	None	None	None	None	None
Pin	52	10	11	12	13	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	phase	phase	phase	phase	phase	ped	ped	ped
FctnIndx	1	2	3	4	5	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	none	none	none	none	specl	specl	specl	specl	specl	specl
FctnIndx	8	0	0	0	0	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

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Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preemption (Fire)	awrigley	03/31/08 14:46:39	jclark	08/29/08 07:57:32

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlaps	awrigley	03/31/08 14:46:39	aescobar	10/16/08 15:25:18

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable																				
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Transition Settings	awrigley	03/31/08 14:46:39	unknown	10/23/08 13:13:48

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Peer Settings	awrigley	03/31/08 14:46:39	awrigley	03/31/08 14:47:29

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	North Park DR.@W. Lake Houston
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	01/25/11 15:35:52

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: 100" Off Peak
Free Plan	2: 144" AM Peak
Default Schedule	1:

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 15:52:32	unknown	04/05/11 13:05:31

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Midday (Weekend)
5																	Midday
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpCfcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1:	
2		1	1								1:	
3		1	1								1:	

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
4		1	1								1:	
5		1	1								1:	
6		1	1								1:	
7		1	1								1:	
8		1	1								1:	
9		1	1								1:	
10		1	1								1:	
11		1	1								1:	
12		1	1								1:	
13		1	1								1:	
14		1	1								1:	
15		1	1								1:	
16		1	1								1:	
17		1	1								1:	
18		1	1								1:	
19		1	1								1:	
20		1	1								1:	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1:	
2		1	1								1:	
3		1	1								1:	
4		1	1								1:	
5		1	1								1:	
6		1	1								1:	
7		1	1								1:	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
8		1	1								1:	
9		1	1								1:	
10		1	1								1:	
11		1	1								1:	
12		1	1								1:	
13		1	1								1:	
14		1	1								1:	
15		1	1								1:	
16		1	1								1:	
17		1	1								1:	
18		1	1								1:	
19		1	1								1:	
20		1	1								1:	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTSI	01/17/00 20:39:24	unknown	12/09/05 11:12:40
2	144" AM Peak	unknown	02/10/05 07:44:03	unknown	12/12/05 08:47:29
3	150" PM Peak	unknown	02/10/05 07:45:39	unknown	12/09/05 11:14:44
4	120" Midday	unknown	02/10/05 07:52:48	unknown	12/09/05 11:11:44

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max1	seconds	1: Phase Data	1: EBLT Leads	1: Overlap Data	1:	1:	100

Plan Table 1 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
20																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	20	15	10	20	10	30	0	0	0	0	0	0	0
CordNom	17	25	27	16	15	27	15	43	0	0	0	0	0	0	0
CordMax	30	40	40	30	20	40	20	60	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	1: EBLT Leads	1: Overlap Data	1:	1:	144

Plan Table 2 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
20																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	25	20	15	25	10	45	0	0	0	0	0	0	0
CordNom	24	44	40	22	24	44	14	62	0	0	0	0	0	0	0
CordMax	30	50	50	40	30	50	20	90	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	1: EBLT Leads	1: Overlap Data	1:	1:	150

Plan Table 3 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
20																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	30	25	15	15	30	10	40	0	0	0	0	0	0	0
CordNom	25	54	36	20	25	54	15	56	0	0	0	0	0	0	0
CordMax	30	60	50	30	30	60	20	80	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	3: Leading Lefts	1: Overlap Data	1:	1:

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	15	10	15	25	10	25	0	0	0	0	0	0	0
CordNom	23	40	25	17	23	40	15	42	0	0	0	0	0	0	0
CordMax	30	50	30	20	30	50	20	50	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	EBLT Leads	GTSI	08/20/99 09:36:34	unknown	02/05/05 19:21:33
2	WBLT Leads	guest	09/26/2002 10:00:33	unknown	02/05/05 19:30:34
3	Leading Lefts	guest	09/26/2002 10:12:02	unknown	02/05/05 19:21:56

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	unknown	09/16/10 10:59:16

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	4.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	5	0	0	0	0	0	0
PedClear	0	0	0	10	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	unknown	05/19/11 12:36:21

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X		X		X		X												
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X	X	X	X	X	X	X	X												

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	7	10	7	6	7	10	7	10	0	0	0	0	0	0	0
Passage	2.5	3.0	2.5	2.0	2.5	3.0	2.5	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	35	30	20	20	40	20	45	0	0	0	0	0	0	0
Max2	30	60	40	35	30	60	20	70	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0	0	0	0	0	0
PedClr	0	10	0	10	0	10	0	10	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	Glen Andrus
Modified On	09/14/2004 14:39:59

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Return to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

NextEdit

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	12/06/05 14:35:15	trf_ctrl	12/06/05 14:35:15

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:23	guest	02/02/2005 16:00:42

Active Config Table 1 Params

Phase	1: Phase Configuration
Overlap	1: Overlap
Preempt	1: Preempt
Cabinet	1: input settings/output settings
Detector	1: Detectors
Coordination	1:
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Configuration	GTSI	01/17/00 18:35:33	unknown	08/31/10 11:25:56

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	2
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,b,4,3,c
2	5,6,a,7,b,8,c
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTSI	08/24/99 16:13:16	unknown	05/19/11 12:45:05

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X								X	X	X	X	X	X	X	X			X				
Call	X				X	X	X	X	X	X	X	X	X	X	X	X			X				X
Extend	X				X	X	X	X	X	X	X	X	X	X	X	X			X				X
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok			X	X	X	X					X	X	X	X	X	X					X	X	X

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable				X	X	X	X	X	X	X	X												
Call	X	X	X	X	X	X	X	X	X	X	X												
Extend	X	X	X	X	X	X	X	X	X	X	X												
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok	X							X	X	X	X												

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	0	0	0	2	5	4	7	3	3	4	4	4	4	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	4	0	0	5	0	0	0	6	1	8	3	7	7	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	8	8	8	8	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	0	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	4	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	unknown	09/16/10 10:58:21

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped	Ped	Ped
FctnIndx	23	24	25	26	27	28	29	30	1	2	3
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	67	69	68
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Ped	PlnSel	Veh	None	None	None	None	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	4	5	36	1	2	3	4	1	2	3	4
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	70	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Prempt	Prempt	Prempt	Prempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	NAnd	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	ovl	phase	phase	phase	phase	ped	ovlped	ped
FctnIndx	1	2	3	4	5	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempt	GTSI	01/10/00 07:53:31	unknown	11/12/04 16:51:27

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable										
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	85	85	85	85	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap	GTSI	01/17/00 18:35:32	unknown	05/19/10 08:32:43

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X			X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable				X																
Ped				X																
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	awrigley	03/29/06 15:09:22

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	trf_ctrl
Created On	08/23/01 14:47:16
Modified By	unknown
Modified On	04/05/11 13:06:30

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	120
Data Filter	X
Key Repeat	X

Admin Dat/Tim DST

Zone	Ctr
DST	X

NextEdit

NextPhase 1.7.5 Admin Comm

Description	
Created By	unknown
Created On	07/09/04 12:15:43
Modified By	unknown
Modified On	05/19/11 12:51:13

Admin Comm Serial

Port	1	2	3	4
Mode	PMPP	None	None	PMPP
PMPP Address	1	0	0	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	9600	9600	9600	9600
Parity	None	None	None	None
Data Bits	8	8	8	8
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	trf_ctrl
Created On	08/05/03 16:06:17
Modified By	trf_ctrl
Modified On	08/05/03 16:06:17

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	unknown
Created On	11/13/07 14:02:37
Modified By	unknown
Modified On	11/13/07 14:07:29

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
UserId	1	2	3	4																									
Password	1	2	3	4																									
Name	Level 1	Level 2	Level 3	Level 4																									
View	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

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Admin Security UserList

User	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62
UserId																																	
Password																																	
Name																																	
View	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

Admin Security UserList

User	63	64
UserId		
Password		
Name		
View	4	4
Edit	4	4

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Control Table Info

Description	Kingwood @ Willow Terrace
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	awrigley
Modified On	12/04/06 15:56:41

NextPhase 1.7.0 Control Params

Control Mode	free
Manual Plan	<TABLE 2 DOES NOT EXIST>
Free Plan	1: 90" Plan
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	awrigley	11/30/06 13:29:01

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	1
2		6	0	X	X	X	X	X	X		sched	2
3		9	0		X	X	X	X	X		sched	4
4		9	0	X						X	sched	4
5		11	0		X	X	X	X	X		sched	4
6		15	0		X	X	X	X	X		sched	3
7		19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Weekend
5																	Midday
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	90" Plan	unknown	10/11/05 17:44:26	awrigley	12/04/06 12:50:53

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Off Peak	1: Standard Leading Lefts	1: Off Peak	1:	1: Active Config

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
90	15																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	10	25	10	10	10	20	15	0	0	0	8	8	0	8
CordNom	0	12	40	15	15	12	30	25	0	0	0	8	8	0	8
CordMax	0	20	50	20	20	20	40	30	0	0	0	8	8	0	8
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	17	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard Leading Lefts	GTSI	08/20/99 09:36:34	awrigley	11/30/06 13:29:17

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 18:35:32	awrigley	12/04/06 12:46:19

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	3	5	0	3	5	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.5	4.5	4.5	0.0	4.5	4.5	0.0	0.0	0.0	0.0
RedClear	1.5	1.5	1.5	0.0	1.5	1.5	0.0	0.0	0.0	0.0
Walk	0	0	4	0	0	4	0	0	0	0
PedClear	0	0	15	0	0	15	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 20:39:24	awrigley	01/02/07 11:25:50

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X	X			X	X				X				X					
SimGap																				
RedRest																				
AutoPed																				
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	0	12	3	3	3	12	3	5	0	0	0	3	3	0	3
Passage	0.0	4.0	1.5	1.0	1.5	4.0	1.5	1.5	0.0	0.0	0.0	1.5	1.5	0.0	1.5
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	25	35	20	20	30	35	30	0	0	0	8	8	0	8
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.5	3.5	4.0	4.5	4.5	3.5	4.0	0.0	0.0	0.0	4.5	3.5	0.0	3.5
RedClr	0.0	1.5	1.5	1.0	1.5	1.5	1.5	1.0	0.0	0.0	0.0	1.5	1.5	0.0	1.5
Walk	0	4	0	0	0	4	0	4	0	0	0	0	0	0	0
PedClr	0	15	0	0	0	15	0	5	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTS	09/10/04 14:08:44	GTS	09/10/04 14:08:45

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Active Config	GTSI	01/17/00 20:39:23	awrigley	02/06/06 12:58:34

Active Config Table 1 Params

Phase	1: rings & ids
Overlap	1: WT/TS Overlaps
Preempt	1:
Cabinet	1: Basic NEMA TS1
Detector	1: Detectors
Coordination	1: GPS Coordination
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	rings & ids	unknown	10/11/05 11:32:27	awrigley	12/04/06 15:11:07

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X			X													
YelStart				X			X													
RedStart																				
SecStart																				
Ped		X				X		X												
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,3,b,13,4,16,c,12,d
2	6,a,5,8,b,7,c,15,d
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTSI	08/24/99 16:13:16	awrigley	12/04/06 15:29:22

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	
Call		X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	
Extend		X	X	X	X					X	X	X	X	X	X	X	X		X		X	X	
AddInit																							
Term																							
RedLok																							
YelLok		X	X	X	X							X	X	X	X								

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable															X	X	X	X
Call															X	X	X	X
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	2	2	2	2	3	3	3	3	4	4	6	6	6	6
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	5	7	7	7	7	8	8	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	13	13	16	15
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X				X		X															

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	3	4	5	6	7	8	0	0	0	0	0	0	0
Overlap	0	3	0	0	0	6	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

NextEdit

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic NEMA TS1	GTS	09/08/04 20:13:17	awrigley	12/04/06 15:11:54

Cabinet Table 1 Cabinet Type

Cabinet Type	TS1
Unit Address 1	none
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped	Ped	Ped
FctnIndx	1	2	3	4	5	6	7	8	1	2	3
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-
Pin	f	K	N	L	P	S	V	t	g	L	P
Chan1	0	0	0	0	65	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Ped	Ped	Ped	Ped	Ped	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	4	5	6	7	8	9	10	11	12	13	14
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-	-B-	-C-	-C-
Pin	M	R	T	U	W	h	M	i	h	m	p
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	15	16	17	18	19	20	21	22	23	24	25
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-C-	-C-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-A-
Pin	EE	X	U	S	R	g	n	q	r	s	EE
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Force	Force	NoMax	NoMax
FctnIndx	26	27	28	29	30	31	32	1	2	1	2
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-B-	-B-	-B-	-B-	-B-	-B-	-A-	-C-	-A-	-C-
Pin	v	j	x	T	k	m	n	i	Y	P	a
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	RedRst	RedRst	PedRcl	PedRcl	Max2	Max2	NoRed	NoRed	CNA	CNA	MCE
FctnIndx	1	2	1	2	1	2	1	2	1	0	1
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-C-	-A-	-B-	-A-	-B-	-A-	-C-	-A-	-A-	-A-
Pin	x	u	FF	V	GG	z	w	v	m	z	k
Chan1	0	0	0	0	0	0	0	0	54	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	Or	And	And

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Advanc	MinRcl	WlkRst	Start	StopTm	StopTm	None	None	None	None	None
FctnIndx	1	1	1	1	1	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-A-	-A-	-A-	-A-	-C-	None	None	None	None	None
Pin	S	j	BB	R	N	Z	None	None	None	None	None
Chan1	0	0	0	0	61	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	Veh	Veh	Veh	Omit	Omit	Veh
FctnIndx	0	61	62	63	15	12	64
Unit	none	1	1	1	1	1	1
Conn/BIU	None	-B-	-B-	-B-	-A-	-A-	-C-
Pin	None	s	K	b	F	F	i
Chan1	0	0	18	0	0	0	0
Chan2	0	0	19	0	0	0	0
Logic	And	And	Or	And	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	phase	phase	ovl	ovl	phase	phase	ovl	ovl	ovl
FctnIndx	1	3	3	4	5	6	7	8	1	2	3
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin1	D	F	F	G	H	G	F	D	q	CC	DD
Conn/BIU2	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin2	Z	b	E	c	J	h	E	e	p	BB	HH
Conn/BIU3	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin3	s	c	D	b	i	g	f	x	AA	GG	FF

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovl	ovlpd	ped	ovlpd	ped	check	phsnxt	phson	check	phsnxt	phson
FctnIndx	4	3	4	6	8	1	1	1	2	2	2
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-B-	-A-	-B-	-C-	-C-	-A-	-B-	-A-	-A-	-B-	-A-
Pin1	u	G	J	z	C	u	A	DD	d	C	e
Conn/BIU2	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin2	EE	H	H	AA	w	None	None	None	None	None	None
Conn/BIU3	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin3	w	J	d	LL	d	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt
FctnIndx	3	3	3	4	4	4	5	5	5	6	6
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-B-	-B-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-C-
Pin1	r	t	s	K	f	e	k	M	N	BB	DD
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	phson	check	phsnxt	phson	check	phsnxt	phson	fl/wd	none	none	none
FctnIndx	6	7	7	7	8	8	8	1	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-C-	-C-	-C-	-C-	-C-	-C-	-C-	-A-	None	None	None
Pin1	CC	MM	PP	NN	FF	HH	GG	X	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/10/00 07:53:31	GTSI	01/17/00 20:39:24

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable										
NoMemLock										
ManEnable										
NoOvrFlash										
NoOvrNext										
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	0	0	0	0	0	0	0	0	0
DwellExt	0	0	0	0	0	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WT/TS Overlaps	GTSI	01/17/00 18:35:32	awrigley	12/04/06 12:41:04

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X										X		X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X							X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X														
Secondary Parent																				
Mod Phase						X														

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X		X	X														
Ped			X			X														
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	GPS Coordination	trf_ctrl	08/23/01 16:25:18	awrigley	02/14/06 09:50:17

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	0	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood @ Royal Forest
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	aescoba
Modified On	05/24/10 16:28:50

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: 100" Off Peak (new)
Free Plan	1: 100" Off Peak (new)
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 10:14:04

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	Holiday Schedule
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	guest
Modified On	02/27/2002 15:12:15

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (new)	GTSI	01/17/00 20:39:24	unknown	02/29/12 10:42:01
2	144" AM Peak (new)	guest	02/27/2002 19:56:27	unknown	02/29/12 10:21:10
3	150" PM Peak (new)	guest	02/27/2002 19:56:29	unknown	02/29/12 10:20:55
4	120" Midday (new)	guest	09/23/2002 15:28:24	unknown	03/01/12 12:01:13

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Coordinated Operation	2: Lag 5	1: Overlap Data	1: Low Priority Bus Detection

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Controller Configuration	100	20																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	30	0	15	10	30	0	10	0	0	0	0	0	0	0
CordNom	18	47	0	18	15	50	0	17	0	0	0	0	0	0	0
CordMax	30	60	0	30	20	60	0	20	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	20	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	30	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	70	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap
coord	max2	seconds	1: Coordinated Operation	3: Lag 5 & 4	1: Overlap Data

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Plan Table 2 Params

Prioritor	Config	Cycle Length	Permissive	SpCfcn							
				1	2	3	4	5	6	7	8
1: Low Priority Bus Detection	1: Controller Configuration	144	20								

Plan Table 2 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	50	0	15	15	50	0	10	0	0	0	0	0	0	0
CordNom	24	82	0	22	24	82	0	16	0	0	0	0	0	0	0
CordMax	30	100	0	30	30	100	0	20	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	25	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	35	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	50	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	100	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap
coord	max2	seconds	1: Coordinated Operation	1: Standard All Leading	1: Overlap Data

Plan Table 3 Params

Prioritor	Config	Cycle Length	Permissive	SpcFcn							
				1	2	3	4	5	6	7	8
1: Low Priority Bus Detection	1: Controller Configuration	150	20								

Plan Table 3 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	12	60	0	12	15	55	0	20	0	0	0	0	0	0	0
CordNom	15	92	0	17	25	82	0	26	0	0	0	0	0	0	0
CordMax	25	120	0	25	30	110	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	32	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	65	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Coordinated Operation	2: Lag 5	1: Overlap Data	1: Low Priority Bus Detection

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: Controller Configuration	120	20	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	40	0	10	15	40	0	15	0	0	0	0	0	0	0
CordNom	20	57	0	17	17	60	0	26	0	0	0	0	0	0	0
CordMax	30	80	0	20	35	80	0	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	25	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	50	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Leading	GTSI	08/20/99 09:36:34	aescoba	05/24/10 16:26:39
2	Lag 5	unknown	09/24/02 17:26:59	aescoba	05/24/10 16:26:43
3	Lag 5 & 4	vtorres	02/28/12 14:03:49	vtorres	02/28/12 14:04:02

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Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	vtorres	02/29/12 09:38:38

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

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Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	3.5	3.5	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	3.0	3.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	2.0	2.0	0.5	0.5	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coordinated Operation	GTSI	01/17/00 20:39:24	clupold	08/14/12 11:52:28

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl																				
MaxRcl		X				X														
SftRcl																				
ConSvc																				
PedRcl		X				X														
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	4	8	0	6	4	8	0	6	0	0	0	60	0	0	0
Passage	2.0	3.5	0.0	2.0	2.0	3.5	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	60	0	30	25	60	0	30	0	0	0	60	0	0	0
Max2	30	60	0	40	30	60	0	40	0	0	0	60	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	0.0	3.5	4.0	4.0	0.0	3.5	0.0	0.0	0.0	4.0	0.0	0.0	0.0
RedClr	2.5	2.0	0.0	3.5	2.5	2.0	0.0	3.5	1.0	0.0	0.0	2.0	0.0	0.0	0.0
Walk	0	4	0	7	0	4	0	7	0	0	0	0	0	0	0
PedClr	0	10	0	7	0	10	0	7	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	60	0	6	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	60	0	30	0	0	0	0	0	0	0	0	0	0	0	0
Max2	60	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	0.0	3.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	0.0	3.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	unknown
Modified On	02/22/08 14:46:59

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1				X						ExtPlan	1	Dial 1
2		X		X						ExtPlan	2	Dial 2
3			X	X						ExtPlan	3	Dial 3
4		X	X	X						ExtPlan	4	New Off Peak
5										Schedule	0	New AM Peak
6										ExtPlan	0	New Mid Day
7										ExtPlan	0	New PM Peak
8										ExtPlan	0	New Night Time
9										ExtPlan	0	Lost Interconnect
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
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89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Low Priority Bus Detection	trf_ctrl	03/30/05 15:22:01	unknown	04/12/06 10:26:13

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	5	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Controller Configuration	GTSI	01/17/00 20:39:23	vtorres	02/28/12 14:08:08

Active Config Table 1 Params

Phase	1: Phase Configuration
Overlap	1: Overlaps
Preempt	1: Preempts
Cabinet	1: COH 332 I/O
Detector	1: 332 Detectors
Coordination	1: Coordination
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Configuration	GTSI	01/17/00 18:35:33	vtorres	02/28/12 14:02:42

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	none	sbt	ebf	wbt	none	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup								X												
YelStart								X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash		X				X														
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall												X				X				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,12,2,a,4,8,b
2	5,16,6,a,18,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	332 Detectors	unknown	09/01/99 11:19:46	jclark	05/25/11 14:42:37

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X		X	X	X	X	X	X										X		X	X	X	
Call	X		X	X	X	X	X	X		X	X			X	X			X		X	X	X	
Extend	X		X	X	X	X	X	X		X	X			X	X			X		X	X	X	
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok			X	X		X															X	X	X

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X					X	X																
Call	X					X	X																
Extend	X					X	X																
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok	X																						

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	2	2	2	2	4	4	2	5	4	7	6	1	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	3	0	0	5	5	6	6	6	6	6	6	7	7	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	8.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	8	8	8	8	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH 332 I/O	GTSI	08/20/99 09:36:29	vtorres	02/28/12 14:09:47

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	3	4	5	6	7	9	11	12	13	14
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	56	39	43	63	76	47	58	41	45	65	78
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Advanc	MCE	Ped	Ped	Ped	Ped	Flash	StopTm
FctnIndx	15	17	18	1	1	1	2	3	4	1	1
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	49	60	62	80	53	67	69	68	70	81	82
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	19	21	22	23	24	25	27	29	30	31	32
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	55	40	44	64	77	48	57	42	46	66	79
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	TimSet	Veh	Preempt	Preempt	Preempt	Preempt	Preempt	Preempt	Veh	Veh
FctnIndx	33	35	36	2	3	4	5	1	6	2	8
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S
Pin	50	59	61	71	73	72	74	51	52	15	16
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	Or	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	PlnSel	PlnSel	PlnSel	PlnSel	None
FctnIndx	10	16	20	26	28	34	1	2	3	4	0
Device	C11S	C11S	C11S	C11S	C11S	C11S	C11S	C11S	C11S	C11S	None
Pin	17	18	19	20	21	22	10	11	12	13	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	ped	ovl	phase	ped	phase	ovl	ped	ovl	phase
FctnIndx	1	2	2	3	4	4	5	1	6	4	8
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	10	7	4	2	32	29	27	24	21
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	17	13	None	8	5	None	33	30	None	25	22
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	11	9	6	3	34	31	28	26	23

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovl	detrst	fl/wd
FctnIndx	8	1	2	3	4	5	6	7	8	1	1
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	None	None	97	94	91	88	85	83	102	103
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	37	38	98	95	101	89	86	100	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	20	35	36	99	96	93	90	87	84	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	bushekn	04/12/06 10:42:48

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable	X	X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres	X									
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1		X			X															
Entry2																				
Dwell	X	X		X	X			X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit												X				X				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.5	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	5	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	20	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	10	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	85	85	85	85	0	0	0	0	0
MinDura	10	10	10	10	10	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlaps	GTSI	01/17/00 18:35:32	vtorres	02/28/12 14:11:06

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X										X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X												
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped																				
YelFlash																				
AltFlsHz	X	X	X	X																
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coordination	trf_ctrl	08/23/01 16:25:18	unknown	01/07/06 11:41:36

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TODOVL	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X					3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	trf_ctrl
Created On	08/23/01 14:47:16
Modified By	unknown
Modified On	08/14/12 11:32:25

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	120
Data Filter	X
Key Repeat	X

Admin Dat/Tim DST

Zone	Ctr
DST	X

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NextPhase 1.7.5 Admin Comm

Description	
Created By	unknown
Created On	07/09/04 12:15:43
Modified By	unknown
Modified On	08/14/12 11:39:13

Admin Comm Serial

Port	1	2	3	4
Mode	PMPP	None	None	PMPP
PMPP Address	1	0	0	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	9600	9600	9600	9600
Parity	None	None	None	None
Data Bits	8	8	8	8
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	10.230.187.31
NetMask	255.255.255.0
Gateway	10.230.187.1
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	10.230.113.9
RefTimeHour	0
RefTimeMinute	59
PeriodHour	6
PeriodMinute	0
Retries	5

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	trf_ctrl
Created On	08/05/03 16:06:17
Modified By	trf_ctrl
Modified On	08/05/03 16:06:17

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	unknown
Created On	11/13/07 14:02:37
Modified By	unknown
Modified On	11/13/07 14:07:29

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
UserId	1	2	3	4																									
Password	1	2	3	4																									
Name	Level 1	Level 2	Level 3	Level 4																									
View	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

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Admin Security UserList

User	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62
UserId																																	
Password																																	
Name																																	
View	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

Admin Security UserList

User	63	64
UserId		
Password		
Name		
View	4	4
Edit	4	4

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Control Table Info

Description	Green Oak @ Kingwood
Created By	tbales
Created On	08/16/05 15:52:27
Modified By	unknown
Modified On	10/23/08 08:43:26

NextPhase 1.7.1 Control Params

Control Mode	scheduler
Manual Plan	2: 144" AM Peak
Free Plan	1: 100" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	gtsi	09/20/2001 19:18:28	unknown	04/06/11 11:14:21

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTSI	01/17/00 20:39:24	unknown	04/10/06 09:54:44
2	144" AM Peak	unknown	03/16/04 16:02:21	unknown	03/29/11 08:42:22
3	150" PM Peak	awrigley	11/28/05 09:03:13	unknown	03/31/11 16:18:55
4	120" Midday (dual)	unknown	12/01/05 15:13:39	awrigley	12/01/05 15:20:33

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: WB Lag	1: WB LAG	1: dC Overlaps	1:	1: Weird Kingwood

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	20																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																X				
Hold																X				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	20	0	10	10	0	0	10	0	0	0	0	0	0	5
CordNom	15	30	0	15	15	0	0	15	0	0	0	0	0	0	5
CordMax	20	40	0	20	20	0	0	20	0	0	0	0	0	0	5
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	35	0	20	0	0
CordNom	0	0	0	0	0	65	0	35	0	0
CordMax	0	0	0	0	0	70	0	40	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	55	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	2: EB Lag	2: EB Lag	1: dC Overlaps	1:	1: Weird Kingwood

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	10																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																X				
Hold																X				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	0	15	0	0	0	15	0	0	5	0	0	0	0
CordNom	20	49	0	25	0	0	0	25	0	0	5	0	0	0	0
CordMax	30	50	0	30	0	0	0	30	0	0	5	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	10	0	0	5	0	0
CordNom	0	0	0	0	0	0	0	0	0	15	0	0	5	0	0
CordMax	0	0	0	0	0	0	0	0	0	20	0	0	5	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	45	0	30	0	0
CordNom	0	0	0	0	0	89	0	55	0	0
CordMax	0	0	0	0	0	90	0	60	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	125	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	3: PM Dual	3: PM Dual	1: dC Overlaps	1:	1: Weird Kingwood

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn										AuxFcn										Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10				
150	20																														

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																X				
Hold																X				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	25	0	20	12	15	0	15	0	0	5	0	0	5	0
CordNom	15	38	0	30	15	20	0	17	0	0	5	0	0	5	0
CordMax	20	50	0	40	25	30	0	30	0	0	5	0	0	5	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	5	45	0	45	0	0
CordNom	0	0	0	0	5	73	0	86	0	0
CordMax	0	0	0	0	5	90	0	90	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	140	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	4: Midday Dual	4: Midday Dual	1: dC Overlaps	1:	1: Weird Kingwood

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																X				
Hold																X				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	10	0	15	0	10	0	15	0	0	5	0	0	5	0
CordNom	15	15	0	20	0	15	0	20	0	0	5	0	0	5	0
CordMax	20	20	0	30	0	20	0	30	0	0	5	0	0	5	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	5	0	0	0	0	0	0	10	0	0	0	0	0
CordNom	0	0	5	0	0	0	0	0	0	15	0	0	0	0	0
CordMax	0	0	5	0	0	0	0	0	0	20	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	5	25	0	50	0	0
CordNom	0	0	0	0	5	35	0	85	0	0
CordMax	0	0	0	0	5	50	0	100	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	20	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WB LAG	GTSI	08/20/99 09:36:34	awrigley	11/29/05 23:48:27
2	EB Lag	guest	10/08/2002 13:33:39	tbales	10/02/07 16:23:06
3	PM Dual	guest	10/08/2002 13:34:09	awrigley	11/29/05 23:47:35
4	Midday Dual	awrigley	12/01/05 12:47:55	awrigley	12/01/05 12:50:06

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	4	3	0	5	0	1	0	7	0	0	0	0	0	0	0	2	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	6	0	0	0	8	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	3	0	7	0	0	0	5	0	0	2	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	8	4	0	0	6	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	3	0	11	4	9	0	6	0	0	2	0	0	10	5	0	0	0	0	0

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Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	12	0	0	0	7	0	0	0	0	0	0	8	0	0	0	0	0

Sequence Table 4 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	3	0	11	0	9	0	5	0	0	2	0	0	10	0	0	0	4	0	0

Sequence Table 4 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	12	7	0	0	6	0	0	0	0	0	0	8	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	dC Overlaps	GTSI	01/17/00 18:35:32	awrigley	04/23/08 13:12:20

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl		X				X														
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	5	5	5	3	5	5	5	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.5	4.0	3.5	4.0	3.5	4.0	3.5	4.0	3.0	3.0
RedClear	0.5	1.0	1.5	1.0	0.5	1.0	1.5	1.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0
PedClear	0	9	0	9	0	11	0	8	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

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Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WB Lag	GTSI	01/17/00 20:39:24	unknown	12/30/08 07:59:04
2	EB Lag	awrigley	11/28/05 09:40:08	unknown	01/27/10 07:53:27
3	PM Dual	awrigley	11/28/05 09:40:11	unknown	03/31/11 16:04:05
4	Midday Dual	awrigley	12/01/05 12:52:10	unknown	12/30/08 08:00:28

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit						X					X			X				X		
PedOmit																				
MinRcl		X																		
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed	X	X			X										X					
WlkRest	X	X			X										X					
PedRcycl																				
RedLock																				
YelLock																				
NoExtend															X					
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit					X			X							X					
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend				X																
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	5	0	1	3	0	0	1	0	0	0	0	0	0	1
Passage	1.0	3.0	0.0	3.0	1.0	0.0	0.0	2.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	20	0	35	25	0	0	30	0	0	0	0	0	0	1
Max2	25	20	0	35	25	0	0	30	0	0	0	0	0	0	1
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5
RedClr	1.0	1.0	2.0	1.0	1.0	2.0	2.0	1.0	2.0	2.0	2.0	2.0	2.0	2.0	0.5
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	1.0	2.0	2.0	2.0	2.0	2.0	0.5	2.0	2.0	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	65	0	65	0	0
Max2	0	0	0	0	0	65	0	65	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit					X	X								X	X			X		
PedOmit																				
MinRcl		X																		
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed	X	X									X									
WlkRest	X	X									X									
PedRcycl																				
RedLock																				
YelLock																				
NoExtend											X									
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X			X				X												

Phase Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit				X											X					
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed					X															
WlkRest					X															
PedRcycl																				
RedLock																				
YelLock																				
NoExtend								X												
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck					X															

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Phase Table 2 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	5	0	1	3	0	0	1	0	0	0	0	0	0	0
Passage	2.0	3.0	0.0	3.0	1.0	0.0	0.0	2.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	50	0	30	20	0	0	30	0	0	1	0	0	0	0
Max2	25	50	0	30	20	0	0	30	0	0	1	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	4.0
RedClr	1.0	1.0	2.0	2.0	1.0	1.0	2.0	1.0	2.0	2.0	0.5	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	3	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	20	0	0	1	0	0
Max2	0	0	0	0	0	0	0	0	0	20	0	0	1	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	1.0	2.0	2.0	0.5	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	90	0	60	0	0
Max2	0	0	0	0	0	90	0	60	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

Phase Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit															X			X		
PedOmit																				
MinRcl		X				X														
MaxRcl						X														
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed	X	X			X	X					X			X						
WlkRest	X	X			X	X					X			X						
PedRcycl																				
RedLock																				
YelLock																				
NoExtend											X			X						
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X			X	X			X												

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Phase Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit				X	X			X												
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed															X					
WlkRest															X					
PedRcycl																				
RedLock																				
YelLock																				
NoExtend															X					
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 3 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	5	0	1	3	5	0	1	0	0	0	0	0	0	0
Passage	1.0	3.0	0.0	3.0	1.0	3.0	0.0	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	30	0	40	25	15	0	25	0	0	0	0	0	0	0
Max2	25	30	0	40	25	15	0	25	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	4.0
RedClr	1.0	1.0	2.0	1.0	1.0	1.0	2.0	1.0	2.0	2.0	0.5	2.0	2.0	1.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 3 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.5	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 3 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	90	0	60	0	0
Max2	0	0	0	0	0	90	0	60	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	1.5	2.0	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

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Phase Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit					X										X					
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed	X	X				X				X				X				X		
WlkRest	X	X				X				X				X				X		
PedRcycl																				
RedLock																				
YelLock																				
NoExtend										X				X				X		
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X			X				X												

Phase Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit				X				X												
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed					X										X					
WlkRest					X										X					
PedRcycl																				
RedLock																				
YelLock																				
NoExtend															X					
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 4 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	5	0	1	0	5	0	1	0	0	1	0	0	0	0
Passage	1.0	3.0	0.0	3.0	0.0	3.0	0.0	2.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	20	0	20	0	20	0	20	0	0	1	0	0	0	0
Max2	20	20	0	20	0	20	0	20	0	0	1	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	0.0	4.0	0.0	4.0	0.0	4.0	0.0	0.0	3.5	0.0	0.0	4.0	0.0
RedClr	1.0	1.0	0.0	1.0	0.0	1.0	0.0	1.0	0.0	0.0	0.5	0.0	0.0	1.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 4 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	3	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	20	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	20	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	4.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 4 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	45	0	90	0	0
Max2	0	0	0	0	0	45	0	90	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	3.5	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.5	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	Table 01
Created By	gtsi
Created On	09/20/2001 19:19:28
Modified By	unknown
Modified On	07/25/05 10:23:17

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	
2	X	X			X					ExtPlan	2	
3	X		X	X						ExtPlan	3	
4	X									ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										Schedule	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
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81										ExtPlan	0	
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98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
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143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
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188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
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226										ExtPlan	0	
227										ExtPlan	0	
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229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	01/31/05 21:05:59	bushekn	04/10/06 12:15:41

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Weird Kingwood	GTSI	01/17/00 20:39:23	awrigley	11/28/05 11:08:04

Active Config Table 1 Params

Phase	1: Rings @ Phases/IDs
Overlap	1: dC Overlaps
Preempt	1: RR and EV Preempts
Cabinet	1: Basic 170_332
Detector	1: COH 332 Detectors
Coordination	1: Coordination Configuration
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Rings @ Phases/IDs	GTSI	01/17/00 18:35:33	tbales	10/02/07 11:48:32

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	nbl	sbt	ebf	wbt	sbl	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X																
YelStart				X																
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz	X	X			X	X					X					X				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	2
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	5,15,2,1,11,a,38,b
2	36,a,14,4,24,6,25,35,18,8,28,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH 332 Detectors	GTSI	08/24/99 16:13:16	unknown	03/24/11 09:06:43

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X			X					X	X			X	X	X	X	X
Call	X	X	X	X	X		X			X	X	X	X	X	X	X			X	X	X	X	X
Extend	X	X	X	X	X		X			X	X	X	X	X	X	X							X
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X	X		X		X		X		X													
Call				X				X		X													
Extend		X		X		X																	
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable		X	X	X	X	X	X	X	X	X	X	X	X	X		X	X	X
Call				X	X	X	X	X	X	X	X	X	X	X		X	X	X
Extend				X	X									X				
AddInit																		
Term		X	X															
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	2	2	2	4	4	4	4	5	6	6	6	6	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	8	8	8	4	4	8	8	2	5	4	0	6	1	8	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	4	0	8	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	4	8	24	28	15	11	35	35	8	4	25
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	19	20	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	8.0	8.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	25	18	14	11
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	GTSI	08/20/99 09:36:29	awrigley	04/23/08 12:43:56

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	And	Or	Or	Or	Or	Or	And	And	And	And

NextEdit

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	1	1	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	And	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NOr	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	Veh
FctnIndx	0	0	0	0	0	0	0	0	0	0	48
Device	None	None	None	None	None	None	None	None	None	None	C1S
Pin	None	None	None	None	None	None	None	None	None	None	6
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	Veh	Veh	Veh	None	None	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	49	50	51	0	0	64	52	53	54	55	56
Device	C1S	Virt	Virt	None	None	Virt	Virt	None	Virt	Virt	Virt
Pin	23	103	104	None	None	4	4	None	5	1	8
Chan1	0	114	115	6	15	0	0	121	0	0	0
Chan2	0	0	0	7	16	0	0	0	0	0	0
Logic	And	And	And	Or	Or	And	And	Or	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	57	58	59	60	61	62	63
Device	Virt	Virt	Virt	None	None	Virt	Virt
Pin	25	28	24	None	None	18	14
Chan1	0	0	0	10	24	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	Or	Or	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovlpd	ovlpd	ovlpd
FctnIndx	1	2	3	4	5	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovlpd	ovl	ovl	ovl	ovl	spec1	spec1	spec1	spec1	spec1	spec1
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	check	check	phson	phson
FctnIndx	0	0	0	0	0	0	0	8	4	18	14
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	Virt	Virt	Virt	Virt
Pin1	None	None	None	None	None	None	None	18	14	28	24
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	phson	phson	phson	phson	phson	ovl	ovl	none	none	none	none
FctnIndx	1	25	8	5	4	19	20	0	0	0	0
Device1	Virt	Virt	Virt	Virt	Virt	None	None	None	None	None	None
Pin1	1	25	8	5	4	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	Virt	Virt	None	None	None	None
Pin3	None	None	None	None	None	103	104	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

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Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	RR and EV Preempts	GTSI	01/10/00 07:53:31	unknown	04/06/11 11:23:23

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X																			
InitDwell						X														
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell					X															
InitDwell		X																		
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell					X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell								X												
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell				X																
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1	X																			
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	dC Overlaps	GTSI	01/17/00 18:35:32	unknown	10/30/08 11:51:34

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X																		
Secondary Parent																				
Mod Phase		X																		

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X	X								X	X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X			X							X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X										X						
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X															
Secondary Parent																				
Mod Phase		X																		

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X															
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X				X					X							X		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X										X		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X		X	X	X		X			X			X	X			X		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent				X	X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X		X	X	X		X			X			X	X			X		
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent				X	X			X							X					
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X	X	X											X	X
Ped		X		X		X		X												
YelFlash																				
AltFlsHz			X	X																
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	162	162
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coordination Configuration	trf_ctrl	08/23/01 16:25:18	unknown	03/18/06 15:23:13

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	01/31/05 21:05:59	trf_ctrl	01/31/05 21:05:59

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood Dr. @ Woodland Grove/Trai
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	aescoba
Modified On	05/24/10 16:40:16

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	2: 144" AM Peak
Free Plan	1: 100" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 11:34:35

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTSI	01/17/00 20:39:24	aescoba	05/24/10 16:39:28
2	144" AM Peak	guest	09/05/2002 12:22:55	unknown	03/29/11 08:45:45
3	150" PM Peak	guest	09/05/2002 12:26:16	unknown	03/30/11 16:29:18
4	120" Midday	guest	09/05/2002 12:36:06	aescoba	05/24/10 16:39:44

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: WBLT Leads	1: WBLT Leads	1: Overlap Data	1:	1: Woodland Grove

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	15																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	10	15	10	30	10	15	0	0	0	0	0	0	0
CordNom	20	45	10	25	15	50	10	25	0	0	0	0	0	0	0
CordMax	30	50	10	30	20	60	10	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	10	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	2: EBLT Leads	2: EBLT Leads	1: Overlap Data	1:	1: Woodland Grove

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	20																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	35	15	20	20	35	15	20	0	0	0	0	0	0	0
CordNom	30	60	25	29	30	60	25	29	0	0	0	0	0	0	0
CordMax	40	70	30	40	40	70	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	2: EBLT Leads	2: EBLT Leads	1: Overlap Data	1:	1: Woodland Grove

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	20																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	55	7	20	25	50	7	20	0	0	0	0	0	0	0
CordNom	15	100	10	25	40	75	10	25	0	0	0	0	0	0	0
CordMax	20	110	15	40	50	100	15	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	65	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: WBLT Leads	1: WBLT Leads	1: Overlap Data	1:	1: Woodland Grove

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	35	10	20	15	30	10	20	0	0	0	0	0	0	0
CordNom	15	65	15	25	25	55	15	25	0	0	0	0	0	0	0
CordMax	20	70	20	40	30	60	20	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	10	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WBLT Leads	GTSI	08/20/99 09:36:34	unknown	09/24/02 18:07:21
2	EBLT Leads	guest	09/05/2002 12:20:37	unknown	09/24/02 18:07:00

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	guest	09/24/2002 15:32:56

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Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	8.0	8.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.5	3.5	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	1.5	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	5	5	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WBLT Leads	GTSI	01/17/00 20:39:24	unknown	03/29/11 17:10:14
2	EBLT Leads	guest	09/24/2002 13:00:40	unknown	03/29/11 17:11:40

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl																				
MaxRcl		X				X														
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest						X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X																			

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	10	1	5	3	2	1	5	0	0	0	0	0	0	0
Passage	3.0	3.5	0.0	2.0	3.0	1.0	0.0	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	40	1	20	40	20	1	20	0	0	0	0	0	0	0
Max2	40	60	1	40	60	40	1	40	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	3.5	3.5	4.0	4.0	3.5	3.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	1.0	1.5	1.5	2.0	9.0	9.5	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	4	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	20	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	5	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl																				
MaxRcl		X				X														
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest						X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X																			

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Phase Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 2 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	10	1	5	3	2	1	5	0	0	0	0	0	0	0
Passage	3.0	3.5	0.0	2.0	3.0	1.0	0.0	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	45	1	30	25	45	1	30	0	0	0	0	0	0	0
Max2	35	75	1	45	40	75	1	45	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	3.5	3.5	4.0	4.0	3.5	3.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	9.0	9.5	1.5	2.0	1.0	1.5	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	4	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	20	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/13/05 12:05:28	rwelborn	07/13/05 12:09:09

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Woodland Grove	GTSI	01/17/00 20:39:23	guest	09/20/2002 19:27:20

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Active Config Table 1 Params

Phase	1: Phase Config
Overlap	1: Woodland Grove
Preempt	1: Preempts
Cabinet	1: input settings/output settings
Detector	1: Detectors
Coordination	1: GPS Coordination
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Config	GTSI	01/17/00 18:35:33	guest	09/24/2002 14:10:49

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup			X					X												
YelStart			X					X												
RedStart																				
SecStart																				
Ped						X														
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

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Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	2
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTSI	08/24/99 16:13:16	jclark	05/09/11 14:07:21

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Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X			X				
Call	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X			X				X
Extend	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X			X				X
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable								X		X													
Call	X	X		X	X	X		X		X													
Extend	X	X		X	X	X		X		X													
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable									X	X								
Call									X	X								
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	2	2	2	3	4	4	5	6	6	6	6	7	7
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	8	0	0	8	0	0	0	2	5	4	0	6	1	8	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	3	0	7	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	7	3	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

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Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	thomasr	02/27/06 13:28:27

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	5	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	Veh	Veh	Veh	Veh	Veh	Veh	None	None
FctnIndx	0	0	0	51	52	53	54	55	56	0	0
Device	None	None	None	Virt	Virt	Virt	Virt	Virt	Virt	None	None
Pin	None	None	None	4	8	14	18	24	28	None	None
Chan1	0	0	0	0	0	0	0	81	82	0	0
Chan2	0	0	0	0	0	0	0	83	84	0	0
Logic	And	And	And	And	And	And	And	Or	Or	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	ovl	phase	phase	phase	ovl	phase	ped	ped	ped
FctnIndx	1	2	1	4	5	6	2	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

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Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	none	none	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	phson	phson	phsnxt	phsnxt	check
FctnIndx	0	0	0	0	0	0	4	8	4	8	4
Device1	None	None	None	None	None	None	Virt	Virt	Virt	Virt	Virt
Pin1	None	None	None	None	None	None	4	8	14	18	24
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	check	none	none	none	none	none	none	none	none	none	none
FctnIndx	8	0	0	0	0	0	0	0	0	0	0
Device1	Virt	None	None	None	None	None	None	None	None	None	None
Pin1	28	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	unknown	02/27/06 15:47:54

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Woodland Grove	GTSI	01/17/00 18:35:32	guest	09/24/2002 12:38:41

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X			X														
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X					X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X																		
Ped																				
YelFlash																				
AltFlsHz	X	X																		
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	GPS Coordination	trf_ctrl	08/23/01 16:25:18	awrigley	02/21/06 08:51:40

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

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Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:42:52
Modified By	rreevey
Modified On	03/19/14 15:42:52

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

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NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:42:52
Modified By	rreevey
Modified On	03/19/14 15:42:52

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:42:52
Modified By	rreevey
Modified On	03/19/14 15:42:52

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:42:52
Modified By	rreevey
Modified On	03/19/14 15:42:52

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	Loop 494 @ Northpark Dr.
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	msmith2
Modified On	07/22/10 08:25:13

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: OFF PEAK 100
Free Plan	1: OFF PEAK 100
Default Schedule	1: TSTOP SCHEDULE

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	TSTOP SCHEDULE	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 13:41:56

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	15	0		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	OFF PEAK 100
2																	AM PEAK 144
3																	OFF PEAK 100
4																	MIDDAY 120
5																	MIDDAY 120
6																	PM PEAK 150
7																	OFF PEAK 100
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: TSTOP SCHEDULE	
2		1	1								1: TSTOP SCHEDULE	
3		1	1								1: TSTOP SCHEDULE	
4		1	1								1: TSTOP SCHEDULE	
5		1	1								1: TSTOP SCHEDULE	
6		1	1								1: TSTOP SCHEDULE	
7		1	1								1: TSTOP SCHEDULE	
8		1	1								1: TSTOP SCHEDULE	
9		1	1								1: TSTOP SCHEDULE	
10		1	1								1: TSTOP SCHEDULE	
11		1	1								1: TSTOP SCHEDULE	
12		1	1								1: TSTOP SCHEDULE	
13		1	1								1: TSTOP SCHEDULE	
14		1	1								1: TSTOP SCHEDULE	
15		1	1								1: TSTOP SCHEDULE	
16		1	1								1: TSTOP SCHEDULE	
17		1	1								1: TSTOP SCHEDULE	
18		1	1								1: TSTOP SCHEDULE	
19		1	1								1: TSTOP SCHEDULE	
20		1	1								1: TSTOP SCHEDULE	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: TSTOP SCHEDULE	
2		1	1								1: TSTOP SCHEDULE	
3		1	1								1: TSTOP SCHEDULE	
4		1	1								1: TSTOP SCHEDULE	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: TSTOP SCHEDULE	
6		1	1								1: TSTOP SCHEDULE	
7		1	1								1: TSTOP SCHEDULE	
8		1	1								1: TSTOP SCHEDULE	
9		1	1								1: TSTOP SCHEDULE	
10		1	1								1: TSTOP SCHEDULE	
11		1	1								1: TSTOP SCHEDULE	
12		1	1								1: TSTOP SCHEDULE	
13		1	1								1: TSTOP SCHEDULE	
14		1	1								1: TSTOP SCHEDULE	
15		1	1								1: TSTOP SCHEDULE	
16		1	1								1: TSTOP SCHEDULE	
17		1	1								1: TSTOP SCHEDULE	
18		1	1								1: TSTOP SCHEDULE	
19		1	1								1: TSTOP SCHEDULE	
20		1	1								1: TSTOP SCHEDULE	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	OFF PEAK 100	unknown	10/11/05 17:44:26	unknown	10/20/10 09:22:19
2	AM PEAK 144	awrigley	06/25/08 15:12:01	unknown	11/17/10 10:19:33
3	PM PEAK 150	awrigley	06/25/08 15:12:06	msmith2	07/22/10 09:12:53
4	MIDDAY 120	awrigley	06/25/08 15:12:09	msmith2	07/22/10 09:13:09

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: Standard Leading Lefts	1: Overlap Data	1: Bus Priority

Plan Table 1 Params

Config	Cycle Length	Permissive	SpCfcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: [COH] Active Configuration	100	20																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold																				

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	15	20	15	20	15	20	0	0	0	0	0	0	0
CordNom	20	33	20	27	20	33	25	22	0	0	0	0	0	0	0
CordMax	30	40	30	40	30	40	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	82	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: Standard Leading Lefts	1: Overlap Data	1: Bus Priority

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Plan Table 2 Params

Config	Cycle Length	Permissive	SpCfn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: [COH] Active Configuration	144	25																

Plan Table 2 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold																				

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	40	20	20	15	40	20	15	0	0	0	0	0	0	0
CordNom	25	74	22	23	25	74	27	18	0	0	0	0	0	0	0
CordMax	40	80	40	40	30	80	40	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	15	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: Standard Leading Lefts	1: Overlap Data	1: Bus Priority

Plan Table 3 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: [COH] Active Configuration	150	25																

Plan Table 3 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold																				

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	40	15	20	15	40	15	20	0	0	0	0	0	0	0
CordNom	20	70	25	35	20	70	25	35	0	0	0	0	0	0	0
CordMax	30	80	30	40	30	80	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: Standard Leading Lefts	1: Overlap Data	1: Bus Priority

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: [COH] Active Configuration	120	20	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold																				

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	15	20	15	25	15	20	0	0	0	0	0	0	0
CordNom	20	45	25	30	20	45	25	30	0	0	0	0	0	0	0
CordMax	30	50	30	40	30	50	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	110	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard Leading Lefts	GTSI	08/20/99 09:36:34	awrigley	04/25/07 21:06:53

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	awrigley	04/25/07 21:06:29

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	unknown	05/20/11 14:01:46

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl									X	X										
SftRcl																				
ConSvc																				
PedRcl		X		X		X		X												
DualEnt		X		X		X		X												
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X	X	X	X	X	X	X	X												

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	7	12	5	5	5	12	5	5	0	0	0	0	0	0	0
Passage	2.5	2.0	2.0	4.5	2.5	2.0	2.5	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	25	50	30	35	25	50	30	35	0	0	0	0	0	0	0
Max2	30	75	30	40	30	75	35	40	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.3	4.3	4.0	4.0	4.3	4.3	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	External Plans
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	awrigley
Modified On	04/25/07 21:06:08

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X	X	X	X						ExtPlan	4	Dial 4
5	X									Schedule	0	Return to Schedule
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Bus Priority	GTS	09/10/04 14:08:44	unknown	07/02/08 07:43:23

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	[COH] Active Configuration	GTSI	01/17/00 20:39:23	awrigley	04/25/07 17:16:31

Active Config Table 1 Params

Phase	1: Rings & IDs
Overlap	1: Overlap Config
Preempt	1: Trains & Fire !
Cabinet	1: [COH] 332 Cabinet
Detector	1: [COH] 332 Detectors
Coordination	1: [COH] Coordination Parameters
Peer	1: Peer Functions

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Rings & IDs	unknown	10/11/05 11:32:27	unknown	09/22/10 09:37:06

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	ebt	nbl	sbt	none	wbt	sbl	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup		X				X														
YelStart		X				X														
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	[COH] 332 Detectors	GTS	09/08/04 20:13:18	unknown	05/20/11 13:57:58

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable					X	X	X	X	X	X	X		X		X	X							
Call	X			X	X	X	X	X	X	X	X		X		X	X							
Extend	X			X	X	X	X	X	X	X	X		X		X	X							
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	5	0	0	1	4	4	8	8	7	7	3	3	4	0	1
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	5	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	[COH] 332 Cabinet	GTS	09/08/04 20:13:17	unknown	03/07/11 09:54:29

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	PlnSel	PlnSel	Advanc	MCE
FctnIndx	12	13	14	15	16	17	18	1	2	1	1
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C11S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	10	11	80	53
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Ped	Ped	Ped	Ped	Flash	StopTm	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	1	1	19	20	21	22	23
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S
Pin	67	69	68	70	81	82	55	19	40	44	64
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	24	25	26	27	28	29	30	31	32	33	34
Device	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S
Pin	77	48	20	57	21	42	46	66	79	50	22
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Veh	Veh	PlnSel	PlnSel	None	TimSet	Prempt	Prempt	Prempt	Prempt	Prempt
FctnIndx	35	36	3	4	1	1	2	3	4	5	1
Device	C1S	C1S	C11S	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	59	61	12	13	54	52	71	73	72	74	51
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	NAnd	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	None	None	None	None
FctnIndx	0	0	0	2	3	4	5	0	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	None	None	None	None
Pin	None	None	None	71	73	72	74	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	phase	phase	phase	phase	phase	ped	ped	ped
FctnIndx	1	2	3	4	5	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	none	none	none	none	none	detrst
FctnIndx	8	1	2	3	4	1	2	3	4	5	1
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	93	83	84	100	102
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	fl/wd	none	none	none	none	none	none	none	none	none	none
FctnIndx	1	0	0	0	0	0	0	0	0	0	0
Device1	C1S	None	None	None	None	None	None	None	None	None	None
Pin1	103	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Trains & Fire !	GTSI	01/10/00 07:53:31	unknown	08/19/10 13:02:38

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable	X									
NoMemLock	X									
ManEnable										
NoOvrFlash										
NoOvrNext										
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1	X					X														
Entry2																				
Dwell			X	X	X			X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit	X					X														

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X			X													

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.5	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	8	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	10	10	10	10	10	0	0	0	0	0
DwellExt	4	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Config	GTSI	01/17/00 18:35:32	awrigley	04/25/07 17:13:23

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable																				
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On
1	[COH] Coordination Parameters	trf_ctrl	08/23/01 16:25:18

Coordination Table Info

Table	Modified By	Modified On
1	unknown	03/07/11 10:09:57

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	1	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				1

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Peer Functions	trf_ctrl	07/30/03 12:20:08	awrigley	04/25/07 17:15:59

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	trf_ctrl
Created On	08/23/01 14:47:16
Modified By	unknown
Modified On	05/20/11 13:55:10

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	120
Data Filter	X
Key Repeat	X

Admin Dat/Tim DST

Zone	Ctr
DST	X

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NextPhase 1.7.5 Admin Comm

Description	
Created By	unknown
Created On	07/09/04 12:15:43
Modified By	unknown
Modified On	05/20/11 14:05:03

Admin Comm Serial

Port	1	2	3	4
Mode	PMPP	None	None	PMPP
PMPP Address	1	0	0	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	9600	9600	9600	19200
Parity	None	None	None	None
Data Bits	8	8	8	8
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Fcn	Pri	Pri	Pri	Pri	None	None	None	None	None	None	None	None	None	None	None	None	None	None	None	None
Idx	2	3	4	5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	48	48	49	49	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Chan	A	B	A	B	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A

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Admin Comm Timeout

Unit Backup Timeout Seconds

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	trf_ctrl
Created On	08/05/03 16:06:17
Modified By	trf_ctrl
Modified On	08/05/03 16:06:17

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	unknown
Created On	11/13/07 14:02:37
Modified By	unknown
Modified On	11/13/07 14:07:29

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
UserId	1	2	3	4																									
Password	1	2	3	4																									
Name	Level 1	Level 2	Level 3	Level 4																									
View	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

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Admin Security UserList

User	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62
UserId																																	
Password																																	
Name																																	
View	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

Admin Security UserList

User	63	64
UserId		
Password		
Name		
View	4	4
Edit	4	4

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Control Table Info

Description	Chestnut Ridge @ Kingwood Dr.
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	03/09/06 15:41:31

NextPhase 1.7.0 Control Params

Control Mode	scheduler
Manual Plan	<TABLE 3 DOES NOT EXIST>
Free Plan	1: 100" Off Peak (new)
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	GTSI	08/20/99 09:36:34	unknown	04/06/11 10:29:47

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	5
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (new)	GTSI	01/17/00 20:39:24	awrigley	03/07/06 08:31:09
2	144" AM Peak (new)	guest	09/05/2002 12:22:55	unknown	03/30/11 17:56:04
4	120" Midday (new)	guest	09/05/2002 12:36:06	unknown	03/31/06 13:04:20
5	PM single -GdeC 3/2011	unknown	03/30/11 16:42:00	unknown	03/31/11 16:48:30

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	2: WB Lag	2: WB Lag	1: Chestnut Ridge OL's	1:

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Chestnut Ridge Configuration	100	20																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																			X	
Hold																			X	

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	12	20	0	12	12	0	0	10	0	0	0	0	0	0	5
CordNom	15	30	0	15	15	0	0	15	0	0	0	0	0	0	5
CordMax	25	40	0	25	25	0	0	20	0	0	0	0	0	0	5
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	35	20
CordNom	0	0	0	0	0	0	0	0	65	35
CordMax	0	0	0	0	0	0	0	0	70	40
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	10	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EB Lag	1: EB Lag	1: Chestnut Ridge OL's	1:

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Plan Table 2 Params

Config	Cycle Length	Permissive	SpCfcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Chestnut Ridge Configuration	144	20																

Plan Table 2 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																			X	
Hold																			X	

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	35	0	15	10	0	0	15	0	0	5	0	0	0	0
CordNom	15	60	0	20	15	0	0	24	0	0	5	0	0	0	0
CordMax	20	70	0	30	20	0	0	30	0	0	5	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	50	30
CordNom	0	0	0	0	0	0	0	0	95	49
CordMax	0	0	0	0	0	0	0	0	100	60
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	2: WB Lag	2: WB Lag	1: Chestnut Ridge OL's	1:

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Chestnut Ridge Configuration	120	20																

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																			X	
Hold																			X	

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	0	10	15	0	0	15	0	0	0	0	0	0	5
CordNom	20	35	0	15	20	0	0	20	0	0	0	0	0	0	5
CordMax	30	40	0	20	30	0	0	30	0	0	0	0	0	0	5
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	5	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	45	25
CordNom	0	0	0	0	0	0	0	0	80	40
CordMax	0	0	0	0	0	0	0	0	90	50
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EB Lag	1: EB Lag	1: Chestnut Ridge OL's	1:

Plan Table 5 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: Chestnut Ridge Configuration	150	20	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 5 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

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Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																			X	
Hold																			X	

Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	0	15	15	0	0	15	0	0	5	0	0	0	0
CordNom	19	56	0	20	25	0	0	20	0	0	5	0	0	0	0
CordMax	30	70	0	30	30	0	0	30	0	0	5	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	5	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	65	30
CordNom	0	0	0	0	0	0	0	0	105	49
CordMax	0	0	0	0	0	0	0	0	130	60
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	95	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	EB Lag	GTSI	08/20/99 09:36:34	awrigley	02/17/06 13:09:31
2	WB Lag	guest	09/05/2002 12:20:37	awrigley	02/17/06 13:10:13

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Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	3	0	7	4	0	0	5	0	0	2	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	8	0	0	0	6	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	4	3	0	5	1	0	0	7	0	0	0	0	0	0	2	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	6	0	0	0	8	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Chestnut Ridge OL's	GTSI	01/17/00 18:35:32	unknown	05/12/11 12:40:20

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	5	5	5	5	5	5	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	3.5	4.0	4.0	4.0	3.5	4.0	3.0	3.0
RedClear	0.0	1.0	1.5	2.0	2.0	0.0	1.5	2.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

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Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	EB Lag	GTSI	01/17/00 20:39:24	unknown	05/17/11 12:09:07
2	WB Lag	guest	09/05/2002 12:16:28	unknown	05/17/11 12:07:54

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit						X								X	X			X		
PedOmit																				
MinRcl																				
MaxRcl		X																		
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X			X															
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit				X	X										X					
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	2	10	0	4	2	10	0	4	0	0	1	0	0	0	0
Passage	1.5	3.0	0.0	1.7	1.5	0.0	0.0	1.7	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	20	0	20	20	0	0	20	0	0	1	0	0	0	1
Max2	40	50	0	40	40	0	0	40	0	0	1	0	0	0	1
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5
RedClr	2.0	2.0	2.0	2.0	1.0	1.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	1.0	1.5
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	2	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	20	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	40	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	3.5	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.5	2.0	2.0	2.0	0.5	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	1	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	1	0	0	0	0
Max2	0	0	0	0	0	1	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	0.5	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit						X					X			X				X		
PedOmit																				
MinRcl																				
MaxRcl		X																		
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X																		
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit					X			X							X					
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 2 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	2	10	0	4	2	10	0	4	0	0	1	0	0	0	0
Passage	1.0	3.0	0.0	2.0	1.0	1.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	20	0	20	20	20	0	20	0	0	1	0	0	0	1
Max2	40	50	0	40	40	40	0	40	0	0	1	0	0	0	1
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5
RedClr	2.0	2.0	2.0	2.0	1.0	1.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	1.0	1.5
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	2	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	20	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	40	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	3.5	4.0	4.0	4.0	3.5	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.5	2.0	2.0	2.0	0.5	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	1	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	1	0	0	85	45
Max2	0	0	0	0	0	1	0	0	85	45
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	11/29/05 20:43:44
Modified By	trf_ctrl
Modified On	11/29/05 20:43:44

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/29/05 20:43:44	unknown	02/22/06 05:57:46

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On
1	Chestnut Ridge Configuration	GTSI	01/17/00 20:39:23

Active Config Table Info

Table	Modified By	Modified On
1	awrigley	11/29/05 21:57:17

Active Config Table 1 Params

Phase	1: deCamp Chestnut Ridge
Overlap	1: Chestnut Ridge Overlaps
Preempt	1: Preempts
Cabinet	1: Basic 170_332
Detector	1: Loops Channels 19 to 32
Coordination	1: Coordination Standard
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	deCamp Chestnut Ridge	GTSI	01/17/00 18:35:33	jclark	05/17/11 12:29:15

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X																
YelStart				X																
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz	X	X			X	X					X	X			X	X				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	2
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	5,15,2,1,11,a,40,b
2	39,a,4,24,6,8,28,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Loops Channels 19 to 32	GTSI	08/24/99 16:13:16	jclark	05/17/11 13:17:24

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																			X	X	X	X	X
Call	X	X	X	X	X	X	X	X	X	X	X								X	X	X	X	X
Extend	X	X	X	X	X	X	X	X	X	X	X								X	X	X	X	X
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X	X	X	X	X	X	X	X	X								X	X	X	X			
Call	X	X	X	X	X	X	X	X	X								X	X	X	X			
Extend	X	X	X	X	X	X	X	X	X														
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	5	4	0	6	1	8	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	3	0	7	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	1	5	4	4	4	4	3	3	8	8	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	1	5	4	4	4	4	0	0	8	8	8	8
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	7	7	0	0	0	0	0	0	0	11	15	24	28	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	3	7	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	8	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

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Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	GTSI	08/20/99 09:36:29	jclark	05/17/11 12:53:43

Cabinet Table 1 Cabinet Type

Cabinet Type	170
Unit Address 1	auto
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	1	1	1	2	3	4	5
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Unit	1	none	none	none	none	none	none	none	none	none	none
Conn/BIU	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NOr	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Unit	none	none	none	1	1	1	1	1	none	none	none
Conn/BIU	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	Veh	Veh	Veh	Veh	None	None	None	None	None
FctnIndx	0	0	40	41	42	43	0	0	0	0	0
Unit	none	none	1	1	1	1	none	none	none	none	none
Conn/BIU	None	None	Virt	Virt	Virt	Virt	None	None	None	None	None
Pin	None	None	1	5	4	8	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	Or	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ovl	ped	ped	ped
FctnIndx	1	2	3	4	5	6	7	8	2	4	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Conn/BIU2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	35	37	36	38	102	103	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	none	none	none	none
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	phson	phson
FctnIndx	0	0	0	0	0	0	0	0	0	5	1
Unit	none	none	none	none	none	none	none	none	none	1	1
Conn/BIU1	None	None	None	None	None	None	None	None	None	Virt	Virt
Pin1	None	None	None	None	None	None	None	None	None	5	1
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	phson	phson	phson	none	none	none	none
FctnIndx	4	25	8	0	0	0	0
Unit	1	1	1	none	none	none	none
Conn/BIU1	Virt	Virt	Virt	None	None	None	None
Pin1	4	25	8	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	bushekn	03/02/06 15:24:42

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Chestnut Ridge Overlaps	GTSI	01/17/00 18:35:32	awrigley	03/07/06 08:27:44

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X	X									X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X			X							X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X															
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X															
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X				X					X									
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X												
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X	X	X												
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coordination Standard	GTSI	08/20/99 09:36:32	unknown	02/24/09 09:08:18

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/29/05 20:43:45	trf_ctrl	11/29/05 20:43:45

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood Dr @ Woodland Hills
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	04/02/08 11:11:09

NextPhase 1.7.2 Control Params

Control Mode	scheduler
Manual Plan	3: 150" PM Peak
Free Plan	<TABLE 10 DOES NOT EXIST>
Default Schedule	1: Kingwood Schedule (plus Church)

Schedule Table Info

Table	Description	Created By	Created On
1	Kingwood Schedule (plus Church)	trf_ctrl	08/23/01 15:52:32

Schedule Table Info

Table	Modified By	Modified On
1	jclark	05/24/11 12:03:43

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	8	0	X							sched	6
4	X	9	0		X	X	X	X	X		sched	1
5	X	9	0							X	sched	5
6	X	11	0		X	X	X	X	X		sched	5
7	X	13	0	X							sched	5
8	X	14	0		X	X	X	X	X		sched	4
9	X	16	30		X	X	X	X	X		sched	3
10	X	19	30	X	X	X	X	X	X	X	sched	1
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	
4																	Off Peak
5																	Midday (Weekend)
6																	Midday
7																	
8																	PM Peak
9																	Off Peak
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
31																	
32																	
33																	
34																	
35																	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule (plus Church)	
2		1	1								1: Kingwood Schedule (plus Church)	
3		1	1								1: Kingwood Schedule (plus Church)	
4		1	1								1: Kingwood Schedule (plus Church)	
5		1	1								1: Kingwood Schedule (plus Church)	
6		1	1								1: Kingwood Schedule (plus Church)	
7		1	1								1: Kingwood Schedule (plus Church)	
8		1	1								1: Kingwood Schedule (plus Church)	
9		1	1								1: Kingwood Schedule (plus Church)	
10		1	1								1: Kingwood Schedule (plus Church)	
11		1	1								1: Kingwood Schedule (plus Church)	
12		1	1								1: Kingwood Schedule (plus Church)	
13		1	1								1: Kingwood Schedule (plus Church)	
14		1	1								1: Kingwood Schedule (plus Church)	
15		1	1								1: Kingwood Schedule (plus Church)	
16		1	1								1: Kingwood Schedule (plus Church)	
17		1	1								1: Kingwood Schedule (plus Church)	
18		1	1								1: Kingwood Schedule (plus Church)	
19		1	1								1: Kingwood Schedule (plus Church)	
20		1	1								1: Kingwood Schedule (plus Church)	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule (plus Church)	
2		1	1								1: Kingwood Schedule (plus Church)	
3		1	1								1: Kingwood Schedule (plus Church)	
4		1	1								1: Kingwood Schedule (plus Church)	
5		1	1								1: Kingwood Schedule (plus Church)	
6		1	1								1: Kingwood Schedule (plus Church)	
7		1	1								1: Kingwood Schedule (plus Church)	
8		1	1								1: Kingwood Schedule (plus Church)	
9		1	1								1: Kingwood Schedule (plus Church)	
10		1	1								1: Kingwood Schedule (plus Church)	
11		1	1								1: Kingwood Schedule (plus Church)	
12		1	1								1: Kingwood Schedule (plus Church)	
13		1	1								1: Kingwood Schedule (plus Church)	
14		1	1								1: Kingwood Schedule (plus Church)	
15		1	1								1: Kingwood Schedule (plus Church)	
16		1	1								1: Kingwood Schedule (plus Church)	
17		1	1								1: Kingwood Schedule (plus Church)	
18		1	1								1: Kingwood Schedule (plus Church)	
19		1	1								1: Kingwood Schedule (plus Church)	
20		1	1								1: Kingwood Schedule (plus Church)	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTSI	01/17/00 20:39:24	tbales	10/01/07 16:37:46
2	144" AM Peak	awrigley	11/29/05 13:53:58	unknown	03/31/11 08:23:51
3	150" PM Peak	awrigley	11/29/05 13:54:02	unknown	03/30/11 17:39:07
4	150" PM Peak (Early)	awrigley	11/29/05 13:54:05	unknown	03/30/11 15:54:03
5	120" Midday	tbales	10/01/07 16:37:06	tbales	10/02/07 15:25:33
6	120' Church Plan	tbales	12/12/08 09:32:39	tbales	12/12/08 09:32:53

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max1	seconds	1: Phase Data	1: Off Peak	1: Overlap Data	1:	1:	100

Plan Table 1 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	10	15	15	25	10	15	0	0	0	0	0	0	0
CordNom	20	45	15	20	25	40	15	20	0	0	0	0	0	0	0
CordMax	30	50	20	30	30	50	20	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	50	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	2: AM Peak	1: Overlap Data	1:	1:	144

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Plan Table 2 Params

Permissive	SpCfcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	40	15	20	20	40	10	20	0	0	0	0	0	0	0
CordNom	24	66	24	30	25	65	19	35	0	0	0	0	0	0	0
CordMax	30	80	30	40	40	80	20	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	2: Phase Data PM Peak	3: PM Peak	1: Overlap Data	1:	1:

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	10																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X									X					
Hold		X													X					

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	45	15	20	15	30	15	20	0	0	0	0	0	0	20
CordNom	30	75	20	25	25	50	20	25	0	0	0	0	0	0	30
CordMax	40	90	30	40	30	60	30	40	0	0	0	0	0	0	40
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	90	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	4: PM (Early)	1: Overlap Data	1:	1:	150

Plan Table 4 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	45	15	20	15	40	15	20	0	0	0	0	0	0	10
CordNom	30	75	20	25	33	60	20	25	0	0	0	0	0	0	12
CordMax	40	90	30	40	40	80	30	40	0	0	0	0	0	0	20
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	5: Midday	1: Overlap Data	1:	1:	120

Plan Table 5 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	15	10	20	30	10	15	0	0	0	0	0	0	0
CordNom	25	60	20	15	35	50	15	20	0	0	0	0	0	0	0
CordMax	30	70	30	20	40	60	20	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 6 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max2	seconds	1: Phase Data	5: Midday	1: Overlap Data	1:	1:	120

Plan Table 6 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

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Plan Table 6 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 6 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 6 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	15	10	20	30	15	10	0	0	0	0	0	0	0
CordNom	25	50	20	25	35	40	30	15	0	0	0	0	0	0	0
CordMax	30	70	30	20	40	60	50	20	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 6 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 6 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 6 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 6 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	08/20/99 09:36:34	tbales	10/02/07 15:23:46
2	AM Peak	guest	09/26/2002 10:00:33	tbales	10/02/07 15:24:56
3	PM Peak	guest	09/26/2002 10:12:02	tbales	10/02/07 15:23:50
4	PM (Early)	awrigley	11/30/05 00:37:19	unknown	03/30/11 15:56:52
5	Midday	unknown	12/01/05 08:09:28	tbales	10/02/07 15:25:18

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 5 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 5 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	awrigley	11/29/05 14:02:05

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	jclark	05/17/11 14:37:15
2	Phase Data PM Peak	awrigley	11/29/05 14:00:18	jclark	05/17/11 14:37:54

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit															X					
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl		X				X														
DualEnt		X		X		X		X												
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock		X				X														
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X		X		X		X													

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	10	5	5	5	10	5	5	0	0	0	0	0	0	0
Passage	2.5	3.5	2.5	2.5	2.5	3.5	2.5	2.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	30	45	30	30	30	45	30	30	0	0	0	0	0	0	0
Max2	30	55	30	30	30	55	20	40	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	6	0	6	0	6	0	6	0	0	0	0	0	0	0
PedClr	0	20	0	20	0	20	0	20	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl						X														
DualEnt		X		X		X		X												
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X																		
PedRcycl																				
RedLock																				
YelLock		X				X														
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X		X		X		X													

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Phase Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 2 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	10	5	5	7	10	5	5	0	0	0	0	0	0	4
Passage	2.5	3.5	2.5	2.5	2.5	3.5	2.5	2.5	0.0	0.0	0.0	0.0	0.0	0.0	2.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	30	55	20	25	30	55	20	25	0	0	0	0	0	0	35
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	4.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	2.0
Walk	0	6	0	6	0	6	0	6	0	0	0	0	0	0	0
PedClr	0	20	0	20	0	20	0	20	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	Glen Andrus
Modified On	09/14/2004 14:39:59

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Return to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/02/05 11:24:18	bushekn	11/02/05 11:26:30

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:23	TransCore	10/31/2005 11:51:15

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Active Config Table 1 Params

Phase	1: Phase Configuration
Overlap	1: Overlap
Preempt	1: Preempt
Cabinet	1: input settings/output settings
Detector	1: Loops 1,2,9-12,19,20,27-30
Coordination	1:
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Configuration	GTSI	01/17/00 18:35:33	awrigley	11/29/05 13:59:58

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

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Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	2
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,4,b
2	5,6,15,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Loops 1,2,9-12,19,20,27-30	GTSI	08/24/99 16:13:16	jclark	05/17/11 14:34:37

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Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X							X	X	X	X							X	X			
Call	X	X					X	X	X	X	X	X			X	X			X	X			
Extend	X	X					X	X	X	X	X	X			X	X			X	X			
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable				X	X	X	X																
Call		X	X	X	X	X	X			X	X												
Extend		X	X	X	X	X	X			X	X												
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		X
Call																		X
Extend																		X
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	0	0	0	0	7	4	3	3	4	4	0	0	3
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	8	0	0	5	5	0	0	0	0	0	2	7	7	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	3	8	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	15
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

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Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	awrigley	06/06/08 12:13:05

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	Or	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	5	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	Veh
FctnIndx	0	0	0	0	0	0	64
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	21
Chan2	0	0	0	0	0	0	22
Logic	And	And	And	And	And	And	Or

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	phase	ovl	phase	phase	phase	ped	ped	ped
FctnIndx	1	2	3	4	1	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempt	GTSI	01/10/00 07:53:31	bushekn	11/02/05 11:35:38

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap	GTSI	01/17/00 18:35:32	awrigley	11/29/05 14:01:28

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X																			
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	awrigley	03/29/06 14:46:11

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/12/04 09:48:01	trf_ctrl	11/12/04 09:48:01

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextEdit

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Control Table Info

Description	Kingwood @ Valley Manor
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	03/22/10 14:25:04

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	4: 120" Midday (new)
Free Plan	<TABLE 10 DOES NOT EXIST>
Default Schedule	1: Table 01

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	guest	07/07/2004 23:23:53	unknown	04/06/11 12:07:12

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X		X		sched	1
4	X	9	0	X						X	sched	4
5	X	9	30					X			sched	1
6	X	11	0		X	X	X	X	X		sched	4
7	X	14	30		X	X	X	X	X		sched	3
8	X	19	30	X	X	X	X	X	X	X	sched	1
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Table 01	
2		1	1								1: Table 01	
3		1	1								1: Table 01	
4		1	1								1: Table 01	
5		1	1								1: Table 01	
6		1	1								1: Table 01	
7		1	1								1: Table 01	
8		1	1								1: Table 01	
9		1	1								1: Table 01	
10		1	1								1: Table 01	
11		1	1								1: Table 01	
12		1	1								1: Table 01	
13		1	1								1: Table 01	
14		1	1								1: Table 01	
15		1	1								1: Table 01	
16		1	1								1: Table 01	
17		1	1								1: Table 01	
18		1	1								1: Table 01	
19		1	1								1: Table 01	
20		1	1								1: Table 01	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Table 01	
2		1	1								1: Table 01	
3		1	1								1: Table 01	
4		1	1								1: Table 01	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Table 01	
6		1	1								1: Table 01	
7		1	1								1: Table 01	
8		1	1								1: Table 01	
9		1	1								1: Table 01	
10		1	1								1: Table 01	
11		1	1								1: Table 01	
12		1	1								1: Table 01	
13		1	1								1: Table 01	
14		1	1								1: Table 01	
15		1	1								1: Table 01	
16		1	1								1: Table 01	
17		1	1								1: Table 01	
18		1	1								1: Table 01	
19		1	1								1: Table 01	
20		1	1								1: Table 01	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (new)	GTSI	01/17/00 20:39:24	aescoba	03/22/10 13:52:30
2	144" AM Peak (new)	awrigley	11/30/05 00:42:51	aescoba	03/22/10 14:02:39
3	150" PM Peak (new)	Unknown	10/06/2001 22:51:35	unknown	03/29/11 16:48:39
4	120" Midday (new)	Unknown	10/06/2001 22:51:36	aescoba	03/22/10 13:54:20

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EBLT Leading	1: EBLT Leading	1: Overlap Parameters	1:

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	100	20																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	20	0	15	0	25	0	15	0	0	0	10	0	0	10
CordNom	15	30	0	20	0	45	0	20	0	0	0	15	0	0	15
CordMax	20	40	0	30	0	50	0	30	0	0	0	20	0	0	20
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	25	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	40	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	50	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EBLT Leading	1: EBLT Leading	1: Overlap Parameters	1:

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Plan Table 2 Params

Config	Cycle Length	Permissive	SpCfcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	144	20																

Plan Table 2 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	0	20	0	35	0	20	0	0	0	15	0	0	15
CordNom	24	36	0	30	0	60	0	30	0	0	0	24	0	0	24
CordMax	30	40	0	40	0	70	0	40	0	0	0	30	0	0	30
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	35	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	60	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	70	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EBLT Leading	1: EBLT Leading	1: Overlap Parameters	1:

Plan Table 3 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	150	20																

Plan Table 3 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	0	20	0	35	0	20	0	0	0	15	0	0	15
CordNom	21	44	0	32	0	65	0	32	0	0	0	21	0	0	21
CordMax	30	50	0	40	0	70	0	40	0	0	0	30	0	0	30
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	35	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	64	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	70	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: EBLT Leading	1: EBLT Leading	1: Overlap Parameters	1:

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: Kingwood Cabinet Configuration	120	20	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	0	15	0	30	0	15	0	0	0	15	0	0	15
CordNom	20	35	0	25	0	55	0	20	0	0	0	20	0	0	20
CordMax	30	40	0	30	0	60	0	30	0	0	0	30	0	0	30
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	25	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	45	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	50	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	EBLT Leading	GTSI	08/20/99 09:36:34	unknown	09/05/02 15:48:37
2	WBLT Leading	guest	09/05/2002 12:20:37	KETI	07/14/2004 17:53:13

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Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Parameters	GTSI	01/17/00 18:35:32	JCLARK6	02/28/13 13:55:41

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	8	8	5	5	5	5	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	4.0	4.0	4.0	4.0	3.0	3.0	3.0	3.0
RedClear	1.0	1.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0
Walk	0	6	6	6	0	0	0	0	0	0
PedClear	0	7	7	7	0	0	0	0	0	0
Reservice	5	5	5	5	5	5	0	0	0	0

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Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	EBLT Leading	GTSI	01/17/00 20:39:24	unknown	07/18/11 16:41:49
2	WBLT Leading	Unknown	10/06/2001 22:23:44	unknown	07/18/11 16:41:33

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit					X						X									
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest		X																		
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	4	0	5	5	4	0	5	0	0	0	4	0	0	5
Passage	2.5	1.5	0.0	5.0	2.5	1.5	0.0	5.0	0.0	0.0	0.0	1.5	0.0	0.0	2.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	35	0	25	15	50	0	25	0	0	0	20	0	0	20
Max2	25	45	0	35	20	70	0	35	0	0	0	30	0	0	30
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	0.0	4.0	4.0	4.0	0.0	4.0	0.0	0.0	0.0	4.0	0.0	0.0	4.0
RedClr	2.0	2.0	0.0	1.0	2.0	2.0	0.0	1.0	0.0	0.0	0.0	2.0	0.0	0.0	2.0
Walk	0	4	0	0	0	0	0	6	0	0	0	0	0	0	0
PedClr	0	10	0	0	0	0	0	7	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	5	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	25	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	35	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit	X														X					
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest		X																		
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 2 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	4	0	5	5	4	0	5	0	0	5	0	0	0	5
Passage	2.5	1.5	0.0	5.0	2.5	1.5	0.0	5.0	0.0	0.0	2.0	0.0	0.0	0.0	2.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	35	0	25	15	50	0	25	0	0	20	0	0	0	20
Max2	25	45	0	35	20	70	0	35	0	0	30	0	0	0	30
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	0.0	4.0	4.0	4.0	0.0	4.0	0.0	0.0	4.0	0.0	0.0	0.0	4.0
RedClr	2.0	2.0	0.0	1.0	2.0	2.0	0.0	1.0	0.0	0.0	2.0	0.0	0.0	0.0	2.0
Walk	0	4	0	0	0	0	0	6	0	0	0	0	0	0	0
PedClr	0	10	0	0	0	0	0	7	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 2 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 2 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	Table 01
Created By	guest
Created On	07/07/2004 23:35:44
Modified By	guest
Modified On	07/07/2004 23:36:32

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	
2	X	X		X						ExtPlan	2	
3	X		X	X						ExtPlan	3	
4	X									Schedule	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
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89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/02/05 10:37:17	bushekn	11/02/05 10:40:14

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On
1	Kingwood Cabinet Configuration	GTSI	01/17/00 20:39:23

Active Config Table Info

Table	Modified By	Modified On
1	TransCore	11/02/2005 10:22:23

Active Config Table 1 Params

Phase	1: Kingwood Dual Ring Phasing
Overlap	1: Dual Intersection Overlaps
Preempt	1: Preempts
Cabinet	1: Basic 170_332
Detector	1: Table 01
Coordination	1: Table 01
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Dual Ring Phasing	GTSI	01/17/00 18:35:33	aescoba	03/22/10 13:28:36

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup								X												
YelStart								X												
RedStart																				
SecStart																				
Ped		X						X												
YelFlash																				
AltFlsHz	X	X			X	X				X						X				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	2
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,1,a,4,8,b,12,11,c
2	5,6,a,17,b,15,c
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Steve	01/17/00 11:32:41	unknown	05/19/11 11:44:17

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X		X	X	X	X	X				X	X	X	X	X	X	X	X	X		X	X	X
Call	X		X	X	X	X	X				X	X	X	X	X	X	X	X	X		X	X	X
Extend	X		X	X	X	X	X				X	X	X	X	X	X	X	X	X		X	X	X
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok			X	X																	X		

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X	X																					
Call	X	X		X	X	X		X	X	X		X		X									
Extend	X	X		X	X	X		X	X	X		X		X									
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	2	2	2	2	2	2	3	3	4	4	8	8	11
SwitchPhs	11	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	8.0	0.0	8.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	11	15	15	5	5	6	6	6	6	6	6	2	5	4	0
SwitchPhs	0	0	0	15	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	3	0	5	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	6	1	8	8	11	0	15	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	0	0	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	3	0	0	4	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	GTSI	08/20/99 09:36:29	unknown	05/03/06 10:02:02

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

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Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	None	None	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	2	3
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Prempt	Prempt	Prempt	Prempt
FctnIndx	1	1	1	1	1	1	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NOr	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	Veh	Veh	Veh	Veh
FctnIndx	0	0	0	15	16	17	18
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	1	2	19	20
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	Or	Or	Or	Or

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovl	phase	ovl	ovl	ovl	phase	ovlped	ped	ovlped
FctnIndx	6	3	1	4	5	4	2	8	3	4	4
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	unknown	01/22/10 15:36:32

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Dual Intersection Overlaps	GTSI	01/17/00 18:35:32	aescoba	03/22/10 13:25:24

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X			X							X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X										X			X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X					X									
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X										X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X														
Ped			X	X																
YelFlash																				
AltFlsHz			X	X	X	X														
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	guest	07/07/2004 23:31:27	unknown	01/09/06 12:53:47

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/15/04 15:41:43	trf_ctrl	07/15/04 15:41:43

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	JCLARK6
Created On	02/28/13 13:55:30
Modified By	JCLARK6
Modified On	02/28/13 13:55:30

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

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NextPhase 1.7.5 Admin Comm

Description	
Created By	JCLARK6
Created On	02/28/13 13:55:30
Modified By	JCLARK6
Modified On	02/28/13 13:55:30

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	JCLARK6
Created On	02/28/13 13:55:30
Modified By	JCLARK6
Modified On	02/28/13 13:55:30

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	JCLARK6
Created On	02/28/13 13:55:30
Modified By	JCLARK6
Modified On	02/28/13 13:55:30

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	W. Lake Houston @ Rustic Woods
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	awrigley
Modified On	12/08/05 10:44:24

NextPhase 1.7.1 Control Params

Control Mode	scheduler
Manual Plan	1: 50" Off Peak
Free Plan	1: 50" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 13:00:41

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Midday
5																	Weekend
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	50" Off Peak	Steve	01/17/00 11:33:03	unknown	12/09/05 10:20:43
2	72" AM Peak	guest	06/27/2003 14:45:43	unknown	12/09/05 10:21:08
3	75" PM Peak	awrigley	12/07/05 20:29:09	unknown	12/09/05 10:20:57
4	60" Midday	awrigley	12/07/05 20:29:11	unknown	12/09/05 10:21:36

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: 3 Phase Operation	1: 3 Phase Operation	1: Overlap Parameters	1:

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	50	1																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	10	0	15	7	10	0	15	0	0	0	0	0	0	0
CordNom	13	12	0	25	10	15	0	25	0	0	0	0	0	0	0
CordMax	20	20	0	30	14	20	0	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	15	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: 3 Phase Operation	1: 3 Phase Operation	1: Overlap Parameters	1:

Plan Table 2 Params

Config	Cycle Length	Permissive	SpCfcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	72	1																

Plan Table 2 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	22	15	0	35	10	27	0	35	0	0	0	0	0	0	0
CordMax	30	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	20	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: 3 Phase Operation	1: 3 Phase Operation	1: Overlap Parameters	1:

Plan Table 3 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: Kingwood Cabinet Configuration	75	0																

Plan Table 3 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	22	15	0	38	10	27	0	38	0	0	0	0	0	0	0
CordMax	30	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	20	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: 3 Phase Operation	1: 3 Phase Operation	1: Overlap Parameters	1:

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: Kingwood Cabinet Configuration	60	0	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	17	13	0	30	10	20	0	30	0	0	0	0	0	0	0
CordMax	20	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	20	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	3 Phase Operation	GTSI	08/20/99 09:36:34	awrigley	12/08/05 10:55:24

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Parameters	GTSI	01/17/00 18:35:32	clupold	07/01/11 09:32:46

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	3	2	2	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.5	3.5	3.5	3.5	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.5	0.5	0.5	0.5	0.0	0.0	0.0	0.0	0.0	0.0
Walk	5	0	0	0	0	0	0	0	0	0
PedClear	11	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	3 Phase Operation	GTSI	01/17/00 20:39:24	clupold	07/01/11 09:34:11

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl																				
MaxRcl				X				X												
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt	X				X															
SimGap																				
RedRest																				
AutoPed		X		X				X												
WlkRest				X				X												
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	5	2	10	3	5	2	10	0	0	0	0	0	0	0
Passage	1.0	1.5	1.0	3.0	1.0	1.5	1.0	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	15	7	40	15	25	7	35	0	0	0	0	0	0	0
Max2	30	25	7	40	15	35	7	40	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.5	4.0	3.5	4.5	3.5	4.0	3.5	4.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.5	1.0	0.5	1.5	0.5	1.0	0.5	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	5	0	5	0	0	0	5	0	0	0	0	0	0	0
PedClr	0	6	0	14	0	0	0	14	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
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51										ExtPlan	0	
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54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
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100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
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173										ExtPlan	0	
174										ExtPlan	0	
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191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
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216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
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227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

NextEdit

Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/28/05 13:52:40	awrigley	02/22/06 14:08:24

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On
1	Kingwood Cabinet Configuration	GTSI	01/17/00 20:39:23

Active Config Table Info

Table	Modified By	Modified On
1	guest	09/05/2002 12:10:14

Active Config Table 1 Params

Phase	1: Lake Houston @ Rustic Woods
Overlap	1: Lake Houston @ Rustic Woods
Preempt	1: Preempts
Cabinet	1: input settings/output settings
Detector	1: Detectors
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On
1	Lake Houston @ Rustic Woods	GTSI	01/17/00 18:35:33

Phase Config Table Info

Table	Modified By	Modified On
1	clupold	07/01/11 09:34:33

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Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup		X				X														
YelStart		X				X														
RedStart																				
SecStart																				
Ped		X		X				X												
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	2
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	2,1,a,3,4,b
2	6,5,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTSI	08/24/99 16:13:16	clupold	07/01/11 09:35:41

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X	X	X	X	X	X	X	X		X			X	X	X	X	
Call	X	X	X	X	X	X	X	X			X	X	X	X		X			X	X	X	X	X
Extend	X	X	X	X	X	X	X	X			X	X	X	X		X			X	X	X	X	X
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call		X		X		X		X		X													
Extend		X		X		X		X		X													
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable				X	X													
Call				X	X													
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	2	7	3	3	4	4	4	4	4	6	6	3	7	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	8	0	0	8	8	7	8	2	0	4	0	6	0	8	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	7	0	3	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	1	5	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	0	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	clupold	07/01/11 09:36:48

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

NextEdit

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	Veh	Veh	None	None	None	None
FctnIndx	6	0	0	0	0	50	51	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	Virt	Virt	None	None	None	None
Pin	52	10	11	12	13	2	6	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	phase	ovl	phase	ovl	phase	ovl	phase	ped	ped	ovlped
FctnIndx	3	2	1	4	4	6	2	8	2	4	1
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	spec1	spec1	spec1	spec1	spec1	spec1
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	phson	phson	none	none	none
FctnIndx	1	2	3	4	1	1	2	6	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	Virt	Virt	None	None	None
Pin1	35	37	36	38	102	103	2	6	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

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Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																

NextEdit

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	awrigley	02/22/06 14:39:02

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X							X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X	X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On
1	Lake Houston @ Rustic Woods	GTSI	01/17/00 18:35:32

Overlap Config Table Info

Table	Modified By	Modified On
1	clupold	07/01/11 09:37:54

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X	X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X	X																	
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X		X																	
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X		X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped	X																			
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	unknown	02/17/06 09:06:34

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	0	TOD	5	0

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Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/28/05 13:52:40	trf_ctrl	11/28/05 13:52:40

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood Dr. @ Loop 494
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	aescoba
Modified On	05/24/10 16:18:10

NextPhase 1.7.1 Control Params

Control Mode	scheduler
Manual Plan	3: 150" PM Peak
Free Plan	1: 100" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 09:59:12

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	Holidays
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	guest
Modified On	10/25/2001 12:30:13

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak	GTSI	01/17/00 20:39:24	aescoba	05/24/10 16:21:18
2	144" AM Peak	guest	10/25/2001 11:35:33	unknown	03/30/11 08:11:13
3	150" PM Peak	guest	10/25/2001 11:35:33	unknown	03/30/11 17:20:40
4	120" Midday	guest	09/23/2002 15:28:24	aescoba	05/24/10 16:21:08

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Phase Data	2: Lag 3	1: No Overlaps	1:	1: Active Controller

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	15																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	25	10	15	15	20	10	20	0	0	0	0	0	0	0
CordNom	14	42	18	26	19	37	15	29	0	0	0	0	0	0	0
CordMax	20	50	20	30	30	40	20	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	3: LAG 3 & 5	1: No Overlaps	1:	1: Active Controller

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	20																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	45	25	20	35	30	15	20	0	0	0	0	0	0	0
CordNom	16	80	30	18	50	46	18	30	0	0	0	0	0	0	0
CordMax	40	90	50	40	70	60	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	3: LAG 3 & 5	1: No Overlaps	1:	1: Active Controller

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	20																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	12	40	15	25	15	40	15	25	0	0	0	0	0	0	0
CordNom	15	78	25	32	23	70	22	35	0	0	0	0	0	0	0
CordMax	20	80	30	50	30	80	30	50	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	90	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	2: Lag 3	1: No Overlaps	1:	1: Active Controller

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	35	10	20	15	25	10	20	0	0	0	0	0	0	0
CordNom	12	63	18	27	30	45	15	30	0	0	0	0	0	0	0
CordMax	20	70	20	40	30	50	20	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Leading	GTSI	08/20/99 09:36:34	aescoba	05/24/10 16:21:01
2	Lag 3	unknown	02/13/02 08:46:08	aescoba	05/24/10 16:20:38
3	LAG 3 & 5	unknown	03/29/11 17:33:49	unknown	03/29/11 17:35:32

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	No Overlaps	GTSI	01/17/00 18:35:32	unknown	01/07/03 14:38:21

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	unknown	05/12/11 03:39:43

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit												X				X				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	10	5	8	5	10	5	8	0	0	0	60	0	0	0
Passage	2.0	4.0	1.7	2.0	2.0	2.0	1.7	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	50	20	30	20	40	20	30	0	0	0	60	0	0	0
Max2	20	80	30	50	40	80	30	50	0	0	0	60	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	4.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	2.0	0.0	0.0	0.0
Walk	0	4	0	4	0	4	0	4	0	0	0	0	0	0	0
PedClr	0	10	0	10	0	10	0	10	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	60	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	60	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	60	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	unknown
Modified On	02/24/06 08:37:47

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						Schedule	1	Dial 1
2	X	X		X						Schedule	2	Dial 2
3	X		X	X						Schedule	3	Dial 3
4	X	X	X	X						Schedule	4	DIAL
5	X									Schedule	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	New Night Time
9										ExtPlan	0	Lost Interconnect
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/29/05 10:39:25	bushekn	04/18/06 13:47:29

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	4	8	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Active Controller	GTSI	01/17/00 20:39:23	unknown	07/31/02 10:57:38

Active Config Table 1 Params

Phase	1: Rings and stuff
Overlap	1: Overlaps
Preempt	1: COH Preempts
Cabinet	1: input settings/output settings
Detector	1: Table 01
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Rings and stuff	GTSI	01/17/00 18:35:33	unknown	01/07/03 14:34:11

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	ebf	wbt	sbl	nbt	wbl	ebt	nbl	sbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall												X				X				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,12,2,a,3,4,b
2	5,16,6,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Steve	01/17/00 11:32:41	unknown	05/19/11 09:12:45

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X	X	X	X	X	X											
Call	X	X	X	X	X	X	X	X	X	X	X	X							X	X	X	X	X
Extend	X	X	X	X	X	X	X	X	X	X	X	X							X	X	X	X	X
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call	X	X	X																				
Extend	X	X	X																				
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	3	4	4	5	6	7	8	8	8	8	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	6	1	4	7	2	5	8	3	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	guest	09/20/00 9:07:32	unknown	12/19/10 10:00:56

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	TimSet	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	Or	Or	Or	Or

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Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	None	None	None	Preempt	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	None	None	None	None	None	None
Pin	52	10	11	12	13	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	None	None	None	None
FctnIndx	0	0	0	2	3	4	5	0	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	None	None	None	None
Pin	None	None	None	71	73	72	74	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	phase	phase	phase	ovl	phase	phase	ped	ped	ped
FctnIndx	1	1	3	4	5	2	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	plnsel	plnsel	plnsel	plnsel	plnsel	spec1
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	93	83	84	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

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Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH Preempts	GTSI	01/10/00 07:53:31	unknown	09/21/09 19:15:26

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable	X	X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres	X									
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1		X			X															
Entry2																				
Dwell	X			X			X	X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit												X				X				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X			X															

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.5	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	5	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	8	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	10	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	20	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlaps	GTSI	01/17/00 18:35:32	unknown	01/07/03 14:31:23

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X										X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X																		
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	unknown	12/19/10 10:30:26

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X						3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	guest	06/20/2003 11:52:34	guest	06/20/2003 11:52:34

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood @ W. Lake Houston
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	08/26/10 07:21:05

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	2: 144" AM Peak (new)
Free Plan	1: 100" Off Peak (new)
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	04/06/11 12:38:27

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X		X		sched	1
4	X	9	0	X						X	sched	4
5	X	9	30					X			sched	1
6	X	11	0		X	X	X	X	X		sched	4
7	X	14	30		X	X	X	X	X		sched	5
8	X	15	30		X	X	X	X	X		sched	3
9	X	19	30	X	X	X	X	X	X	X	sched	1
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
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34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (new)	GTSI	01/17/00 20:39:24	unknown	03/29/11 10:50:05
2	144" AM Peak (new)	guest	09/05/2002 12:22:55	unknown	03/31/11 09:18:39
3	150" PM Peak (new)	guest	09/05/2002 12:26:16	unknown	03/29/11 15:49:39
4	120" Midday (new)	guest	09/05/2002 12:36:06	unknown	03/29/11 11:34:56
5	150" Early PM Peak (new)	guest	09/05/2002 12:39:11	unknown	09/13/10 08:57:29

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Phase Data	1: Lag 1 & 7	1: Overlap Data	1:	1: West Lake Houston

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn										AuxFcn										Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10				
100	10																														

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	20	15	20	15	20	15	20	0	0	0	0	0	0	0
CordNom	18	27	23	32	18	27	20	35	0	0	0	0	0	0	0
CordMax	30	40	30	40	30	40	30	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	50	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	4: Lag 1 & 3	1: Overlap Data	1:	1: West Lake Houston

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	20																										

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	30	20	35	15	30	20	35	0	0	0	0	0	0	0
CordNom	24	45	25	50	24	45	25	50	0	0	0	0	0	0	0
CordMax	40	60	40	70	40	60	40	70	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	141	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	4: Lag 1 & 3	1: Overlap Data	1:	1: West Lake Houston

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	20																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X		X		X		X												

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	20	30	20	30	15	30	0	0	0	0	0	0	0
CordNom	18	56	27	49	29	45	29	47	0	0	0	0	0	0	0
CordMax	30	70	40	60	40	60	30	60	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	2: Lag 3 & 5	1: Overlap Data	1:	1: West Lake Houston

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	25	15	25	15	25	15	25	0	0	0	0	0	0	0
CordNom	25	35	25	35	20	40	25	35	0	0	0	0	0	0	0
CordMax	30	50	30	50	30	50	30	50	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Phase Data	2: Lag 3 & 5	1: Overlap Data	1:	1: West Lake Houston

Plan Table 5 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	20																										

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X		X		X		X												

Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	30	20	30	20	30	20	30	0	0	0	0	0	0	0
CordNom	18	56	30	46	29	45	30	46	0	0	0	0	0	0	0
CordMax	30	60	40	60	40	60	40	60	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	140	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Lag 1 & 7	GTSI	08/20/99 09:36:34	aescoba	07/20/10 12:12:42
2	Lag 3 & 5	guest	10/09/2002 14:32:40	aescoba	07/20/10 12:12:59
3	Lag 5 & 7	guest	10/09/2002 14:33:10	aescoba	07/20/10 12:13:07
4	Lag 1 & 3	guest	10/09/2002 14:33:37	aescoba	07/20/10 12:13:14

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Data	GTSI	01/17/00 18:35:32	unknown	08/25/10 11:46:29

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri	X	X	X	X																
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

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Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	unknown	09/13/10 08:49:57

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl				X				X												
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X		X		X		X												
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	10	3	4	3	10	3	4	0	0	0	0	0	0	0
Passage	2.0	1.2	2.0	2.0	2.0	1.2	1.5	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	30	45	30	40	30	45	30	40	0	0	0	0	0	0	0
Max2	40	60	40	60	40	60	40	60	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	4.0	4.0	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0	0	0	0	0	0
PedClr	0	34	0	26	0	34	0	26	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	5	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	
21										ExtPlan	0	
22										ExtPlan	0	
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24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
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61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
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67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
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151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
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162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	11/29/05 10:56:16	bushekn	04/12/06 14:46:36

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	2	6	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	West Lake Houston	GTSI	01/17/00 20:39:23	unknown	10/09/02 14:37:07

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Active Config Table 1 Params

Phase	1: Phase Config
Overlap	1: Overlap Config
Preempt	1: Preempts
Cabinet	1: COH ITS CABINET [SERIAL]
Detector	1: Kingwood @ W. Lake Houston
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Config	GTSI	01/17/00 18:35:33	aescoba	07/01/10 10:07:13

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	ebf	wbt	nbl	sbt	wbl	ebt	sbl	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

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Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	3
Red Revert	3
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On
1	Kingwood @ W. Lake Houston	GTSI	08/24/99 16:13:16

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Detector Table Info

Table	Modified By	Modified On
1	unknown	08/26/10 07:35:17

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X							X	X							X	X					
Call	X	X							X	X							X	X					
Extend	X	X							X	X							X	X					
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable		X	X					X	X														
Call		X	X					X	X														
Extend		X	X					X	X														
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YellLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	2	2	2	2	2	2	3	3	4	4	4	4	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	4	5	5	6	6	6	6	6	6	7	7	8	8	8	8
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	8	8	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X		X		X		X															

NextEdit

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	2	0	4	0	6	0	8	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH ITS CABINET [SERIAL]	GTSI	08/20/99 09:36:31	unknown	09/02/10 14:08:30

Cabinet Table 1 Options

Cabinet Type	ITS_CBD
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1
Pin	6	7	8	9	10	11	12	13	14	15	16
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2	IN2	IN2
Pin	17	18	19	20	21	6	7	8	9	10	11
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped
FctnIndx	23	24	25	26	27	28	29	30	31	32	2
Device	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN1
Pin	12	13	14	15	16	17	18	19	20	21	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel	TimSet	Prempt	Prempt	Prempt
FctnIndx	4	6	8	1	2	3	4	1	2	3	4
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2
Pin	27	28	29	22	23	24	25	28	24	25	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	NAnd	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Prempt	Prempt	MCE	Advanc	StopTm	Flash	Start	None	None	None	None
FctnIndx	5	6	1	1	1	1	1	0	0	0	0
Device	IN2	IN2	OUT1	OUT1	OUT1	OUT1	OUT1	None	None	None	None
Pin	27	29	Opto1	Opto2	Opto3	Opto4	Opto4	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	NAnd	And	And	Or	NAnd	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	None	None	None	None
FctnIndx	0	0	0	2	3	4	5	0	0	0	0
Device	None	None	None	IN2	IN2	IN2	IN2	None	None	None	None
Pin	None	None	None	24	25	26	27	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	ped	phase	phase	ped	phase	phase	ped	phase	phase
FctnIndx	1	2	2	3	4	4	5	6	6	7	8
Device1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin1	0	3	6	9	12	15	18	21	24	27	30
Device2	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin2	1	4	7	10	13	16	19	22	25	28	31
Device3	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin3	2	5	8	11	14	17	20	23	26	29	32

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	none	none	ovl	ovl	ovl	ovl	none	none	none	none
FctnIndx	8	0	0	1	2	3	4	13	14	0	0
Device1	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin1	33	36	39	0	3	6	9	12	15	None	None
Device2	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin2	34	37	40	1	4	7	10	13	16	None	None
Device3	OUT1	OUT1	OUT1	OUT3	OUT3	OUT3	OUT3	OUT3	OUT3	None	None
Pin3	35	38	41	2	5	8	11	14	17	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	14Pak	Off	6Pak	Off	unused
Det BIU/In SIU	On	On	Off	Off	Off

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1							X	X	X							
2			X				X	X	X							
3							X	X	X							
4										X	X	X			X	X
5						X				X	X	X			X	
6										X	X	X			X	

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
7																
8									X							
9																
10															X	X
11												X				X
12																X
13																
14																
15																X
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1	X	X														
2		X														
3		X														
4																
5																
6																
7	X	X														
8	X															
9	X															
10																
11																
12																
13																
14																
15																
16																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
17		X														
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Preempts	GTSI	01/10/00 07:53:31	unknown	08/25/10 12:59:02

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X			X													

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	2	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Config	GTSI	01/17/00 18:35:32	aescoba	07/12/10 09:32:14

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X																	
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X															
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	3	3	3	3	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	unknown	01/09/06 10:41:52

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

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Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:44:54
Modified By	rreevey
Modified On	03/19/14 15:44:54

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:44:54
Modified By	rreevey
Modified On	03/19/14 15:44:54

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:44:54
Modified By	rreevey
Modified On	03/19/14 15:44:54

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:44:54
Modified By	rreevey
Modified On	03/19/14 15:44:54

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	Kingwood @ Kingwood HS
Created By	tbales
Created On	08/16/05 15:52:27
Modified By	tbales
Modified On	08/16/05 15:52:27

NextPhase 1.7.0 Control Params

Control Mode	scheduler
Manual Plan	1: 100" Off Peak (New)
Free Plan	1: 100" Off Peak (New)
Default Schedule	1: Kingwood Schedule (HS)

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule (HS)	gtsi	09/20/2001 19:18:28	unknown	04/06/11 12:24:41

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2	X	5	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X		X		sched	1
4	X	9	0	X						X	sched	4
5	X	9	30					X			sched	1
6	X	11	0		X	X	X	X	X		sched	4
7	X	14	0		X	X	X	X	X		sched	5
8	X	14	30		X	X	X	X	X		sched	3
9	X	19	30	X	X	X	X	X	X	X	sched	1
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	1
13		0	0								sched	1
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	1
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	1
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	1
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule (HS)	
2		1	1								1: Kingwood Schedule (HS)	
3		1	1								1: Kingwood Schedule (HS)	
4		1	1								1: Kingwood Schedule (HS)	
5		1	1								1: Kingwood Schedule (HS)	
6		1	1								1: Kingwood Schedule (HS)	
7		1	1								1: Kingwood Schedule (HS)	
8		1	1								1: Kingwood Schedule (HS)	
9		1	1								1: Kingwood Schedule (HS)	
10		1	1								1: Kingwood Schedule (HS)	
11		1	1								1: Kingwood Schedule (HS)	
12		1	1								1: Kingwood Schedule (HS)	
13		1	1								1: Kingwood Schedule (HS)	
14		1	1								1: Kingwood Schedule (HS)	
15		1	1								1: Kingwood Schedule (HS)	
16		1	1								1: Kingwood Schedule (HS)	
17		1	1								1: Kingwood Schedule (HS)	
18		1	1								1: Kingwood Schedule (HS)	
19		1	1								1: Kingwood Schedule (HS)	
20		1	1								1: Kingwood Schedule (HS)	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule (HS)	
2		1	1								1: Kingwood Schedule (HS)	
3		1	1								1: Kingwood Schedule (HS)	
4		1	1								1: Kingwood Schedule (HS)	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule (HS)	
6		1	1								1: Kingwood Schedule (HS)	
7		1	1								1: Kingwood Schedule (HS)	
8		1	1								1: Kingwood Schedule (HS)	
9		1	1								1: Kingwood Schedule (HS)	
10		1	1								1: Kingwood Schedule (HS)	
11		1	1								1: Kingwood Schedule (HS)	
12		1	1								1: Kingwood Schedule (HS)	
13		1	1								1: Kingwood Schedule (HS)	
14		1	1								1: Kingwood Schedule (HS)	
15		1	1								1: Kingwood Schedule (HS)	
16		1	1								1: Kingwood Schedule (HS)	
17		1	1								1: Kingwood Schedule (HS)	
18		1	1								1: Kingwood Schedule (HS)	
19		1	1								1: Kingwood Schedule (HS)	
20		1	1								1: Kingwood Schedule (HS)	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (New)	GTSI	01/17/00 20:39:24	aescoba	05/24/10 16:42:09
2	144" AM Peak (New)	unknown	03/16/04 16:02:21	aescoba	05/24/10 16:42:15
3	150" PM Peak (New)	unknown	03/16/04 16:02:28	aescoba	05/24/10 16:42:22
4	120" Midday (New)	unknown	01/27/04 15:10:36	aescoba	05/24/10 16:42:29
5	120" School PM (New)	unknown	07/25/05 09:54:32	aescoba	05/24/10 16:42:34

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Kingwood @ KHS	1: Standard All Leading	1:	1:	1:

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn										AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10		
100	20																												

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	30	0	29	0	30	0	0	0	0	0	12	0	0	0
CordNom	0	50	0	47	0	50	0	0	0	0	0	15	0	0	0
CordMax	0	60	0	58	0	60	0	0	0	0	0	24	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	14	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	60	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Kingwood @ KHS	1: Standard All Leading	1:	1:	1:

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	30																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	35	0	30	0	35	0	0	0	0	0	15	0	0	0
CordNom	0	55	0	56	0	55	0	0	0	0	0	25	0	0	0
CordMax	0	70	0	60	0	70	0	0	0	0	0	30	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	5	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	80	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Kingwood @ KHS	1: Standard All Leading	1:	1:	1:

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	30																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	40	0	30	0	40	0	0	0	0	0	10	0	0	0
CordNom	0	70	0	50	0	70	0	0	0	0	0	18	0	0	0
CordMax	0	80	0	60	0	80	0	0	0	0	0	20	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	12	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	95	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Kingwood @ KHS	1: Standard All Leading	1:	1:	1:

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	30																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	35	0	25	0	35	0	0	0	0	0	12	0	0	0
CordNom	0	55	0	42	0	55	0	0	0	0	0	15	0	0	0
CordMax	0	70	0	50	0	70	0	0	0	0	0	24	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	14	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: Kingwood @ KHS	1: Standard All Leading	1:	1:	1:

Plan Table 5 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	30																										

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	20	0	20	0	20	0	0	0	0	0	10	0	0	0
CordNom	0	47	0	50	0	47	0	0	0	0	0	15	0	0	0
CordMax	0	40	0	40	0	40	0	0	0	0	0	20	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	14	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Leading	GTSI	08/20/99 09:36:34	aescoba	05/24/10 16:44:28

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	aescoba	02/23/11 16:10:28
2		unknown	07/25/05 10:08:39	unknown	07/25/05 10:08:39

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Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	6	6	0	0	0	0	0	0
PedClear	0	0	7	7	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 2 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

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Overlap Table 2 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 2 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood @ KHS	GTSI	01/17/00 20:39:24	unknown	03/09/11 05:49:59

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest						X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	0	4	0	5	0	4	0	0	0	5	8	5	0	0	0
Passage	0.0	1.5	0.0	3.0	0.0	1.5	0.0	0.0	0.0	3.0	3.0	3.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	70	0	60	0	70	0	0	0	20	60	20	0	0	0
Max2	0	75	0	60	0	70	0	0	0	20	60	20	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.0	0.0	4.0	0.0	4.0	0.0	0.0	0.0	4.0	4.0	4.0	0.0	0.0	0.0
RedClr	0.0	2.0	0.0	2.0	0.0	2.0	0.0	0.0	0.0	2.0	2.0	2.0	0.0	0.0	0.0
Walk	0	0	0	0	0	6	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	7	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	Table 01
Created By	gtsi
Created On	09/20/2001 19:19:28
Modified By	unknown
Modified On	07/25/05 10:23:17

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	
2	X	X			X					ExtPlan	2	
3	X		X	X						ExtPlan	3	
4	X									ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										Schedule	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
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51										ExtPlan	0	
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53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
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81										ExtPlan	0	
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98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
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126										ExtPlan	0	
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129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
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144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
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189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
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216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
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221										ExtPlan	0	
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232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	01/31/05 21:05:59	unknown	05/17/06 14:28:24

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable	X	X	X	X						
Service Phase	6	2	4	8	None	None	None	None	None	None
Override Next										
Lockout Same	150	150	150	150	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:23	unknown	01/17/00 21:10:34

Active Config Table 1 Params

Phase	1: Rings @ Phases/IDs
Overlap	1:
Preempt	1: RR and EV Preempts
Cabinet	1: Basic 170_332
Detector	1: COH 332 Detectors
Coordination	1: Coordination Configuration
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Rings @ Phases/IDs	GTSI	01/17/00 18:35:33	aescoba	02/23/11 16:29:27

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	nbl	sbt	ebf	wbt	sbl	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X																
YelStart				X																
RedStart																				
SecStart																				
Ped						X														
YelFlash																				
AltFlsHz	X	X			X	X					X					X				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	2
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,12,b,11,c
2	6,a,10,b,4,16,c
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH 332 Detectors	GTSI	08/24/99 16:13:16	aescoba	02/23/11 16:30:47

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X		X		X		X		X		X												
Call	X		X		X		X		X		X												
Extend	X		X		X		X		X		X												
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		X
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	0	4	0	6	0	8	0	12	0	16	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	1	0	2	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	16
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable			X																				

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	4	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	GTSI	08/20/99 09:36:29	unknown	09/13/07 08:18:04

Cabinet Table 1 Cabinet Type

Cabinet Type	170
Unit Address 1	auto
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIdx	1	2	3	4	5	6	7	8	9	10	11
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIdx	12	13	14	15	16	17	18	19	20	21	22
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIdx	23	24	25	26	27	28	29	30	31	32	33
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	And	Or	Or	Or	Or	Or	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	1	1	1	2	3	4	5
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	NOr	Or	Or	Or	Or

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Unit	1	none	none	none	none	none	none	none	none	none	none
Conn/BIU	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NOr	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Unit	none	none	none	1	1	1	1	1	none	none	none
Conn/BIU	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	Veh
FctnIndx	0	0	0	0	0	0	64
Unit	none	none	none	none	none	none	1
Conn/BIU	None	None	None	None	None	None	Virt
Pin	None	None	None	None	None	None	4
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	ovl	phase	phase	ovl	ovl	phase	ovlped	ped	ped
FctnIndx	1	3	1	4	5	4	2	8	2	4	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Conn/BIU2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	35	37	36	38	102	103	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	1	1	1	1	1	1	1	none	none	none	none
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	phson
FctnIndx	0	0	0	0	0	0	4
Unit	none	none	none	none	none	none	1
Conn/BIU1	None	None	None	None	None	None	Virt
Pin1	None	None	None	None	None	None	4
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	RR and EV Preempts	GTSI	01/10/00 07:53:31	unknown	05/17/06 11:51:24

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X							
NoMemLock										
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X						
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X							

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell						X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X																		
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit								X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X																

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YelFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YelFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YelFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YelFIs																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	0	0	0	0	0	0	0
DwellExt	0	2	2	0	0	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	0	0	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	unknown	07/25/05 14:34:43

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X								X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X												X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X										X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped				X																
YelFlash																				
AltFlsHz			X	X																
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Coordination Configuration	trf_ctrl	08/23/01 16:25:18	unknown	01/09/06 13:02:59

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				0

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	01/31/05 21:05:59	trf_ctrl	01/31/05 21:05:59

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Fire Station 101 @ Kingwood
Created By	aescobar
Created On	04/16/09 15:12:56
Modified By	jclark
Modified On	05/27/11 15:24:30

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: free
Free Plan	1: free
Default Schedule	1: Free

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Free	aescobar	04/16/09 15:12:56	jclark	05/27/11 15:13:45

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1		0	0								sched	0
2		0	0								sched	0
3		0	0								sched	0
4		0	0								sched	0
5		0	0								sched	0
6		0	0								sched	0
7		0	0								sched	0
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpCfcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	aescobar
Created On	04/16/09 15:12:56
Modified By	aescobar
Modified On	04/16/09 15:17:24

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Free	
2		1	1								1: Free	
3		1	1								1: Free	

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
4		1	1								1: Free	
5		1	1								1: Free	
6		1	1								1: Free	
7		1	1								1: Free	
8		1	1								1: Free	
9		1	1								1: Free	
10		1	1								1: Free	
11		1	1								1: Free	
12		1	1								1: Free	
13		1	1								1: Free	
14		1	1								1: Free	
15		1	1								1: Free	
16		1	1								1: Free	
17		1	1								1: Free	
18		1	1								1: Free	
19		1	1								1: Free	
20		1	1								1: Free	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Free	
2		1	1								1: Free	
3		1	1								1: Free	
4		1	1								1: Free	
5		1	1								1: Free	
6		1	1								1: Free	
7		1	1								1: Free	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
8		1	1								1: Free	
9		1	1								1: Free	
10		1	1								1: Free	
11		1	1								1: Free	
12		1	1								1: Free	
13		1	1								1: Free	
14		1	1								1: Free	
15		1	1								1: Free	
16		1	1								1: Free	
17		1	1								1: Free	
18		1	1								1: Free	
19		1	1								1: Free	
20		1	1								1: Free	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	free	aescobar	04/16/09 15:12:56	jclark	05/27/11 15:15:57

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
free	max1	seconds	1:	1: Standard All Phases Leading	1: Overlap Settings	1:	1:

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn										AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10		
60	10																												

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	30	0	30	0	30	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Phases Leading	aescobar	04/16/09 15:12:56	aescobar	04/16/09 15:17:24

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Settings	aescobar	04/16/09 15:12:56	jclark	05/27/11 15:25:32

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	3	0	3	0	3	0	0	0	0
MaxGreen	0	90	0	90	0	90	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	4.5	0.0	4.5	0.0	4.5	0.0	0.0	0.0	0.0
RedClear	0.0	1.5	0.0	1.5	0.0	1.5	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:17:35

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	0	3	0	3	0	3	0	0	0	0	0	0	0	0	0
Passage	0.0	2.0	0.0	2.0	0.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	55	0	30	0	55	0	0	0	0	0	0	0	0	0
Max2	0	55	0	30	0	55	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.5	0.0	4.5	0.0	4.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	1.5	0.0	1.5	0.0	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	aescobar
Created On	04/16/09 15:12:56
Modified By	aescobar
Modified On	04/16/09 15:17:25

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	1	
2										ExtPlan	2	
3										ExtPlan	3	
4										Schedule	0	Return to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	aescobar	04/16/09 15:17:25

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	aescobar	04/16/09 15:17:19

Active Config Table 1 Params

Phase	1:
Overlap	1:
Preempt	1:
Cabinet	1: 303 Cab I/O
Detector	1:
Coordination	1:
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:26:07

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	ebt	none	sbt	none	wbt	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X																
YelStart				X																
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,4,b
2	6,a,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:19:23

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	303 Cab I/O	aescobar	04/16/09 15:12:56	jclark	05/27/11 15:31:43

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	56	47	39	43	58	49	41	45	55	48	40
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	PlnSel	PlnSel	PlnSel	PlnSel	Preempt	Preempt	Preempt	Preempt
FctnIndx	12	13	14	1	2	3	4	3	4	5	6
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	44	57	50	63	76	64	77	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Ped	Ped	Ped	Ped	Flash	StopTm	Advanc	MCE	None	None	None
FctnIndx	1	2	3	4	1	1	1	1	0	0	0
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	67	69	68	70	81	82	80	53	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	54	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	Or	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	61	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	phase	ovl	phase	ovl	phase	phase	detrst	fl/wd	none
FctnIndx	1	2	3	4	5	6	7	8	1	1	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	10	7	4	2	32	29	102	103	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	17	13	35	8	5	37	33	30	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	11	9	6	3	34	31	None	None	None

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:20:05

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable					X					
NoMemLock										
ManEnable										
NoOvrFlash										
NoOvrNext										
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	0.0	0.0	0.0	0.5	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	0	0	0	10	0	0	0	0	0
DwellExt	0	0	0	0	0	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	60	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:23:10

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X																		
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X														
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable		X		X		X														
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	RedStd	Norm	RedFls	Norm	RedStd	Norm	Norm	Norm	Norm
Yellow	Norm	YelStd	Norm	YelStd	Norm	YelStd	Norm	Norm	Norm	Norm
Green	Norm	GrnFls	Norm	GrnStd	Norm	GrnFls	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	jclark	05/27/11 15:24:12

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	0	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		aescobar	04/16/09 15:12:56	aescobar	04/16/09 15:16:57

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:42:11
Modified By	rreevey
Modified On	03/19/14 15:42:11

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

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NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:42:11
Modified By	rreevey
Modified On	03/19/14 15:42:11

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:42:11
Modified By	rreevey
Modified On	03/19/14 15:42:11

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:42:11
Modified By	rreevey
Modified On	03/19/14 15:42:11

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Control Table Info

Description	High Valley @ Kingwood
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	unknown
Modified On	08/07/08 06:57:39

NextPhase 1.7.0 Control Params

Control Mode	free
Manual Plan	1: Free Plan
Free Plan	1: Free Plan
Default Schedule	1: Default Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Default Schedule	trf_ctrl	08/23/01 15:52:32	dworley	07/28/05 09:33:22

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	0	0	X	X	X	X	X	X	X	sched	1
2		0	0								sched	0
3		0	0								sched	0
4		0	0								sched	0
5		0	0								sched	0
6		0	0								sched	0
7		0	0								sched	0
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	1
23		0	0								sched	2
24		0	0								sched	2
25		0	0								sched	2
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Default Schedule	
2		1	1								1: Default Schedule	
3		1	1								1: Default Schedule	
4		1	1								1: Default Schedule	
5		1	1								1: Default Schedule	
6		1	1								1: Default Schedule	
7		1	1								1: Default Schedule	
8		1	1								1: Default Schedule	
9		1	1								1: Default Schedule	
10		1	1								1: Default Schedule	
11		1	1								1: Default Schedule	
12		1	1								1: Default Schedule	
13		1	1								1: Default Schedule	
14		1	1								1: Default Schedule	
15		1	1								1: Default Schedule	
16		1	1								1: Default Schedule	
17		1	1								1: Default Schedule	
18		1	1								1: Default Schedule	
19		1	1								1: Default Schedule	
20		1	1								1: Default Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Default Schedule	
2		1	1								1: Default Schedule	
3		1	1								1: Default Schedule	
4		1	1								1: Default Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Default Schedule	
6		1	1								1: Default Schedule	
7		1	1								1: Default Schedule	
8		1	1								1: Default Schedule	
9		1	1								1: Default Schedule	
10		1	1								1: Default Schedule	
11		1	1								1: Default Schedule	
12		1	1								1: Default Schedule	
13		1	1								1: Default Schedule	
14		1	1								1: Default Schedule	
15		1	1								1: Default Schedule	
16		1	1								1: Default Schedule	
17		1	1								1: Default Schedule	
18		1	1								1: Default Schedule	
19		1	1								1: Default Schedule	
20		1	1								1: Default Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Free Plan	GTS	09/08/04 20:13:20	dworley	07/28/05 09:36:41

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
free	max1	seconds	1:	1: Sequence 1	1:	1:	1:	60

Plan Table 1 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Sequence 1	GTSI	08/20/99 09:36:34	dworley	07/28/05 08:53:33

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	unknown	08/26/05 11:21:49

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	3	3	3	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	4.0	4.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	5	5	0	0	0	0	0	0
PedClear	0	0	5	5	0	0	0	0	0	0
Reservice	5	5	5	5	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:24	unknown	04/01/09 07:20:05

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed																				
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	0	20	0	8	0	20	0	8	0	0	0	8	0	0	0
Passage	0.0	4.0	0.0	3.0	0.0	4.0	0.0	3.0	0.0	0.0	0.0	3.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	50	0	20	0	50	0	35	0	0	0	20	0	0	0
Max2	0	60	0	40	0	60	0	40	0	0	0	30	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.0	0.0	4.0	0.0	4.0	0.0	4.0	0.0	0.0	0.0	4.0	0.0	0.0	0.0
RedClr	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0	0.0	0.0	0.0	2.0	0.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0	0	0	0	0	0
PedClr	0	5	0	15	0	5	0	15	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	3.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	30	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	rwelborn
Modified On	06/14/05 17:24:22

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Return To Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
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89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTS	09/10/04 14:08:44	unknown	08/25/05 16:59:50

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 20:39:23	unknown	01/17/00 21:10:34

Active Config Table 1 Params

Phase	1: rings/id's
Overlap	1:
Preempt	1:
Cabinet	1: Basic 170_332
Detector	1: Detectors
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	rings/id's	GTSI	01/17/00 18:35:33	unknown	08/26/05 11:25:41

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	ebt	none	nbt	none	wbt	none	sbt	none	none	none	ebr	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	wbr	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X																
YelStart				X																
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz				X				X												
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,12,8,b,c
2	6,a,b,4,16,c
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTS	09/08/04 20:13:18	unknown	04/01/09 07:00:39

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable			X	X							X	X	X	X	X	X					X	X	
Call			X	X							X	X			X	X					X	X	
Extend			X	X							X	X	X	X	X	X					X	X	
AddInit																							
Term																							
RedLok																							
YelLok			X	X									X	X							X	X	

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable						X	X	X	X	X	X												
Call						X	X			X	X												
Extend						X	X	X	X	X	X												
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	1	2	2	2	2	2	2	3	3	4	4	4	4	4
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	16	16	16
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	2	2	2
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	8.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	4	0	0	5	5	6	6	6	6	6	6	7	7	8	8
SwitchPhs	16	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	8.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	8	8	8	8	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	12	12	12	12	0	0	0	0	0	0	0	0	0	0	0
Overlap	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	3	0	4	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

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Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic 170_332	GTS	09/08/04 20:13:17	unknown	01/06/06 14:44:26

Cabinet Table 1 Cabinet Type

Cabinet Type	170
Unit Address 1	auto
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	PlnSel	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	0	1	1	1	1	2	3	4	5
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	NAnd	And	And	And	And

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Unit	1	none	none	none	none	none	none	none	none	none	none
Conn/BIU	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	1	2	3	4	0	0	0	0
Unit	none	none	none	1	1	1	1	1	none	none	none
Conn/BIU	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	ovl	phase	phase	ovl	ovl	phase	ovlped	ped	ovlped
FctnIndx	1	3	1	4	5	4	2	8	3	4	4
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Conn/BIU2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Conn/BIU2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Conn/BIU3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Unit	1	1	1	1	1	1	none	none	none	none	none
Conn/BIU1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/10/00 07:53:31	dworley	07/29/05 11:54:49

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell						X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X																		
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell							X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/17/00 18:35:32	unknown	08/05/05 13:08:55

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X				X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X												X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X										X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped			X	X																
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	unknown	01/30/06 14:52:56

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Forest Garden @ Kingwood
Created By	awrigley
Created On	12/07/05 14:03:54
Modified By	unknown
Modified On	12/04/06 16:16:35

NextPhase 1.7.0 Control Params

Control Mode	free
Manual Plan	2: 72" AM Peak
Free Plan	1: 50" Off Peak
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	unknown	10/19/06 14:15:36

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	4
2	X	6	0	X	X	X	X	X	X		sched	2
3		9	0		X	X	X	X	X		sched	4
4	X	9	0	X						X	sched	4
5		11	0		X	X	X	X	X		sched	4
6	X	15	0		X	X	X	X	X		sched	3
7	X	20	0	X	X	X	X	X	X	X	sched	4
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Midday
5																	Weekend
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	50" Off Peak	Steve	01/17/00 11:33:03	unknown	12/04/06 16:20:27
2	72" AM Peak	guest	08/29/02 12:14:16	unknown	10/19/06 15:01:50
3	75" PM Peak	guest	08/29/02 12:14:24	unknown	02/14/06 17:38:26
4	60" Midday	Keti	08/10/2004 8:50:59	unknown	06/12/06 13:21:28

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Table 001	1: Standard All Leading	1: Table 01	1:	1: ACTIVE TABLE

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
50	10																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	7	10	10	7	7	10	7	10	0	0	0	0	0	0	0
CordNom	10	15	15	10	10	15	10	15	0	0	0	0	0	0	0
CordMax	14	20	20	14	14	20	14	20	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	45	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Table 001	1: Standard All Leading	1: Table 01	1:	1: ACTIVE TABLE

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
72	10																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	8	20	13	12	8	20	12	13	0	0	0	0	0	0	0
CordNom	15	30	15	12	15	30	12	15	0	0	0	0	0	0	0
CordMax	16	40	25	25	16	40	25	25	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	61	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config	Cycle Length
coord	max1	seconds	1: Table 001	3: Lag 5	1: Table 01	1:	1: ACTIVE TABLE	75

Plan Table 3 Params

Permissive	SpcFcn								AuxFcn								Prioritor Disable									
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
10																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	20	15	10	15	15	10	15	0	0	0	0	0	0	0
CordNom	15	30	15	15	20	25	15	15	0	0	0	0	0	0	0
CordMax	20	40	30	20	30	30	20	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	35	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Table 001	1: Standard All Leading	1: Table 01	1:	1: ACTIVE TABLE

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
60	10																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	15	10	10	10	15	10	15	0	0	0	0	0	0	0
CordNom	13	17	17	13	13	17	13	17	0	0	0	0	0	0	0
CordMax	20	30	20	20	20	30	20	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	50	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard All Leading	Unknown	01/24/00 14:56:25	jclark	11/04/05 12:45:29
2	Lag 1	awrigley	12/07/05 14:33:46	awrigley	12/07/05 14:34:28
3	Lag 5	awrigley	12/07/05 14:33:52	awrigley	12/07/05 14:34:32
4	Lag 1 & Lag 5	awrigley	12/07/05 14:34:08	awrigley	12/07/05 14:34:39

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 4 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Unknown	01/24/00 14:56:24	Unknown	01/24/00 14:56:24

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

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Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 001	Unknown	01/24/00 14:56:02	unknown	03/26/07 14:15:58

Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit			X				X													
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X				X														
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	3	15	2	5	3	15	2	5	0	0	0	0	0	0	0
Passage	1.5	5.0	1.0	2.0	1.5	5.0	1.0	2.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	45	20	20	20	45	30	30	0	0	0	0	0	0	0
Max2	30	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.5	4.0	4.0	4.0	4.5	4.0	4.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	1.0	1.5	2.0	1.0	1.0	1.5	2.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0	0	0	0	0	0
PedClr	0	9	0	9	0	9	0	9	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	guest
Modified On	08/29/02 12:17:01

Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Return to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	01/25/05 10:40:15	P0088409	02/03/05 00:05:29

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	ACTIVE TABLE	Steve	01/17/00 11:32:23	Unknown	07/27/2001 13:38:29

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Active Config Table 1 Params

Phase	1: Table 01
Overlap	1: Table 01
Preempt	1: Table 01
Cabinet	1: METRO Cabinet
Detector	1: Table 01
Coordination	1:
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Steve	01/18/00 10:59:17	unknown	11/29/05 17:17:59

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	nbl	sbt	ebf	wbt	sbl	nbt	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X				X												
YelStart				X				X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

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Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	2
FlashClearance	3
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,4,8,b
2	5,6,a,3,7,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Steve	01/17/00 11:32:41	unknown	01/02/07 11:39:00

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Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X	X	X	X	X	X	X	X									
Call	X	X	X	X	X	X	X	X	X	X	X	X	X	X									
Extend	X	X	X	X	X	X	X	X	X	X	X	X	X	X									
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	5	6	6	6	6	8	8	1	2	2	2	2	4	4	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X		X		X		X															

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	2	0	4	0	6	0	8	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	METRO Cabinet	GTSI	08/20/99 09:36:31	bushekn	11/29/05 11:02:07

Cabinet Table 1 Cabinet Type

Cabinet Type	TS1
Unit Address 1	auto
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped	Ped	Ped
FctnIndx	1	2	3	4	5	6	7	8	1	2	3
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-
Pin	f	K	N	L	P	S	V	t	g	L	P
Chan1	0	0	8	0	0	0	4	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	Or	And	And	And	Or	And	And	And	And

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Ped	Ped	Ped	Ped	Ped	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	4	5	6	7	8	9	10	11	12	13	14
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-	-B-	-C-	-C-
Pin	M	R	T	U	W	h	M	i	h	m	p
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	15	16	17	18	19	20	21	22	23	24	25
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	-C-	-C-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-A-
Pin	EE	X	U	S	R	g	n	q	r	s	EE
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Force	Force	NoMax	NoMax
FctnIndx	26	27	28	29	30	31	32	1	2	1	2
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	-A-	-B-	-B-	-B-	-B-	-B-	-B-	-A-	-C-	-A-	-C-
Pin	v	j	x	T	k	m	n	i	Y	P	a
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	RedRst	RedRst	PedRcl	PedRcl	Max2	Max2	NoRed	NoRed	CNA	CNA	MCE
FctnIndx	1	2	1	2	1	2	1	2	1	0	1
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU	-A-	-C-	-A-	-B-	-A-	-B-	-A-	-C-	-A-	-A-	-A-
Pin	x	u	FF	V	GG	z	w	v	m	z	k
Chan1	0	0	0	0	0	0	0	0	54	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	Or	And	And

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Advanc	MinRcl	WlkRst	Start	StopTm	StopTm	None	None	None	None	None
FctnIndx	1	1	1	1	1	0	0	0	0	0	0
Unit	1	1	1	1	1	1	none	none	none	none	none
Conn/BIU	-A-	-A-	-A-	-A-	-A-	-C-	None	None	None	None	None
Pin	S	j	BB	R	N	Z	None	None	None	None	None
Chan1	0	0	0	0	61	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	PriLow	PriLow	PriLow	PriLow	Flash	None	None	None	None	None	None
FctnIndx	2	3	4	5	1	0	0	0	0	0	0
Unit	1	1	1	1	1	none	none	none	none	none	none
Conn/BIU	-D-	-D-	-D-	-D-	None	None	None	None	None	None	None
Pin	u	v	w	x	None	None	None	None	None	None	None
Chan1	0	0	0	0	60	0	0	0	0	0	0
Chan2	0	0	0	0	61	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	phase	phase	phase	phase	phase	ovl	ovl	ovl
FctnIndx	1	2	3	4	5	6	7	8	1	2	3
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin1	D	F	F	G	H	G	F	D	q	CC	DD
Conn/BIU2	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin2	Z	b	E	c	J	h	E	e	p	BB	HH
Conn/BIU3	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin3	s	c	D	b	i	g	f	x	AA	GG	FF

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovl	ped	ped	ped	ped	check	phsnxt	phson	check	phsnxt	phson
FctnIndx	4	2	4	6	8	1	1	1	2	2	2
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	-B-	-A-	-B-	-C-	-C-	-A-	-B-	-A-	-A-	-B-	-A-
Pin1	u	G	J	z	C	u	A	DD	d	C	e
Conn/BIU2	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin2	EE	H	H	AA	w	None	None	None	None	None	None
Conn/BIU3	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin3	w	J	d	LL	d	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt
FctnIndx	3	3	3	4	4	4	5	5	5	6	6
Unit	1	1	1	1	1	1	1	1	1	1	1
Conn/BIU1	-B-	-B-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-C-
Pin1	r	t	s	K	f	e	k	M	N	BB	DD
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	phson	check	phsnxt	phson	check	phsnxt	phson	fl/wd	none	none	none
FctnIndx	6	7	7	7	8	8	8	1	0	0	0
Unit	1	1	1	1	1	1	1	1	none	none	none
Conn/BIU1	-C-	-C-	-C-	-C-	-C-	-C-	-C-	-A-	None	None	None
Pin1	CC	MM	PP	NN	FF	HH	GG	X	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Unknown	01/24/00 14:56:07	Keti	08/12/2004 13:04:28

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

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Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell			X					X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X			X													
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	Unknown	01/24/00 14:56:05	guest	04/15/2002 8:52:16

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable																				
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	08/23/01 16:25:18	guest	04/19/2002 9:38:49

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	0	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kingwood @ Timber Shade
Created By	GTSI
Created On	08/20/99 09:36:31
Modified By	awrigley
Modified On	12/04/06 16:03:21

NextPhase 1.7.0 Control Params

Control Mode	free
Manual Plan	<TABLE 2 DOES NOT EXIST>
Free Plan	1: 90" Plan
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	trf_ctrl	08/23/01 15:52:32	awrigley	11/30/06 13:29:01

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	1
2		6	0	X	X	X	X	X	X		sched	2
3		9	0		X	X	X	X	X		sched	4
4		9	0	X						X	sched	4
5		11	0		X	X	X	X	X		sched	4
6		15	0		X	X	X	X	X		sched	3
7		19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Weekend
5																	Midday
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	90" Plan	unknown	10/11/05 17:44:26	awrigley	12/04/06 12:50:53

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: Off Peak	1: Standard Leading Lefts	1: Off Peak	1:	1: Active Config

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
90	15																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	10	25	10	10	10	20	15	0	0	0	8	8	0	8
CordNom	0	12	40	15	15	12	30	25	0	0	0	8	8	0	8
CordMax	0	20	50	20	20	20	40	30	0	0	0	8	8	0	8
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	17	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Standard Leading Lefts	GTSI	08/20/99 09:36:34	awrigley	11/30/06 13:29:17

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 18:35:32	awrigley	12/04/06 12:46:19

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	3	3	5	0	3	5	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.5	4.5	4.5	0.0	4.5	4.5	0.0	0.0	0.0	0.0
RedClear	1.5	1.5	1.5	0.0	1.5	1.5	0.0	0.0	0.0	0.0
Walk	0	0	4	0	0	4	0	0	0	0
PedClear	0	0	15	0	0	15	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Off Peak	GTSI	01/17/00 20:39:24	awrigley	01/02/07 11:26:14

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X	X			X	X				X				X					
SimGap																				
RedRest																				
AutoPed																				
WlkRest		X				X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	0	12	3	3	3	12	3	5	0	0	0	3	3	0	3
Passage	0.0	4.0	1.5	1.0	1.5	4.0	1.5	1.5	0.0	0.0	0.0	1.5	1.5	0.0	1.5
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	25	35	20	20	30	35	30	0	0	0	8	8	0	8
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	4.5	3.5	4.0	4.5	4.5	3.5	4.0	0.0	0.0	0.0	4.5	3.5	0.0	3.5
RedClr	0.0	1.5	1.5	1.0	1.5	1.5	1.5	1.0	0.0	0.0	0.0	1.5	1.5	0.0	1.5
Walk	0	4	0	0	0	4	0	4	0	0	0	0	0	0	0
PedClr	0	15	0	0	0	15	0	5	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	trf_ctrl
Created On	08/23/01 15:52:31
Modified By	trf_ctrl
Modified On	08/23/01 15:52:31

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTS	09/10/04 14:08:44	GTS	09/10/04 14:08:45

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Active Config	GTSI	01/17/00 20:39:23	awrigley	02/06/06 12:58:34

Active Config Table 1 Params

Phase	1: rings & ids
Overlap	1: WT/TS Overlaps
Preempt	1:
Cabinet	1: Basic NEMA TS1
Detector	1: Detectors
Coordination	1: GPS Coordination
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	rings & ids	unknown	10/11/05 11:32:27	awrigley	12/04/06 15:11:07

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup				X			X													
YelStart				X			X													
RedStart																				
SecStart																				
Ped		X				X		X												
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	2,a,3,b,13,4,16,c,12,d
2	6,a,5,8,b,7,c,15,d
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Detectors	GTSI	08/24/99 16:13:16	awrigley	12/04/06 15:29:22

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	
Call	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	
Extend	X	X	X	X						X	X	X	X	X	X	X		X		X	X		
AddInit																							
Term																							
RedLok																							
YelLok		X	X	X	X							X	X	X	X								

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable															X	X	X	X
Call															X	X	X	X
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	2	2	2	2	3	3	3	3	4	4	6	6	6	6
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	5	7	7	7	7	8	8	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

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Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	13	13	16	15
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X				X		X															

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	2	3	4	5	6	7	8	0	0	0	0	0	0	0
Overlap	0	3	0	0	0	6	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0

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Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

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Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Basic NEMA TS1	GTS	09/08/04 20:13:17	awrigley	12/04/06 15:11:54

Cabinet Table 1 Cabinet Type

Cabinet Type	TS1
Unit Address 1	none
Unit Address 2	none

Cabinet Table 1 Input Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped	Ped	Ped
FctnIndx	1	2	3	4	5	6	7	8	1	2	3
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-
Pin	f	K	N	L	P	S	V	t	g	L	P
Chan1	0	0	0	0	65	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Ped	Ped	Ped	Ped	Ped	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	4	5	6	7	8	9	10	11	12	13	14
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-B-	-C-	-C-	-C-	-C-	-A-	-A-	-B-	-B-	-C-	-C-
Pin	M	R	T	U	W	h	M	i	h	m	p
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	15	16	17	18	19	20	21	22	23	24	25
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-C-	-C-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-A-
Pin	EE	X	U	S	R	g	n	q	r	s	EE
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Force	Force	NoMax	NoMax
FctnIndx	26	27	28	29	30	31	32	1	2	1	2
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-B-	-B-	-B-	-B-	-B-	-B-	-A-	-C-	-A-	-C-
Pin	v	j	x	T	k	m	n	i	Y	P	a
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	RedRst	RedRst	PedRcl	PedRcl	Max2	Max2	NoRed	NoRed	CNA	CNA	MCE
FctnIndx	1	2	1	2	1	2	1	2	1	0	1
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-C-	-A-	-B-	-A-	-B-	-A-	-C-	-A-	-A-	-A-
Pin	x	u	FF	V	GG	z	w	v	m	z	k
Chan1	0	0	0	0	0	0	0	0	54	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	Or	And	And

Cabinet Table 1 Input Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Advanc	MinRcl	WlkRst	Start	StopTm	StopTm	None	None	None	None	None
FctnIndx	1	1	1	1	1	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	-A-	-A-	-A-	-A-	-A-	-C-	None	None	None	None	None
Pin	S	j	BB	R	N	Z	None	None	None	None	None
Chan1	0	0	0	0	61	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Input Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Input Channels

Channel	122	123	124	125	126	127	128
FctnType	None	Veh	Veh	Veh	Omit	Omit	Veh
FctnIndx	0	61	62	63	15	12	64
Unit	none	1	1	1	1	1	1
Conn/BIU	None	-B-	-B-	-B-	-A-	-A-	-C-
Pin	None	s	K	b	F	F	i
Chan1	0	0	18	0	0	0	0
Chan2	0	0	19	0	0	0	0
Logic	And	And	Or	And	And	And	And

Cabinet Table 1 Output Channels

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	ovl	phase	phase	ovl	ovl	phase	phase	ovl	ovl	ovl
FctnIndx	1	3	3	4	5	6	7	8	1	2	3
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin1	D	F	F	G	H	G	F	D	q	CC	DD
Conn/BIU2	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin2	Z	b	E	c	J	h	E	e	p	BB	HH
Conn/BIU3	-A-	-A-	-B-	-B-	-C-	-C-	-C-	-C-	-B-	-B-	-B-
Pin3	s	c	D	b	i	g	f	x	AA	GG	FF

Cabinet Table 1 Output Channels

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovl	ovlpd	ped	ovlpd	ped	check	phsnxt	phson	check	phsnxt	phson
FctnIndx	4	3	4	6	8	1	1	1	2	2	2
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-B-	-A-	-B-	-C-	-C-	-A-	-B-	-A-	-A-	-B-	-A-
Pin1	u	G	J	z	C	u	A	DD	d	C	e
Conn/BIU2	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin2	EE	H	H	AA	w	None	None	None	None	None	None
Conn/BIU3	-B-	-A-	-B-	-C-	-C-	None	None	None	None	None	None
Pin3	w	J	d	LL	d	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt	phson	check	phsnxt
FctnIndx	3	3	3	4	4	4	5	5	5	6	6
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-B-	-B-	-B-	-B-	-B-	-B-	-C-	-C-	-C-	-C-	-C-
Pin1	r	t	s	K	f	e	k	M	N	BB	DD
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	phson	check	phsnxt	phson	check	phsnxt	phson	fl/wd	none	none	none
FctnIndx	6	7	7	7	8	8	8	1	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	-C-	-C-	-C-	-C-	-C-	-C-	-C-	-A-	None	None	None
Pin1	CC	MM	PP	NN	FF	HH	GG	X	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Output Channels

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Output Channels

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Unit	none	none	none	none	none	none	none
Conn/BIU1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Conn/BIU2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Conn/BIU3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		GTSI	01/10/00 07:53:31	GTSI	01/17/00 20:39:24

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable										
NoMemLock										
ManEnable										
NoOvrFlash										
NoOvrNext										
OmitInDelay										
ResetMaxPres										
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	0	0	0	0	0	0	0	0	0
DwellExt	0	0	0	0	0	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	WT/TS Overlaps	GTSI	01/17/00 18:35:32	awrigley	12/04/06 12:41:04

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent			X										X		X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent							X													
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X							X								
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X														
Secondary Parent																				
Mod Phase						X														

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent						X										X				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X		X	X														
Ped			X			X														
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	GPS Coordination	trf_ctrl	08/23/01 16:25:18	awrigley	02/14/06 09:50:17

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	0	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	07/30/03 12:20:08	unknown	08/05/03 16:51:00

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Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Kings Crossing @ W. Lake Houston
Created By	awrigley
Created On	12/28/05 09:20:23
Modified By	awrigley
Modified On	12/28/05 09:20:23

NextPhase 1.7.2 Control Params

Control Mode	scheduler
Manual Plan	2: 72" AM Peak
Free Plan	4: 60" Midday
Default Schedule	1: STARDARD SCHEDULE

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	STARDARD SCHEDULE	gtsi	09/20/2001 19:18:28	awrigley	12/28/05 09:21:03

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	1
2	X	6	0		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	15	0		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	1
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	1
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	1
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	Off Peak
2																	AM Peak
3																	Off Peak
4																	Midday (Weekend)
5																	Midday
6																	PM Peak
7																	Off Peak
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	OFF PEAK
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	GTSI
Created On	08/20/99 09:36:32
Modified By	unknown
Modified On	01/17/00 21:05:37

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: STARDARD SCHEDULE	
2		1	1								1: STARDARD SCHEDULE	
3		1	1								1: STARDARD SCHEDULE	
4		1	1								1: STARDARD SCHEDULE	
5		1	1								1: STARDARD SCHEDULE	
6		1	1								1: STARDARD SCHEDULE	
7		1	1								1: STARDARD SCHEDULE	
8		1	1								1: STARDARD SCHEDULE	
9		1	1								1: STARDARD SCHEDULE	
10		1	1								1: STARDARD SCHEDULE	
11		1	1								1: STARDARD SCHEDULE	
12		1	1								1: STARDARD SCHEDULE	
13		1	1								1: STARDARD SCHEDULE	
14		1	1								1: STARDARD SCHEDULE	
15		1	1								1: STARDARD SCHEDULE	
16		1	1								1: STARDARD SCHEDULE	
17		1	1								1: STARDARD SCHEDULE	
18		1	1								1: STARDARD SCHEDULE	
19		1	1								1: STARDARD SCHEDULE	
20		1	1								1: STARDARD SCHEDULE	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: STARDARD SCHEDULE	
2		1	1								1: STARDARD SCHEDULE	
3		1	1								1: STARDARD SCHEDULE	
4		1	1								1: STARDARD SCHEDULE	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: STARDARD SCHEDULE	
6		1	1								1: STARDARD SCHEDULE	
7		1	1								1: STARDARD SCHEDULE	
8		1	1								1: STARDARD SCHEDULE	
9		1	1								1: STARDARD SCHEDULE	
10		1	1								1: STARDARD SCHEDULE	
11		1	1								1: STARDARD SCHEDULE	
12		1	1								1: STARDARD SCHEDULE	
13		1	1								1: STARDARD SCHEDULE	
14		1	1								1: STARDARD SCHEDULE	
15		1	1								1: STARDARD SCHEDULE	
16		1	1								1: STARDARD SCHEDULE	
17		1	1								1: STARDARD SCHEDULE	
18		1	1								1: STARDARD SCHEDULE	
19		1	1								1: STARDARD SCHEDULE	
20		1	1								1: STARDARD SCHEDULE	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	50" Off Peak	GTSI	01/17/00 20:39:24	unknown	12/13/05 10:35:32
2	72" AM Peak	guest	09/04/2002 14:44:09	unknown	12/12/05 07:56:26
3	75" PM Peak	guest	09/04/2002 14:45:59	awrigley	12/09/05 11:41:25
4	60" Midday	Unknown	01/09/2003 11:09:52	awrigley	12/09/05 11:41:46
5	Free	tbales	12/05/05 09:14:23	tbales	12/05/05 09:14:39

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: 3 Phase	1: King's Crossing @ W. Lake Houston	1:

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn							
			1	2	3	4	5	6	7	8
1: King's Crossing @ W. Lake Houston	50	10								

Plan Table 1 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	10	0	15	7	10	0	15	0	0	0	0	0	0	0
CordNom	13	12	0	25	10	15	0	25	0	0	0	0	0	0	0
CordMax	20	20	0	30	14	20	0	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	40	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: 3 Phase	1: King's Crossing @ W. Lake Houston	1:

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Plan Table 2 Params

Config	Cycle Length	Permissive	SpCfcn							
			1	2	3	4	5	6	7	8
1: King's Crossing @ W. Lake Houston	72	15								

Plan Table 2 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	22	15	0	35	10	27	0	35	0	0	0	0	0	0	0
CordMax	30	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	20	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: 3 Phase	1: King's Crossing @ W. Lake Houston	1:

Plan Table 3 Params

Config	Cycle Length	Permissive	SpcFcn							
			1	2	3	4	5	6	7	8
1: King's Crossing @ W. Lake Houston	75	15								

Plan Table 3 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	22	15	0	38	10	27	0	38	0	0	0	0	0	0	0
CordMax	30	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	25	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max1	seconds	1: Phase Data	1: 3 Phase	1: King's Crossing @ W. Lake Houston	1:

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn							
1: King's Crossing @ W. Lake Houston	60	15	1	2	3	4	5	6	7	8

Plan Table 4 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord				X				X												
Hold				X				X												

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	10	0	20	7	15	0	20	0	0	0	0	0	0	0
CordNom	17	13	0	30	10	20	0	30	0	0	0	0	0	0	0
CordMax	20	20	0	40	14	30	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	b	None	None	None	None	None	None	None	None	None	None
Value	15	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
free	max1	seconds	1: Phase Data	1: 3 Phase	1: King's Crossing @ W. Lake Houston	1:

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Plan Table 5 Params

Config	Cycle Length	Permissive	SpCfcn							
			1	2	3	4	5	6	7	8
1: King's Crossing @ W. Lake Houston	0	1								

Plan Table 5 Params

AuxFcn								Prioritor Disable									
1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord																				
Hold																				

Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10
Barrier	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	11	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	3 Phase	GTSI	08/20/99 09:36:34	unknown	08/17/05 09:27:48
2	WBLT Lead/EBLT Lag	guest	09/04/2002 14:47:59	Unknown	01/09/2003 11:09:25
3	EBLT Lead/WBLT Lag	guest	09/04/2002 14:48:27	Unknown	01/09/2003 11:09:38

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 2 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0

Sequence Table 2 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0

Sequence Table 3 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On
1	King's Crossing @ W. Lake Houston	GTSI	01/17/00 18:35:32

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Overlap Table Info

Table	Modified By	Modified On
1	unknown	10/18/05 14:04:15

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
AutoExt																				

Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.2	3.2	3.2	3.2	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	1.1	1.1	1.1	1.1	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	5	5	5	5	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	GTSI	01/17/00 20:39:24	jclark	05/09/11 11:12:00

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl				X				X												
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt	X				X															
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest				X				X												
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck	X	X			X	X														

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	5	0	15	5	5	0	15	0	0	3	0	0	0	3
Passage	2.0	2.0	0.0	3.0	2.0	2.0	0.0	3.0	0.0	0.0	1.0	0.0	0.0	0.0	1.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	15	20	0	50	15	20	0	50	0	0	7	0	0	0	7
Max2	20	30	0	50	20	30	0	50	0	0	7	0	0	0	7
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	3.5	3.5	3.0	4.5	3.5	3.5	3.0	4.5	0.0	0.0	3.5	0.0	0.0	0.0	3.5
RedClr	0.5	0.5	0.0	1.5	0.5	0.5	0.0	1.5	1.0	0.0	0.5	0.0	0.0	0.0	0.5
Walk	0	4	0	4	0	4	0	4	0	0	0	0	0	0	0
PedClr	0	7	0	12	0	7	0	12	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	Table 01
Created By	gtsi
Created On	09/20/2001 19:19:28
Modified By	gtsi
Modified On	09/21/2001 16:29:35

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1										ExtPlan	0	
2										ExtPlan	0	
3										ExtPlan	0	
4										ExtPlan	0	
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
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91										ExtPlan	0	
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93										ExtPlan	0	
94										ExtPlan	0	
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96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	10/18/05 10:55:55	trf_ctrl	10/18/05 10:55:55

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable										
Service Phase	None	None	None	None	None	None	None	None	None	None
Override Next										
Lockout Same	0	0	0	0	0	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On
1	King's Crossing @ W. Lake Houston	GTSI	01/17/00 20:39:23

Active Config Table Info

Table	Modified By	Modified On
1	Unknown	01/09/2003 11:11:40

Active Config Table 1 Params

Phase	1: Phase Config
Overlap	1: Overlap Configuration
Preempt	1: Fire Preempts
Cabinet	1: King's Crossing @ W. Lake Houston
Detector	1: C.O.H.332 DETECTORS
Coordination	1: COORD. CONFIG.
Peer	<TABLE 0 DOES NOT EXIST>

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Config	GTSI	01/17/00 18:35:33	Unknown	01/09/2003 11:03:34

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	nbl	sbt	ebf	wbt	sbl	nbt	none	none	none	none	none	none	none

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Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	none	none	none	none	none	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	none	none	none	none	none	none	none	none	none	none

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup		X				X														
YelStart		X				X														
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz				X				X												
FlsEntry																				
FlsExit																				
NoCall											X				X					
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,11,4,b
2	5,6,a,15,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	C.O.H.332 DETECTORS	GTSI	08/24/99 16:13:16	unknown	03/26/07 13:53:39

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable			X	X	X	X	X	X			X	X									X	X	X
Call			X	X			X	X			X	X									X	X	X
Extend			X	X	X	X	X	X			X	X									X	X	X
AddInit																							
Term																							
RedLok																							
YelLok											X	X											

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X	X	X			X	X																
Call	X					X	X																
Extend	X	X	X			X	X																
AddInit																							
Term																							
RedLok																							
YelLok						X	X																

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Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable				X	X													
Call				X	X													
Extend																		
AddInit																		
Term																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	2	2	11	11	11	11	0	0	4	4	0	0	0
SwitchPhs	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	6	6	15	15	15	15	0	0	8	8
SwitchPhs	0	0	0	0	0	0	0	5	5	5	5	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	10.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	1	5	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X	X	X																			

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	5	6	7	8	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

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Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

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Cabinet Table Info

Table	Description	Created By	Created On
1	King's Crossing @ W. Lake Houston	GTSI	08/20/99 09:36:31

Cabinet Table Info

Table	Modified By	Modified On
1	awrigley	03/29/06 14:57:16

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	63	63	76	47	16	58	17	41
Chan1	7	5	0	0	0	0	0	0	0	0	0
Chan2	8	6	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	None	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	66	78	49	18	60	62	55	19	40	44
Chan1	0	1	0	0	0	0	0	23	25	0	0
Chan2	0	2	0	0	0	0	0	24	26	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	None	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	19	0	0
Chan2	0	0	0	0	0	0	0	0	20	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

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Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	2	3
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Preempt	Preempt	Preempt	Preempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	54	75	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	54	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or	Or

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Preempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	None	None	None	None	None	None	None	None	None	None
Pin	52	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	61	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	Or	And	And	And	Or	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	Veh	Veh	None	None	None	None	None	None	None	None	None
FctnIndx	50	51	0	0	0	0	0	0	0	0	0
Device	Virt	Virt	None	None	None	None	None	None	None	None	None
Pin	2	6	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	phase	ovl	phase	ovl	phase	ovl	phase	ped	ped	ped
FctnIndx	1	2	3	4	2	6	4	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	ovl	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	phson	phson	none	none	none	none	none	none	none	none	none
FctnIndx	2	6	0	0	0	0	0	0	0	0	0
Device1	Virt	Virt	None	None	None	None	None	None	None	None	None
Pin1	2	6	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Fire Preempts	GTSI	01/10/00 07:53:31	Unknown	01/09/2003 11:12:56

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash		X	X	X						
NoOvrNext										
OmitInDelay										
ResetMaxPres		X	X	X	X					
ReturnInStep										

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

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Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X			X									
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

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Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X											X					
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	2.0	2.0	2.0	2.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	0	0	0	0	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Configuration	GTSI	01/17/00 18:35:32	Unknown	01/09/2003 11:11:54

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X										X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X									X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X	X									X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X																
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COORD. CONFIG.	trf_ctrl	08/23/01 16:25:18	awrigley	03/29/06 14:57:02

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Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	TOD	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
								2

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		trf_ctrl	10/18/05 10:55:55	trf_ctrl	10/18/05 10:55:55

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

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Control Table Info

Description	Northpark@ RockSprings
Created By	KGBOSI
Created On	04/08/10 11:45:01
Modified By	aescoba
Modified On	01/25/80 05:07:09

NextPhase 1.7.5 Control Params

Control Mode	remote
Manual Plan	1: 100" Off Peak (new)
Free Plan	1: 100" Off Peak (new)
Default Schedule	1: Kingwood Schedule

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Kingwood Schedule	KGBOSI	04/08/10 11:45:01	vtorres	10/24/11 09:28:23

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1		5	0	X	X	X	X	X	X	X	sched	1
2		5	30		X	X	X	X	X		sched	2
3		9	0		X	X	X	X	X		sched	1
4		9	0	X						X	sched	4
5		11	0		X	X	X	X	X		sched	4
6		14	30		X	X	X	X	X		sched	3
7		19	30	X	X	X	X	X	X	X	sched	1
8	X	0	0								sched	5
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	
2																	
3																	
4																	
5																	
6																	
7																	
8																	Free
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	KGBOSI
Created On	04/08/10 11:45:01
Modified By	KGBOSI
Modified On	04/08/10 11:45:23

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1: Kingwood Schedule	
2		1	1								1: Kingwood Schedule	
3		1	1								1: Kingwood Schedule	
4		1	1								1: Kingwood Schedule	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
5		1	1								1: Kingwood Schedule	
6		1	1								1: Kingwood Schedule	
7		1	1								1: Kingwood Schedule	
8		1	1								1: Kingwood Schedule	
9		1	1								1: Kingwood Schedule	
10		1	1								1: Kingwood Schedule	
11		1	1								1: Kingwood Schedule	
12		1	1								1: Kingwood Schedule	
13		1	1								1: Kingwood Schedule	
14		1	1								1: Kingwood Schedule	
15		1	1								1: Kingwood Schedule	
16		1	1								1: Kingwood Schedule	
17		1	1								1: Kingwood Schedule	
18		1	1								1: Kingwood Schedule	
19		1	1								1: Kingwood Schedule	
20		1	1								1: Kingwood Schedule	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100" Off Peak (new)	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:24:36
2	144" AM Peak (new)	KGBOSI	04/08/10 11:45:23	unknown	10/24/11 09:24:50
3	150" PM Peak (new)	KGBOSI	04/08/10 11:45:23	unknown	10/24/11 09:25:22
4	120" Midday (new)	KGBOSI	04/08/10 11:45:23	unknown	10/24/11 09:25:07
5	Free	vtorres	10/24/11 09:29:15	vtorres	10/24/11 09:29:59

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: ALL LEADING	1: Overlap Parameters	1:

Plan Table 1 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: COH ITS CABINET [340]	100	20																

Plan Table 1 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X																		
Hold		X																		

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Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																			X	
Hold																			X	

Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	7	15	0	15	7	15	0	15	0	0	0	0	0	0	0
CordNom	10	20	0	20	10	20	0	20	0	0	0	0	0	0	0
CordMax	14	30	0	30	14	30	0	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	30	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	55	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: ALL LEADING	1: Overlap Parameters	1:

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Plan Table 2 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: COH ITS CABINET [340]	144	20																

Plan Table 2 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	40	0	20	15	40	0	20	0	0	0	0	0	0	0
CordNom	15	69	0	30	20	64	0	30	0	0	0	0	0	0	0
CordMax	20	80	0	40	30	80	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	80	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: ALL LEADING	1: Overlap Parameters	1:

Plan Table 3 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: COH ITS CABINET [340]	150	20																

Plan Table 3 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	10	40	0	20	20	35	0	20	0	0	0	0	0	0	0
CordNom	15	75	0	30	25	65	0	30	0	0	0	0	0	0	0
CordMax	20	80	0	40	30	70	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	80	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
coord	max2	seconds	1: Phase Data	1: ALL LEADING	1: Overlap Parameters	1:

Plan Table 4 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
1: COH ITS CABINET [340]	120	20	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

Plan Table 4 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

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Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	30	0	15	15	30	0	15	0	0	0	0	0	0	0
CordNom	20	50	0	25	20	50	0	25	0	0	0	0	0	0	0
CordMax	30	60	0	30	30	60	0	30	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	40	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	50	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	60	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	75	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 5 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor
free	max1	seconds	1: Phase Data	1: ALL LEADING	1: Overlap Parameters	1:

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Plan Table 5 Params

Config	Cycle Length	Permissive	SpcFcn								AuxFcn							
			1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
1: COH ITS CABINET [340]	60	10																

Plan Table 5 Params

Prioritor Disable									
1	2	3	4	5	6	7	8	9	10

Plan Table 5 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 5 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 5 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 5 Offsets

Offset	1	2	3	4	5	6	7	8	9	10
Barrier	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0

Plan Table 5 Offsets

Offset	11	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	ALL LEADING	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:24:10

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Overlap Parameters	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:40:46

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

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Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	5	5	5	5	5	5	5	5	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	4.0	4.0	3.5	4.0	4.0	4.0	3.5	4.0	0.0	0.0
RedClear	2.0	2.0	1.5	2.0	2.0	2.0	1.5	2.0	0.0	0.0
Walk	0	5	0	5	0	5	0	5	0	0
PedClear	0	7	0	10	0	7	0	10	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	3.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Phase Data	KGBOSI	04/08/10 11:45:01	vtorres	10/24/11 11:18:14

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit																				
PedOmit																				
MinRcl	X	X		X	X	X		X			X				X					
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt						X														
SimGap																				
RedRest																				
AutoPed		X		X		X		X												
WlkRest						X														
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl				X				X											X	X
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	10	10	0	8	10	4	0	8	0	0	4	0	0	0	4
Passage	3.0	3.0	0.0	3.0	3.0	4.0	0.0	3.0	0.0	0.0	3.0	0.0	0.0	0.0	3.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	20	50	0	25	20	50	0	25	0	0	8	0	0	0	8
Max2	25	75	0	30	25	75	0	30	0	0	8	0	0	0	8
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.0	4.0	0.0	4.0	4.0	4.0	0.0	4.0	0.0	0.0	4.0	0.0	0.0	0.0	4.0
RedClr	2.0	2.0	0.0	2.0	2.0	2.0	0.0	2.0	0.0	0.0	2.0	0.0	0.0	0.0	2.0
Walk	0	5	0	5	0	5	0	5	0	0	0	0	0	0	0
PedClr	0	7	0	10	0	7	0	10	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	4	0	0	0	4	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	3.0	0.0	0.0	0.0	3.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	8	0	0	0	8	0	0
Max2	0	0	0	0	0	0	0	0	8	0	0	0	8	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	4.0	0.0	0.0	0.0	4.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	2.0	0.0	0.0	0.0	2.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	10	10
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	4.0	4.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	50	50
Max2	0	0	0	0	0	0	0	0	75	75
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	4.0	4.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	2.0	2.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	KGBOSI
Created On	04/08/10 11:45:01
Modified By	KGBOSI
Modified On	04/08/10 11:45:25

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	
2	X	X		X						ExtPlan	2	
3	X		X	X						ExtPlan	3	
4		X								ExtPlan	2	
5			X							ExtPlan	3	
6					X					ExtPlan	2	
7						X				ExtPlan	3	
8	X									Schedule	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PinSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		KGBOSI	04/08/10 11:45:01	KGBOSI	04/08/10 11:45:25

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH ITS CABINET [340]	KGBOSI	04/08/10 11:45:01	KGBOSI	04/08/10 14:10:41
2	PEAK	KGBOSI	04/08/10 14:05:54	KGBOSI	04/08/10 14:05:54

Active Config Table 1 Params

Phase	1: Table 001
Overlap	1: COH OVERLAP CONTROLS
Preempt	1: COH PREEMPT CONTROLS
Cabinet	1: COH ITS CABINET [SERIAL]
Detector	1: COH DETECTORS[ITS]
Coordination	1: COH SYNC CONTROLS
Peer	1: COH PEER CONTROLS

Active Config Table 2 Params

Phase	<TABLE 2 DOES NOT EXIST>
Overlap	1: COH OVERLAP CONTROLS
Preempt	1: COH PREEMPT CONTROLS
Cabinet	1: COH ITS CABINET [SERIAL]
Detector	1: COH DETECTORS[ITS]
Coordination	1: COH SYNC CONTROLS
Peer	1: COH PEER CONTROLS

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Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 001	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:30:34

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	nbr	ebr	nbl	sbt	nbr	wbr	sbl	nbt	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup								X												
YelStart								X												
RedStart																				
SecStart																				
Ped		X		X		X		X												
YelFlash																				
AltFlsHz	X	X			X	X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEomit																				

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Phase Config Table 1 Misc

Startup Flash	5
Red Revert	4
FlashClearance	4
Ring Groups	one

Phase Config Table 1 Ring

Ring	Ring
1	5,15,2,1,11,a,40,b
2	39,a,4,24,8,28,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH DETECTORS[ITS]	KGBOSI	04/08/10 11:45:01	vtorres	10/24/11 11:17:14

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X			X	X	X	X	X	X	X	X	X	X	X	X	X	X			X	X	X	X
Call	X			X	X	X	X	X	X	X	X	X	X	X	X	X	X			X	X	X	X
Extend	X			X	X	X	X	X	X	X	X	X	X	X	X	X	X			X	X	X	X
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

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Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable	X	X	X	X	X	X	X	X	X														
Call	X	X	X	X	X	X	X	X	X														
Extend	X	X	X	X	X	X	X	X	X														
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	1	1	2	2	2	2	2	2	3	3	4	4	4	4	4
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	5	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	4	5	5	6	6	6	6	6	6	7	7	8	8	8	8
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	8	8	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable		X		X		X		X															

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	2	0	4	0	6	0	8	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

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Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH ITS CABINET [SERIAL]	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:56:32

Cabinet Table 1 Options

Cabinet Type	ITS_CBD
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN1
Pin	6	7	8	9	10	11	12	13	14	15	16
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2	IN2	IN2
Pin	17	18	19	20	21	6	7	8	9	10	11
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Ped
FctnIndx	23	24	25	26	27	28	29	30	31	32	2
Device	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN2	IN1
Pin	12	13	14	15	16	17	18	19	20	21	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel	Preempt	Preempt	Preempt	Preempt
FctnIndx	4	6	8	1	2	3	4	1	2	3	4
Device	IN1	IN1	IN1	IN1	IN1	IN1	IN1	IN2	IN2	IN2	IN2
Pin	27	28	29	22	23	24	25	28	24	25	26
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	NAnd	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Preempt	Preempt	MCE	Advanc	StopTm	Flash	Start	None	None	None	None
FctnIndx	5	6	1	1	1	1	1	0	0	0	0
Device	IN2	IN2	OUT1	OUT1	OUT1	OUT1	OUT1	None	None	None	None
Pin	27	29	Opto1	Opto2	Opto3	Opto4	Opto4	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	NAnd	And	And	Or	NAnd	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	None	None	None	None
FctnIndx	0	0	0	2	3	4	5	0	0	0	0
Device	None	None	None	IN2	IN2	IN2	IN2	None	None	None	None
Pin	None	None	None	24	25	26	27	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	ovl	ovl	ovlpd	ovl	ovl	ovlpd	ovl	ovl	ovlpd	ovl	ovl
FctnIndx	1	2	2	3	4	4	5	6	6	7	8
Device1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin1	0	3	6	9	12	15	18	21	24	27	30
Device2	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin2	1	4	7	10	13	16	19	22	25	28	31
Device3	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1	OUT1
Pin3	2	5	8	11	14	17	20	23	26	29	32

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ovlpd	none	none	none	none	none	none	none	none	none	none
FctnIndx	8	1	2	3	4	11	12	13	14	0	0
Device1	OUT1	OUT1	OUT1	OUT2	OUT2	OUT2	OUT2	OUT2	OUT2	None	None
Pin1	33	36	39	0	3	6	9	12	15	None	None
Device2	OUT1	OUT1	OUT1	OUT2	OUT2	OUT2	OUT2	OUT2	OUT2	None	None
Pin2	34	37	40	1	4	7	10	13	16	None	None
Device3	OUT1	OUT1	OUT1	OUT2	OUT2	OUT2	OUT2	OUT2	OUT2	None	None
Pin3	35	38	41	2	5	8	11	14	17	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

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Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	14Pak	Off	Off	Off	unused
Det BIU/In SIU	On	Off	Off	Off	Off

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1				X				X	X							
2			X				X	X	X	X						
3							X	X	X	X						
4					X	X		X	X	X						
5						X										
6																
7										X						
8									X							
9																
10											X	X				
11												X				
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On
1	COH PREEMPT CONTROLS	KGBOSI	04/08/10 11:45:01

Preempt Table Info

Table	Modified By	Modified On
1	aescoba	01/24/80 03:19:27

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwIFIs																				
YeIFIs																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X			X															
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

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Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	100	100	100	100	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH OVERLAP CONTROLS	KGBOSI	04/08/10 11:45:01	unknown	10/24/11 09:36:25

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X																			
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent		X			X										X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X			X							X									
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent				X																
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X															
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent	X	X									X									
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent					X			X							X					
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent								X												
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X												
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable	X	X	X	X	X	X	X	X												
Ped		X		X		X		X												
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH SYNC CONTROLS	KGBOSI	04/08/10 11:45:01	KGBOSI	04/08/10 14:10:44

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	0	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

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Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	COH PEER CONTROLS	KGBOSI	04/08/10 11:45:01	KGBOSI	04/08/10 14:10:44

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	0
2	0	0.0.0.0	0
3	0	0.0.0.0	0
4	0	0.0.0.0	0
5	0	0.0.0.0	0
6	0	0.0.0.0	0
7	0	0.0.0.0	0
8	0	0.0.0.0	0

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	trf_ctrl
Created On	08/23/01 14:47:16
Modified By	unknown
Modified On	05/26/11 09:57:40

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	120
Data Filter	X
Key Repeat	X

Admin Dat/Tim DST

Zone	Ctr
DST	X

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NextPhase 1.7.5 Admin Comm

Description	
Created By	unknown
Created On	07/09/04 12:15:43
Modified By	unknown
Modified On	07/28/11 11:21:51

Admin Comm Serial

Port	1	2	3	4
Mode	PMPP	None	None	PMPP
PMPP Address	1	0	0	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	9600	9600	9600	9600
Parity	None	None	None	None
Data Bits	8	8	8	8
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	10.230.188.27
NetMask	255.255.255.0
Gateway	10.230.188.1
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	trf_ctrl
Created On	08/05/03 16:06:17
Modified By	trf_ctrl
Modified On	08/05/03 16:06:17

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	unknown
Created On	11/13/07 14:02:37
Modified By	unknown
Modified On	11/13/07 14:07:29

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
UserId	1	2	3	4																									
Password	1	2	3	4																									
Name	Level 1	Level 2	Level 3	Level 4																									
View	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	1	2	3	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

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Admin Security UserList

User	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62
UserId																																	
Password																																	
Name																																	
View	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4
Edit	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4

Admin Security UserList

User	63	64
UserId		
Password		
Name		
View	4	4
Edit	4	4

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Control Table Info

Description	Northpark @ Russell Palmer #4240
Created By	clupold
Created On	06/02/10 08:09:08
Modified By	clupold
Modified On	07/22/10 11:06:05

NextPhase 1.7.5 Control Params

Control Mode	scheduler
Manual Plan	1: 100
Free Plan	1: 100
Default Schedule	1:

Schedule Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		clupold	06/02/10 08:09:08	clupold	07/22/10 11:15:16

Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
1	X	5	0	X	X	X	X	X	X	X	sched	1
2	X	5	30		X	X	X	X	X		sched	2
3	X	9	0		X	X	X	X	X		sched	1
4	X	9	0	X						X	sched	4
5	X	11	0		X	X	X	X	X		sched	4
6	X	14	30		X	X	X	X	X		sched	3
7	X	19	30	X	X	X	X	X	X	X	sched	1
8		0	0								sched	0
9		0	0								sched	0
10		0	0								sched	0
11		0	0								sched	0
12		0	0								sched	0
13		0	0								sched	0
14		0	0								sched	0
15		0	0								sched	0
16		0	0								sched	0
17		0	0								sched	0
18		0	0								sched	0
19		0	0								sched	0
20		0	0								sched	0
21		0	0								sched	0
22		0	0								sched	0
23		0	0								sched	0
24		0	0								sched	0
25		0	0								sched	0
26		0	0								sched	0
27		0	0								sched	0
28		0	0								sched	0
29		0	0								sched	0
30		0	0								sched	0
31		0	0								sched	0
32		0	0								sched	0
33		0	0								sched	0
34		0	0								sched	0

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Schedule Table 1 Params

Event	Enable	Hour	Minute	Day Of Week							CtrMode	Plan
				Su	Mo	Tu	We	Th	Fr	Sa		
35		0	0								sched	0
36		0	0								sched	0
37		0	0								sched	0
38		0	0								sched	0
39		0	0								sched	0
40		0	0								sched	0

Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
1																	100
2																	AM Peak 144
3																	100
4																	120
5																	120
6																	PM Peak 150
7																	100
8																	
9																	
10																	
11																	
12																	
13																	
14																	
15																	
16																	
17																	
18																	
19																	
20																	
21																	
22																	
23																	
24																	
25																	
26																	
27																	
28																	
29																	
30																	
31																	
32																	
33																	
34																	
35																	

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Schedule Table 1 Params

Event	SpcFcn								AuxFcn								Description
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	
36																	
37																	
38																	
39																	
40																	

Holiday Table Info

Description	
Created By	clupold
Created On	06/02/10 08:09:08
Modified By	clupold
Modified On	06/02/10 08:09:40

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1:	
2		1	1								1:	
3		1	1								1:	

Parameters Holiday Fixed

Holiday	Enable	Month	Day	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
4		1	1								1:	
5		1	1								1:	
6		1	1								1:	
7		1	1								1:	
8		1	1								1:	
9		1	1								1:	
10		1	1								1:	
11		1	1								1:	
12		1	1								1:	
13		1	1								1:	
14		1	1								1:	
15		1	1								1:	
16		1	1								1:	
17		1	1								1:	
18		1	1								1:	
19		1	1								1:	
20		1	1								1:	

Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
1		1	1								1:	
2		1	1								1:	
3		1	1								1:	
4		1	1								1:	
5		1	1								1:	
6		1	1								1:	
7		1	1								1:	

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Parameters Holiday Floating

Holiday	Enable	Month	Week	Day of Week							Schedule	Description
				Su	Mo	Tu	We	Th	Fr	Sa		
8		1	1								1:	
9		1	1								1:	
10		1	1								1:	
11		1	1								1:	
12		1	1								1:	
13		1	1								1:	
14		1	1								1:	
15		1	1								1:	
16		1	1								1:	
17		1	1								1:	
18		1	1								1:	
19		1	1								1:	
20		1	1								1:	

Plan Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	100	clupold	06/02/10 08:09:08	clupold	07/22/10 10:35:06
2	AM Peak 144	clupold	06/02/10 08:09:40	clupold	07/22/10 10:35:24
3	PM Peak 150	clupold	06/02/10 08:09:40	clupold	07/22/10 10:35:36
4	120	clupold	07/22/10 08:11:07	clupold	07/22/10 10:35:50

Plan Table 1 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: phase/timing	1: STANDARD	1: Table 01	1:	1: configuration table

Plan Table 1 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
100	20																										

Plan Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

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Plan Table 1 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	15	35	0	25	0	55	0	25	0	0	0	0	0	0	0
CordNom	20	45	0	35	0	65	0	35	0	0	0	0	0	0	0
CordMax	25	55	0	45	0	75	0	45	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	87	0	0	0	0	0	0	0	0	0	0

Plan Table 1 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 2 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: phase/timing	1: STANDARD	1: Table 01	1:	1: configuration table

Plan Table 2 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
144	25																										

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Plan Table 2 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 2 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 2 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	60	0	35	0	90	0	35	0	0	0	0	0	0	0
CordNom	30	70	0	44	0	100	0	44	0	0	0	0	0	0	0
CordMax	40	80	0	55	0	110	0	55	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	36	0	0	0	0	0	0	0	0	0	0

Plan Table 2 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

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Plan Table 3 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max2	seconds	1: phase/timing	1: STANDARD	1: Table 01	1:	1: configuration table

Plan Table 3 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
150	25																										

Plan Table 3 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 3 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 3 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	60	0	40	0	90	0	40	0	0	0	0	0	0	0
CordNom	30	70	0	50	0	100	0	50	0	0	0	0	0	0	0
CordMax	40	80	0	60	0	110	0	60	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

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Plan Table 3 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	25	0	0	0	0	0	0	0	0	0	0

Plan Table 3 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Plan Table 4 Params

Plan Mode	Max Mode	Splt/Ofs	Phase	Sequence	Overlap	Prioritor	Config
coord	max1	seconds	1: phase/timing	1: STANDARD	1: Table 01	1:	1: configuration table

Plan Table 4 Params

Cycle Length	Permissive	SpcFcn								AuxFcn								Prioritor Disable									
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	9	10
120	20																										

Plan Table 4 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Coord		X				X														
Hold		X				X														

Plan Table 4 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Coord																				
Hold																				

Plan Table 4 Splits

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
CordMin	20	50	0	20	0	80	0	20	0	0	0	0	0	0	0
CordNom	30	60	0	30	0	90	0	30	0	0	0	0	0	0	0
CordMax	40	70	0	40	0	100	0	40	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Splits

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
CordMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Plan Table 4 Splits

Phase	31	32	33	34	35	36	37	38	39	40
CordMin	0	0	0	0	0	0	0	0	0	0
CordNom	0	0	0	0	0	0	0	0	0	0
CordMax	0	0	0	0	0	0	0	0	0	0
PriMin	0	0	0	0	0	0	0	0	0	0
PriMax	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	1	2	3	4	5	6	7	8	9	10	11
Barrier	a	None	None	None	None	None	None	None	None	None	None
Value	30	0	0	0	0	0	0	0	0	0	0

Plan Table 4 Offsets

Offset	12	13	14	15	16	17	18	19	20
Barrier	None	None	None	None	None	None	None	None	None
Value	0	0	0	0	0	0	0	0	0

Sequence Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	STANDARD	clupold	06/02/10 08:09:08	clupold	06/02/10 08:09:41

Sequence Table 1 Sequence

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Sequence Table 1 Sequence

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Sequence	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Overlap Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	clupold	06/02/10 08:09:08	clupold	07/22/10 10:19:09

Overlap Table 1 Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Omit																				
OmitPed																				
RcallPri																				
RcallSec																				
PedRecal																				
PedRecycl																				
WalkRest																				
AutoExt																				

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Overlap Table 1 Timing

Overlap	A	B	C	D	E	F	G	H	I	J
MinGreen	0	0	4	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	3.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	2.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Overlap Table 1 Timing

Overlap	K	L	M	N	O	P	Q	R	S	T
MinGreen	0	0	0	0	0	0	0	0	0	0
MaxGreen	0	0	0	0	0	0	0	0	0	0
GrnClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
YelClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClear	0	0	0	0	0	0	0	0	0	0
Reservice	0	0	0	0	0	0	0	0	0	0

Phase Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	phase/timing	clupold	06/02/10 08:09:08	clupold	07/22/10 10:18:50

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Phase Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Omit			X	X	X		X													
PedOmit																				
MinRcl		X				X														
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt		X		X		X		X										X		
SimGap	X	X				X		X												
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

Phase Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Omit																				
PedOmit																				
MinRcl																				
MaxRcl																				
SftRcl																				
ConSvc																				
PedRcl																				
DualEnt																				
SimGap																				
RedRest																				
AutoPed																				
WlkRest																				
PedRcycl																				
RedLock																				
YelLock																				
NoExtend																				
NoAdded																				
NoReduce																				
NoRange																				
NoMaxLck																				

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Phase Table 1 Timing

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
MinGrn	5	15	0	0	0	15	0	5	0	0	0	0	0	0	0
Passage	2.0	2.5	0.0	0.0	0.0	2.5	0.0	1.5	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	30	45	0	0	0	45	0	50	0	0	0	0	0	0	0
Max2	35	100	0	0	0	100	0	50	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	4.3	4.3	0.0	0.0	0.0	4.3	0.0	3.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	2.0	2.0	0.0	0.0	0.0	2.0	0.0	2.6	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Phase Table 1 Timing

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
MinGrn	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Phase Table 1 Timing

Phase	31	32	33	34	35	36	37	38	39	40
MinGrn	0	0	0	0	0	0	0	0	0	0
Passage	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MinTerm	0	0	0	0	0	0	0	0	0	0
Max1	0	0	0	0	0	0	0	0	0	0
Max2	0	0	0	0	0	0	0	0	0	0
CSMax	0	0	0	0	0	0	0	0	0	0
YelClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
RedClr	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Walk	0	0	0	0	0	0	0	0	0	0
PedClr	0	0	0	0	0	0	0	0	0	0
Added	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
TTR	0	0	0	0	0	0	0	0	0	0
TBR	0	0	0	0	0	0	0	0	0	0
MinGap	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
MxInit	0	0	0	0	0	0	0	0	0	0

External Plan Table Info

Description	
Created By	clupold
Created On	06/02/10 08:09:08
Modified By	clupold
Modified On	06/02/10 08:09:42

Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
1	X			X						ExtPlan	1	Dial 1
2	X	X		X						ExtPlan	2	Dial 2
3	X		X	X						ExtPlan	3	Dial 3
4	X									Schedule	0	Return to Schedule
5										ExtPlan	0	
6										ExtPlan	0	
7										ExtPlan	0	
8										ExtPlan	0	
9										ExtPlan	0	
10										ExtPlan	0	
11										ExtPlan	0	
12										ExtPlan	0	
13										ExtPlan	0	
14										ExtPlan	0	
15										ExtPlan	0	
16										ExtPlan	0	
17										ExtPlan	0	
18										ExtPlan	0	
19										ExtPlan	0	
20										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
21										ExtPlan	0	
22										ExtPlan	0	
23										ExtPlan	0	
24										ExtPlan	0	
25										ExtPlan	0	
26										ExtPlan	0	
27										ExtPlan	0	
28										ExtPlan	0	
29										ExtPlan	0	
30										ExtPlan	0	
31										ExtPlan	0	
32										ExtPlan	0	
33										ExtPlan	0	
34										ExtPlan	0	
35										ExtPlan	0	
36										ExtPlan	0	
37										ExtPlan	0	
38										ExtPlan	0	
39										ExtPlan	0	
40										ExtPlan	0	
41										ExtPlan	0	
42										ExtPlan	0	
43										ExtPlan	0	
44										ExtPlan	0	
45										ExtPlan	0	
46										ExtPlan	0	
47										ExtPlan	0	
48										ExtPlan	0	
49										ExtPlan	0	
50										ExtPlan	0	
51										ExtPlan	0	
52										ExtPlan	0	
53										ExtPlan	0	
54										ExtPlan	0	
55										ExtPlan	0	
56										ExtPlan	0	
57										ExtPlan	0	
58										ExtPlan	0	
59										ExtPlan	0	
60										ExtPlan	0	
61										ExtPlan	0	
62										ExtPlan	0	
63										ExtPlan	0	
64										ExtPlan	0	
65										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
66										ExtPlan	0	
67										ExtPlan	0	
68										ExtPlan	0	
69										ExtPlan	0	
70										ExtPlan	0	
71										ExtPlan	0	
72										ExtPlan	0	
73										ExtPlan	0	
74										ExtPlan	0	
75										ExtPlan	0	
76										ExtPlan	0	
77										ExtPlan	0	
78										ExtPlan	0	
79										ExtPlan	0	
80										ExtPlan	0	
81										ExtPlan	0	
82										ExtPlan	0	
83										ExtPlan	0	
84										ExtPlan	0	
85										ExtPlan	0	
86										ExtPlan	0	
87										ExtPlan	0	
88										ExtPlan	0	
89										ExtPlan	0	
90										ExtPlan	0	
91										ExtPlan	0	
92										ExtPlan	0	
93										ExtPlan	0	
94										ExtPlan	0	
95										ExtPlan	0	
96										ExtPlan	0	
97										ExtPlan	0	
98										ExtPlan	0	
99										ExtPlan	0	
100										ExtPlan	0	
101										ExtPlan	0	
102										ExtPlan	0	
103										ExtPlan	0	
104										ExtPlan	0	
105										ExtPlan	0	
106										ExtPlan	0	
107										ExtPlan	0	
108										ExtPlan	0	
109										ExtPlan	0	
110										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
111										ExtPlan	0	
112										ExtPlan	0	
113										ExtPlan	0	
114										ExtPlan	0	
115										ExtPlan	0	
116										ExtPlan	0	
117										ExtPlan	0	
118										ExtPlan	0	
119										ExtPlan	0	
120										ExtPlan	0	
121										ExtPlan	0	
122										ExtPlan	0	
123										ExtPlan	0	
124										ExtPlan	0	
125										ExtPlan	0	
126										ExtPlan	0	
127										ExtPlan	0	
128										ExtPlan	0	
129										ExtPlan	0	
130										ExtPlan	0	
131										ExtPlan	0	
132										ExtPlan	0	
133										ExtPlan	0	
134										ExtPlan	0	
135										ExtPlan	0	
136										ExtPlan	0	
137										ExtPlan	0	
138										ExtPlan	0	
139										ExtPlan	0	
140										ExtPlan	0	
141										ExtPlan	0	
142										ExtPlan	0	
143										ExtPlan	0	
144										ExtPlan	0	
145										ExtPlan	0	
146										ExtPlan	0	
147										ExtPlan	0	
148										ExtPlan	0	
149										ExtPlan	0	
150										ExtPlan	0	
151										ExtPlan	0	
152										ExtPlan	0	
153										ExtPlan	0	
154										ExtPlan	0	
155										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
156										ExtPlan	0	
157										ExtPlan	0	
158										ExtPlan	0	
159										ExtPlan	0	
160										ExtPlan	0	
161										ExtPlan	0	
162										ExtPlan	0	
163										ExtPlan	0	
164										ExtPlan	0	
165										ExtPlan	0	
166										ExtPlan	0	
167										ExtPlan	0	
168										ExtPlan	0	
169										ExtPlan	0	
170										ExtPlan	0	
171										ExtPlan	0	
172										ExtPlan	0	
173										ExtPlan	0	
174										ExtPlan	0	
175										ExtPlan	0	
176										ExtPlan	0	
177										ExtPlan	0	
178										ExtPlan	0	
179										ExtPlan	0	
180										ExtPlan	0	
181										ExtPlan	0	
182										ExtPlan	0	
183										ExtPlan	0	
184										ExtPlan	0	
185										ExtPlan	0	
186										ExtPlan	0	
187										ExtPlan	0	
188										ExtPlan	0	
189										ExtPlan	0	
190										ExtPlan	0	
191										ExtPlan	0	
192										ExtPlan	0	
193										ExtPlan	0	
194										ExtPlan	0	
195										ExtPlan	0	
196										ExtPlan	0	
197										ExtPlan	0	
198										ExtPlan	0	
199										ExtPlan	0	
200										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
201										ExtPlan	0	
202										ExtPlan	0	
203										ExtPlan	0	
204										ExtPlan	0	
205										ExtPlan	0	
206										ExtPlan	0	
207										ExtPlan	0	
208										ExtPlan	0	
209										ExtPlan	0	
210										ExtPlan	0	
211										ExtPlan	0	
212										ExtPlan	0	
213										ExtPlan	0	
214										ExtPlan	0	
215										ExtPlan	0	
216										ExtPlan	0	
217										ExtPlan	0	
218										ExtPlan	0	
219										ExtPlan	0	
220										ExtPlan	0	
221										ExtPlan	0	
222										ExtPlan	0	
223										ExtPlan	0	
224										ExtPlan	0	
225										ExtPlan	0	
226										ExtPlan	0	
227										ExtPlan	0	
228										ExtPlan	0	
229										ExtPlan	0	
230										ExtPlan	0	
231										ExtPlan	0	
232										ExtPlan	0	
233										ExtPlan	0	
234										ExtPlan	0	
235										ExtPlan	0	
236										ExtPlan	0	
237										ExtPlan	0	
238										ExtPlan	0	
239										ExtPlan	0	
240										ExtPlan	0	
241										ExtPlan	0	
242										ExtPlan	0	
243										ExtPlan	0	
244										ExtPlan	0	
245										ExtPlan	0	

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Parameters External Plan Params

Plan	Enable	PlnSel								CtrlMode	Plan	Description
		Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8			
246										ExtPlan	0	
247										ExtPlan	0	
248										ExtPlan	0	
249										ExtPlan	0	
250										ExtPlan	0	

Prioritor Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		clupold	06/02/10 08:09:08	clupold	06/02/10 08:09:42

Prioritor Table 1 General

Prioritor	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
Service Phase	None	6	2	8	4	None	None	None	None	None
Override Next										
Lockout Same	0	150	150	150	150	0	0	0	0	0
Lockout Diff	0	0	0	0	0	0	0	0	0	0

Active Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	configuration table	clupold	06/02/10 08:09:08	clupold	06/02/10 08:09:56

Active Config Table 1 Params

Phase	1: rings/id's
Overlap	1: Table 01
Preempt	1: premmpts
Cabinet	1: input settings/output settings
Detector	1: Table 01
Coordination	1:
Peer	1:

Phase Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	rings/id's	clupold	06/02/10 08:09:08	clupold	07/22/10 10:19:23

Phase Config Table 1 Phase ID

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Direction	wbl	ebt	nbl	sbt	ebf	wbt	sbl	nbt	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Phase ID

Phase	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

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Phase Config Table 1 Phase ID

Phase	31	32	33	34	35	36	37	38	39	40
Direction	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr	nbr

Phase Config Table 1 Flags

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Startup		X				X														
YelStart		X				X														
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz		X				X														
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Flags

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Startup																				
YelStart																				
RedStart																				
SecStart																				
Ped																				
YelFlash																				
AltFlsHz																				
FlsEntry																				
FlsExit																				
NoCall																				
MCEOmit																				

Phase Config Table 1 Misc

Startup Flash	0
Red Revert	3
FlashClearance	3
Ring Groups	one

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Phase Config Table 1 Ring

Ring	Ring
1	1,2,a,3,4,b
2	5,6,a,7,8,b
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	
17	
18	
19	
20	

Detector Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	clupold	06/02/10 08:09:08	clupold	07/22/10 08:57:43

Detector Table 1 Vehicle Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable	X	X				X		X															
Call	X	X				X		X															
Extend	X	X				X		X															
AddInit																							
Term																							
Type3																							
RedLok																							
YellLok																							

Detector Table 1 Vehicle Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							
Call																							
Extend																							
AddInit																							
Term																							
Type3																							
RedLok																							
YelLok																							

Detector Table 1 Vehicle Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		
Call																		
Extend																		
AddInit																		
Term																		
Type3																		
RedLok																		
YelLok																		

Detector Table 1 Vehicle Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	5	4	7	6	1	8	3	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 Vehicle Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SwitchPhs	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Vehicle Params

Detector	61	62	63	64
Phase	0	0	0	0
SwitchPhs	0	0	0	0
Overlap	0	0	0	0
Delay	0.0	0.0	0.0	0.0
Extend	0.0	0.0	0.0	0.0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Ped Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 Ped Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 Ped Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

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Detector Table 1 Ped Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	2	4	6	8	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Overlap	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Ped Params

Detector	61	62	63	64
Phase	0	0	0	0
Overlap	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 System Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
Enable																							

Detector Table 1 System Flags

Detector	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46
Enable																							

Detector Table 1 System Flags

Detector	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
Enable																		

Detector Table 1 System Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 System Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Phase	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Length	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
NoActiv	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
MaxPres	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ErrCount	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

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Detector Table 1 System Params

Detector	61	62	63	64
Phase	0	0	0	0
Length	0	0	0	0
NoActiv	0	0	0	0
MaxPres	0	0	0	0
ErrCount	0	0	0	0

Detector Table 1 Prioritor Flags

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
Enable		X	X	X	X																
Request		X	X	X	X																
Request Trail																					
Release																					
Release Trail		X	X	X	X																
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63
Enable																					
Request																					
Request Trail																					
Release																					
Release Trail																					
Release On Green																					

Detector Table 1 Prioritor Flags

Detector	64
Enable	
Request	
Request Trail	
Release	
Release Trail	
Release On Green	

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Detector Table 1 Prioritor Params

Detector	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Prioritor	0	2	3	4	5	0	0	0	0	0	0	0	0	0	0
Min Travel	0	12	12	12	12	0	0	0	0	0	0	0	0	0	0
Max Travel	0	25	25	25	25	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	85	85	85	85	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Detector Table 1 Prioritor Params

Detector	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
Prioritor	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Min Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Max Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Avg Travel	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
Max On	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

NextEdit

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Detector Table 1 Prioritor Params

Detector	61	62	63	64
Prioritor	0	0	0	0
Min Travel	0	0	0	0
Max Travel	0	0	0	0
Avg Travel	0	0	0	0
Request Delay	0.0	0.0	0.0	0.0
Release Delay	0.0	0.0	0.0	0.0
Min On	0.0	0.0	0.0	0.0
Min Off	0.0	0.0	0.0	0.0
Max On	0	0	0	0

Cabinet Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	input settings/output settings	clupold	06/02/10 08:09:08	clupold	07/22/10 14:14:54

Cabinet Table 1 Options

Cabinet Type	Caltrans
Verbose Logging	

Cabinet Table 1 Inputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	1	2	3	4	5	6	7	8	9	10	11
Device	C1S	C11S	C1S	C1S	C1S	C1S	C1S	C11S	C1S	C11S	C1S
Pin	56	15	39	43	63	76	47	16	58	17	41
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	12	13	14	15	16	17	18	19	20	21	22
Device	C1S	C1S	C1S	C1S	C11S	C1S	C1S	C1S	C11S	C1S	C1S
Pin	45	65	78	49	18	60	62	55	19	40	44
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh	Veh
FctnIndx	23	24	25	26	27	28	29	30	31	32	33
Device	C1S	C1S	C1S	C11S	C1S	C11S	C1S	C1S	C1S	C1S	C1S
Pin	64	77	48	20	57	21	42	46	66	79	50
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	Veh	Veh	Veh	Ped	Ped	Ped	Ped	PlnSel	PlnSel	PlnSel	PlnSel
FctnIndx	34	35	36	1	2	3	4	1	2	3	4
Device	C11S	C1S	C1S	C1S	C1S	C1S	C1S	C11S	C11S	C11S	C11S
Pin	22	59	61	67	69	68	70	10	11	12	13
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	Advanc	MCE	Flash	StopTm	None	None	TimSet	Prempt	Prempt	Prempt	Prempt
FctnIndx	1	1	1	1	0	0	1	2	3	4	5
Device	C1S	C1S	C1S	C1S	None	None	C1S	C1S	C1S	C1S	C1S
Pin	80	53	81	82	None	None	51	71	73	72	74
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	Prempt	None	None	None	None	None	None	None	None	None	None
FctnIndx	6	0	0	0	0	0	0	0	0	0	0
Device	C1S	C11S	C11S	C11S	C11S	None	None	None	None	None	None
Pin	52	10	11	12	13	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	NAnd	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	None	None	None	PriLow	PriLow	PriLow	PriLow	Door	None	None	None
FctnIndx	0	0	0	2	3	4	5	1	0	0	0
Device	None	None	None	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin	None	None	None	71	73	72	74	54	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

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Cabinet Table 1 Inputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	None	None	None	None	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And	And	And	And	And

Cabinet Table 1 Inputs

Channel	122	123	124	125	126	127	128
FctnType	None	None	None	None	None	None	None
FctnIndx	0	0	0	0	0	0	0
Device	None	None	None	None	None	None	None
Pin	None	None	None	None	None	None	None
Chan1	0	0	0	0	0	0	0
Chan2	0	0	0	0	0	0	0
Logic	And	And	And	And	And	And	And

Cabinet Table 1 Outputs

Channel	1	2	3	4	5	6	7	8	9	10	11
FctnType	phase	phase	phase	ovl	phase	phase	phase	phase	ped	ped	ped
FctnIndx	1	2	3	3	5	6	7	8	2	4	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	16	12	7	4	32	29	24	21	10	2	27
Device2	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None
Pin2	17	13	8	5	33	30	25	22	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin3	18	15	9	6	34	31	26	23	11	3	28

Cabinet Table 1 Outputs

Channel	12	13	14	15	16	17	18	19	20	21	22
FctnType	ped	ovl	ovl	none	ovl	specl	specl	specl	specl	specl	specl
FctnIndx	8	1	2	3	4	1	2	3	4	5	6
Device1	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S	C1S
Pin1	19	97	94	88	85	91	101	93	83	100	84
Device2	None	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin2	None	98	95	89	86	None	None	None	None	None	None
Device3	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None	None
Pin3	20	99	96	90	87	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	23	24	25	26	27	28	29	30	31	32	33
FctnType	aux	aux	aux	aux	detrst	fl/wd	none	none	none	none	none
FctnIndx	1	2	3	4	1	1	0	0	0	0	0
Device1	C1S	C1S	C1S	C1S	C1S	C1S	None	None	None	None	None
Pin1	35	37	36	38	102	103	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	34	35	36	37	38	39	40	41	42	43	44
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	45	46	47	48	49	50	51	52	53	54	55
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	56	57	58	59	60	61	62	63	64	65	66
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	67	68	69	70	71	72	73	74	75	76	77
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	78	79	80	81	82	83	84	85	86	87	88
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	89	90	91	92	93	94	95	96	97	98	99
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	100	101	102	103	104	105	106	107	108	109	110
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	111	112	113	114	115	116	117	118	119	120	121
FctnType	none	none	none	none	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None	None	None	None	None

Cabinet Table 1 Outputs

Channel	122	123	124	125	126	127	128
FctnType	none	none	none	none	none	none	none
FctnIndx	0	0	0	0	0	0	0
Device1	None	None	None	None	None	None	None
Pin1	None	None	None	None	None	None	None
Device2	None	None	None	None	None	None	None
Pin2	None	None	None	None	None	None	None
Device3	None	None	None	None	None	None	None
Pin3	None	None	None	None	None	None	None

Cabinet Table 1 Devices

Device	1	2	3	4	5
FIO Addr/TF BIU/Out SIU	auto	none	unused	unused	unused
Det BIU/In SIU	unused	unused	unused	unused	unused

Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																

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Cabinet Table 1 Permissive

Channel	Channel															
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																
28																
29																
30																
31																

Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
1																
2																
3																
4																
5																
6																
7																
8																
9																
10																
11																
12																
13																
14																
15																
16																
17																
18																
19																
20																
21																
22																
23																
24																
25																
26																
27																

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Cabinet Table 1 Permissive

Channel	Channel															
	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32
28																
29																
30																
31																

Preempt Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	premmpts	clupold	06/02/10 08:09:08	clupold	07/22/10 08:59:31

Preempt Table 1 Preempt Flags

Preempt	1	2	3	4	5	6	7	8	9	10
Enable		X	X	X	X					
NoMemLock		X	X	X	X					
ManEnable										
NoOvrFlash										
NoOvrNext		X	X	X	X					
OmitInDelay										
ResetMaxPres										
ReturnInStep		X	X	X	X					

Table 1 Phase Flags Preempt 1

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 1

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 2

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell	X					X														
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 2

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 3

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell		X																		
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit		X				X														

Table 1 Phase Flags Preempt 3

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 4

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell								X												
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 4

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 5

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell				X																
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit				X				X												

Table 1 Phase Flags Preempt 5

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 6

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 6

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 7

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 8

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 8

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 9

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

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Table 1 Phase Flags Preempt 10

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Table 1 Phase Flags Preempt 10

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Entry1																				
Entry2																				
Dwell																				
InitDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				
Recall																				
Exit																				

Preempt Table 1 Timing

Preempt	1	2	3	4	5	6	7	8	9	10
MinPres	0.0	1.0	1.0	1.0	1.0	0.0	0.0	0.0	0.0	0.0
Delay	0	0	0	0	0	0	0	0	0	0
StrtGrn	0	0	0	0	0	0	0	0	0	0
StrtWlk	0	0	0	0	0	0	0	0	0	0
StrtPClr	0	0	0	0	0	0	0	0	0	0
Ent1Grn	0	0	0	0	0	0	0	0	0	0
Ent2Grn	0	0	0	0	0	0	0	0	0	0
MinDwell	0	10	10	10	10	0	0	0	0	0
DwellExt	0	2	2	2	2	0	0	0	0	0
FreeAft	0	0	0	0	0	0	0	0	0	0
MinDiff	0	0	0	0	0	0	0	0	0	0
MinSame	0	0	0	0	0	0	0	0	0	0
MaxPres	0	60	60	60	60	0	0	0	0	0
MinDura	0	10	10	10	10	0	0	0	0	0

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Table 1 Overlap Flags Preempt 1

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 2

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 3

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 4

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 5

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 6

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 7

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 8

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 9

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

Table 1 Overlap Flags Preempt 10

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
NoEntry1																				
NoEntry2																				
NoDwell																				
DwellPed																				
NoDwlFls																				
YelFls																				

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Overlap Config Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1	Table 01	clupold	06/02/10 08:09:08	clupold	07/22/10 10:39:01

Table 1 Phase Flags Overlap A

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap A

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap B

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent								X												
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap C

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap D

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap D

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap E

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap F

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap G

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap H

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap H

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap I

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap J

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap K

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap K

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap L

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap M

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap N

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap O

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap P

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap Q

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap R

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

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Table 1 Phase Flags Overlap R

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap S

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Table 1 Phase Flags Overlap T

Phase	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Primary Parent																				
Secondary Parent																				
Mod Phase																				

Overlap Config Table 1 Overlap Flags

Overlap	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T
Enable			X																	
Ped																				
YelFlash																				
AltFlsHz																				
Option1																				

Overlap Config Table 1 Params

Overlap	A	B	C	D	E	F	G	H	I	J
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

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Overlap Config Table 1 Params

Overlap	K	L	M	N	O	P	Q	R	S	T
Type	2	2	2	2	2	2	2	2	2	2
Red	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Yellow	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Green	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm	Norm
Option1	0	0	0	0	0	0	0	0	0	0

Coordination Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		clupold	06/02/10 08:09:08	clupold	07/22/10 14:16:18

Coordination Table 1 Params

Transition Mode	Max Dwell Time	Cycle Sync Mode	Hour	Minute
Shortest	20	OVR	5	0

Coordination Table 1 Params

HWSyncLines								HWSyncLength
line1	line2	line3	line4	line5	line6	line7	line8	
		X	X	X				3

Peer Table Info

Table	Description	Created By	Created On	Modified By	Modified On
1		clupold	06/02/10 08:09:08	clupold	06/02/10 08:09:59

Peer Table 1 Sources

Peer	Peer	IP Address	Timeout
1	0	0.0.0.0	30
2	0	0.0.0.0	30
3	0	0.0.0.0	30
4	0	0.0.0.0	30
5	0	0.0.0.0	30
6	0	0.0.0.0	30
7	0	0.0.0.0	30
8	0	0.0.0.0	30

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Peer Table 1 Functions

Peer	Peer	SrcFcn	SrcIdx	InFcn	InIdx	FailOn
1	0	None	0	None	0	
2	0	None	0	None	0	
3	0	None	0	None	0	
4	0	None	0	None	0	
5	0	None	0	None	0	
6	0	None	0	None	0	
7	0	None	0	None	0	
8	0	None	0	None	0	
9	0	None	0	None	0	
10	0	None	0	None	0	
11	0	None	0	None	0	
12	0	None	0	None	0	
13	0	None	0	None	0	
14	0	None	0	None	0	
15	0	None	0	None	0	
16	0	None	0	None	0	
17	0	None	0	None	0	
18	0	None	0	None	0	
19	0	None	0	None	0	
20	0	None	0	None	0	
21	0	None	0	None	0	
22	0	None	0	None	0	
23	0	None	0	None	0	
24	0	None	0	None	0	
25	0	None	0	None	0	
26	0	None	0	None	0	
27	0	None	0	None	0	
28	0	None	0	None	0	
29	0	None	0	None	0	
30	0	None	0	None	0	
31	0	None	0	None	0	
32	0	None	0	None	0	

NextPhase 1.7.5 Admin Front Panel

Description	
Created By	rreevey
Created On	03/19/14 15:50:15
Modified By	rreevey
Modified On	03/19/14 15:50:15

Admin Front Panel Front Panel Cfg

FP Type	AUTO
BL Timeout	1
Data Filter	
Key Repeat	

Admin Dat/Tim DST

Zone	Pac
DST	

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NextPhase 1.7.5 Admin Comm

Description	
Created By	rreevey
Created On	03/19/14 15:50:15
Modified By	rreevey
Modified On	03/19/14 15:50:15

Admin Comm Serial

Port	1	2	3	4
Mode	None	None	None	None
PMPP Address	1	1	1	1
IP Address	0.0.0.0	0.0.0.0	0.0.0.0	0.0.0.0
Default Gateway				
Baud Rate	1200	1200	1200	1200
Parity	None	None	None	None
Data Bits	7	7	7	7
Stop Bits	1	1	1	1
Flow Control	None	None	None	None
CTS Delay	0	0	0	0
RTS Extention	0	0	0	0

Admin Comm Ethernet

Address	0.0.0.0
NetMask	0.0.0.0
Gateway	0.0.0.0
DHCP	

Admin Comm VME

Address	0.0.0.0
Dest	0.0.0.0

Admin Comm Routes

Routes	1	2
Dest	0.0.0.0	0.0.0.0
Gateway	0.0.0.0	0.0.0.0
Routes	0.0.0.0	0.0.0.0

Admin Comm NTP

IP Address	0.0.0.0
RefTimeHour	0
RefTimeMinute	0
PeriodHour	0
PeriodMinute	0
Retries	0

Admin Comm Opticom

Opti	1	2	3	4	5	6	7	8	9	10
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

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Admin Comm Opticom

Opti	11	12	13	14	15	16	17	18	19	20
Fcn	None	None	None	None	None	None	None	None	None	None
Idx	0	0	0	0	0	0	0	0	0	0
Port	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1	sp1
Addr	0	0	0	0	0	0	0	0	0	0
Chan	A	A	A	A	A	A	A	A	A	A

Admin Comm Timeout

Unit Backup Timeout Seconds	

Admin Comm AdaptHosts

AdaptHost	0.0.0.0
RhodesHost	0.0.0.0
PredictHost	0.0.0.0

NextPhase 1.7.5 Admin Controller Info

Description	
Created By	rreevey
Created On	03/19/14 15:50:15
Modified By	rreevey
Modified On	03/19/14 15:50:15

Admin Controller Info Controller Info

ID	0
Name	
Loc1	
Loc2	
Note1	
Note2	
Note3	
Note4	

NextPhase 1.7.5 Admin Security

Description	
Created By	rreevey
Created On	03/19/14 15:50:15
Modified By	rreevey
Modified On	03/19/14 15:50:15

Admin Security Setup

Password	
LogTimeout	0
FailTimeout	0

Admin Security UserList

User	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34
UserId																																		
Password																																		
Name																																		
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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Admin Security UserList

User	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64
UserId																														
Password																														
Name																														
View	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Edit	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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